# M38 - DVT

11/16/05

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

| REV | ZONE | ECN | DESCRIPTION OF CHANGE | CK APPD DATE | ENG APPD DATE |
|---|---|---|---|---|---|
| 09 | | 400372 | ENGINEERING RELEASED | 09/16/05 | 06/22/04 |



DIMENSIONS ARE IN MILLIMETERS

XX ±

X.XX ±

X.XXX ±

ANGLES ±

DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION

METRIC

DRAFTER / DESIGN CK / ENG APPD / MFG APPD / QA APPD / DESIGNER / RELEASE / SCALE NONE

MATERIAL/FINISH NOTED AS APPLICABLE

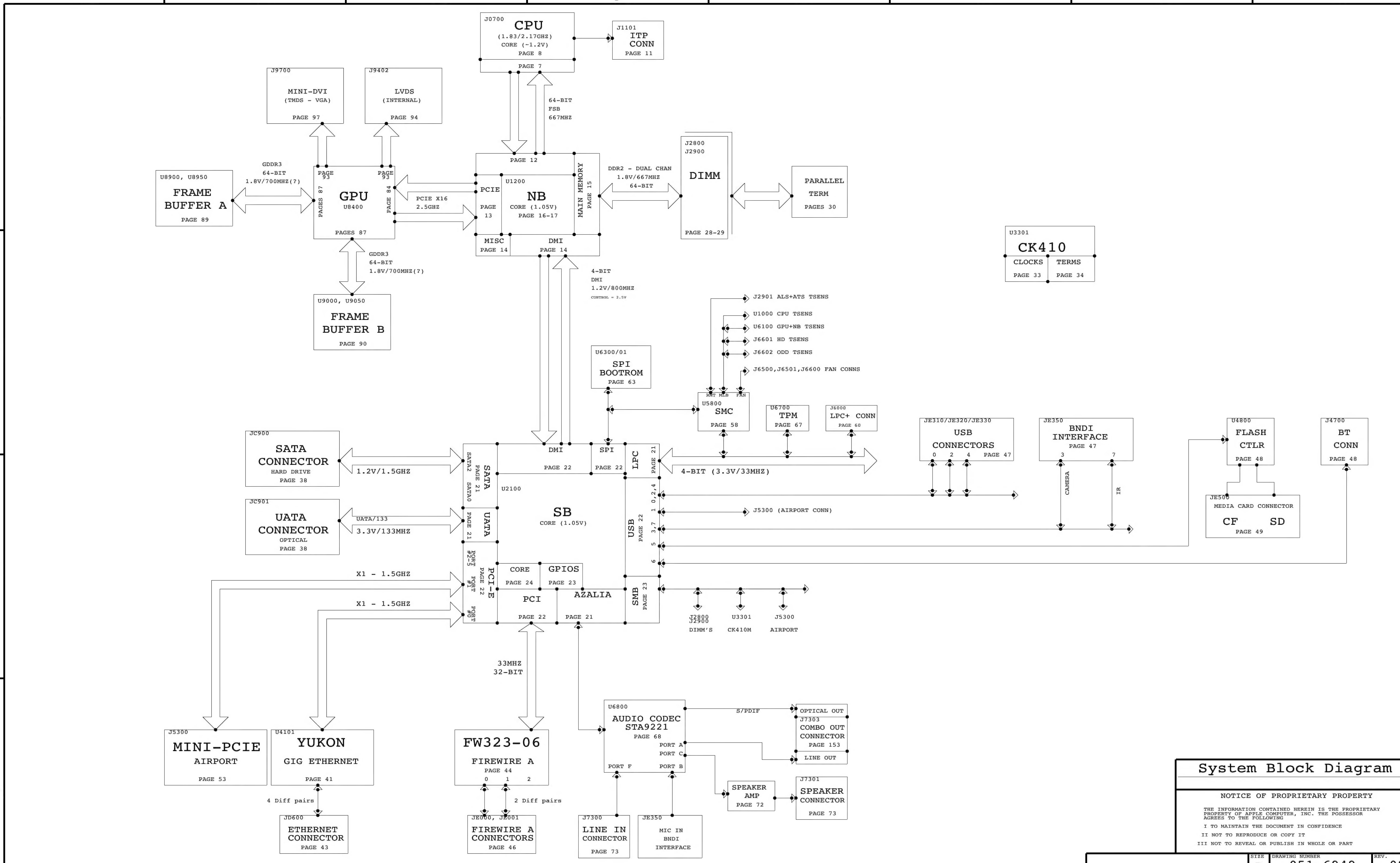
Apple Computer Inc.



TITLE: SCHEM,M38

SIZE: D — DRAWING NUMBER: 051-6949 — REV. 09

SHT 1 OF 111




# System Block Diagram

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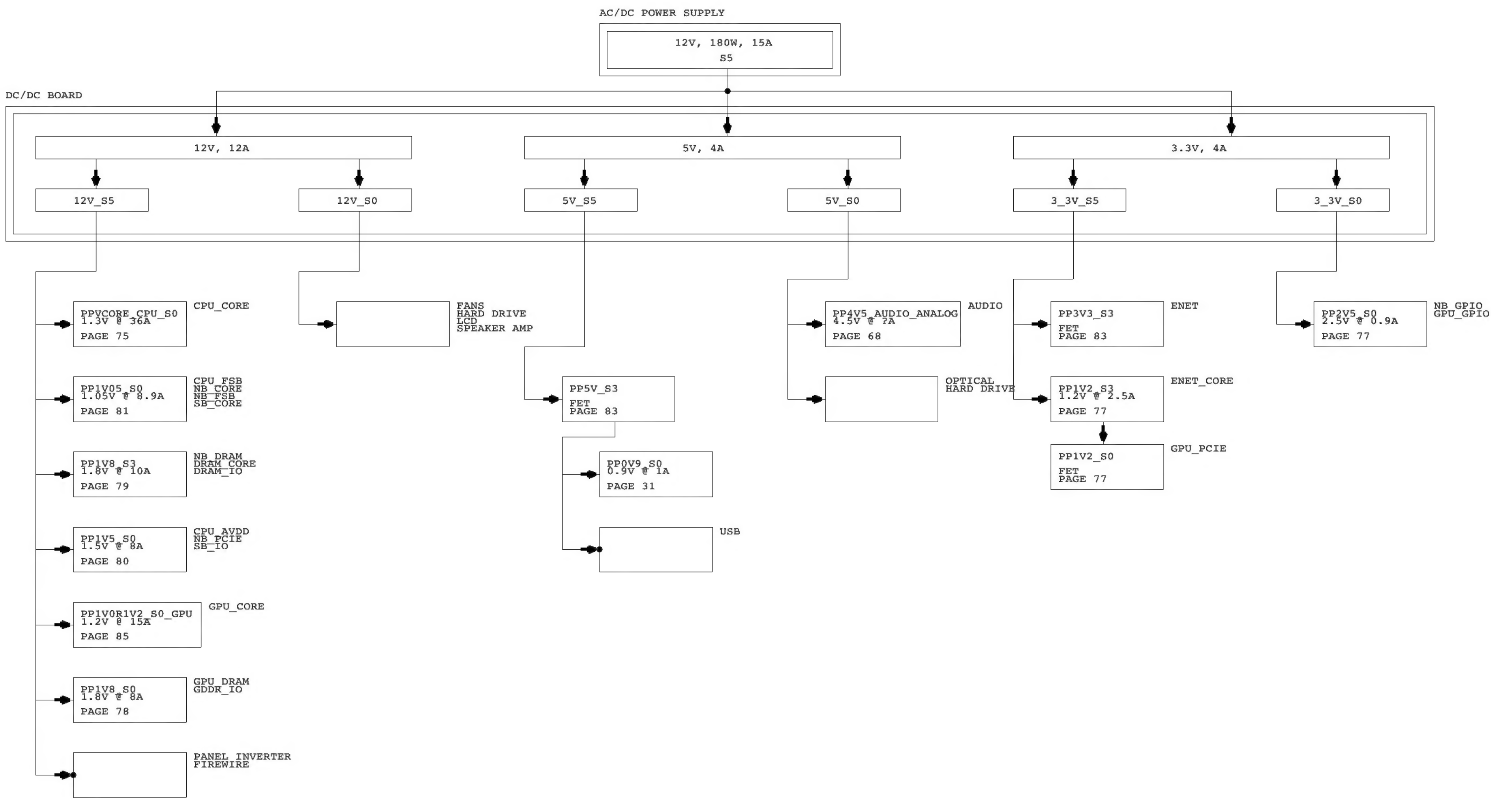
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# Power Block Diagram



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 3 OF 111 | |

## COMMON

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 511S0025 | 1 | IC,CPU-SKT,479BGA | J0700 | CRITICAL | |
| 338S0269 | 1 | IC,945GM,NORTHBRIDGE | U1200 | CRITICAL | |
| 343S0385 | 1 | IC,SB,652BGA | U2100 | CRITICAL | |
| 742-0048 | 1 | BAT,COIN,3V,220MAH,CR2032 | BT2600 | CRITICAL | |
| 359S0101 | 1 | IC,CY28445-5,CLK GEN,68PIN QFN | U3301 | CRITICAL | |
| 338S0270 | 1 | IC,88E8053,GIGABIT ENET XCVR,64P QFN,NO | U4101 | CRITICAL | |
| (335S0382) 341S1797 | 1 | IC,ENET LAN ROM | U4102 | CRITICAL | |
| 338S0279 | 1 | IC,FW32306,1394A LINK,TQFP | U4400 | CRITICAL | |
| 338S0274 | 1 | IC,SMC,HS8/2116,BLANK | U5800 | CRITICAL | |
| 341S1789 | 1 | IC,TPM,TSSOP,28P | U6700 | CRITICAL | LEMENU |
| 353S1235 | 1 | IC,CPU VREG,IMVP,TWO PHASE | U7500 | CRITICAL | |
| 338S0266 | 1 | IC,ATI,M56P,GRAFIX CTLR,880BGA,LF | U8400 | CRITICAL | ATI_B24 |
| 338S0305 | 1 | IC,ATI,M56P,GRAFIX CTLR,880BGA,LF | U8400 | CRITICAL | ATI_A24 |
| 128S0078 | 3 | CAP,EL,AL,330UF,20%,16V,10X12.7MM,SMD,LF | C7517,C7518,C7910 | CRITICAL | |

| PART NUMBER | ALTERNATE FOR PART NUMBER | BOM OPTION | REF DES | COMMENTS: |
|---|---|---|---|---|
| 126S0096 | 126S0076 | | C7801 | SANYO W16CE680KX 680UF 16V LF |
| 126S0086 | 126S0078 | | C699,C940,C1900,C1901,C1968 | SANYO W6CE330FS 330UF 6.3V LF |
| 128S0080 | 128S0078 | | C7517,C7518,C7910 | SANYO 16SVP330M 330UF 16V SMD LF |
| 197S0177 | 197S0020 | | Y4101 | XTAL,25MHZ,50PPM,16PF,3.2X2.5 SMD,LF |
| 338S0302 | 338S0266 | | U8400 | IC,ATI,M36D,GRAFIX CTLR,88PBGA,LF |

## M38

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 051-6949 | 1 | PCB,SCHEM,MLB,M38 | SCH1 | | 17_INCH_LCD |
| 820-1919 | 1 | PCB,FAB,MLB,M38 | MLB1 | | 17_INCH_LCD |
| (335S0384) 341T0003 | 1 | EFI ROM,M38 | U6301 | CRITICAL | 17_INCH_LCD |
| 337S3241 | 1 | M38/M39 LOW-SPEED CPU (QINY) | CPU | CRITICAL | CPU_M38 |
| 337S3242 | 1 | M00-SPEED CPU (QINZ) | CPU | CRITICAL | CPU_M00 |

## M39

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 051-6950 | 1 | PCB,SCHEM,MLB,M39 | SCH1 | | 20_INCH_LCD |
| 820-1888 | 1 | PCB,FAB,MLB,M39 | MLB1 | | 20_INCH_LCD |
| (335S0384) 341T0004 | 1 | EFI ROM,M39 | U6301 | CRITICAL | 20_INCH_LCD |
| 337S3243 | 1 | M39 HI-SPEED CPU (QHJJ) | CPU | CRITICAL | CPU_M39 |

## M38 / M39

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 333S0354 | 4 | IC,SGRAM,GDDR3,8MX32,700MHZ,136FBGA | U8900,U8950,U9000,U9050 | CRITICAL | ATI_FB_128M_SAMSUNG |
| 333S0358 | 4 | IC,SGRAM,GDDR3,8MX32,700MHZ,136FBGA | U8900,U8950,U9000,U9050 | CRITICAL | ATI_FB_128M_HYNIX |

## M39 - CTO

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 333S0350 | 4 | IC,SGRAM,GDDR3,16MX32,700MHZ,136FBGA | U8900,U8950,U9000,U9050 | CRITICAL | ATI_FB_256M_SAMSUNG |
| 333S0351 | 4 | IC,SGRAM,GDDR3,16MX32,700MHZ,136FBGA | U8900,U8950,U9000,U9050 | CRITICAL | ATI_FB_256M_HYNIX |

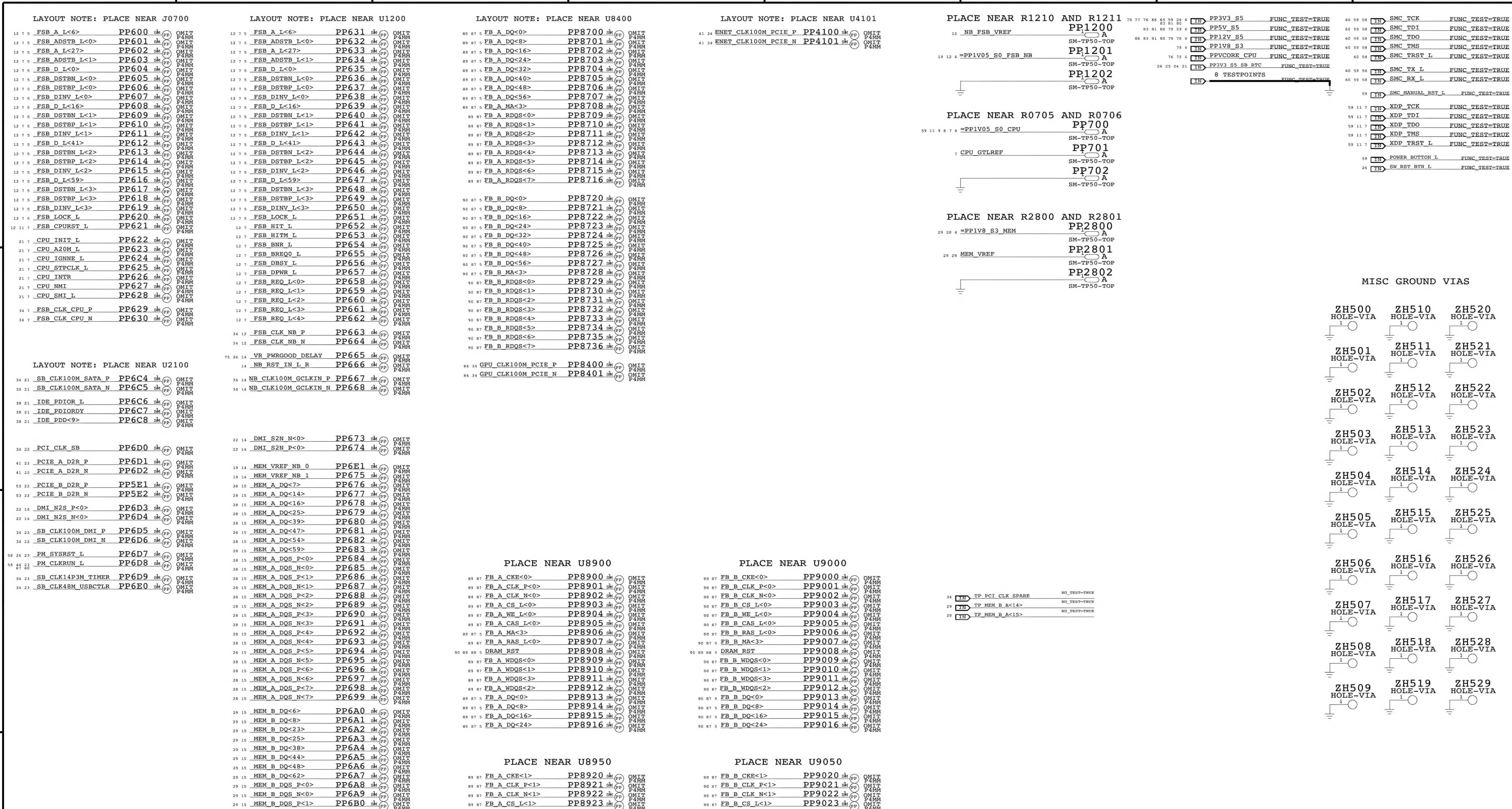
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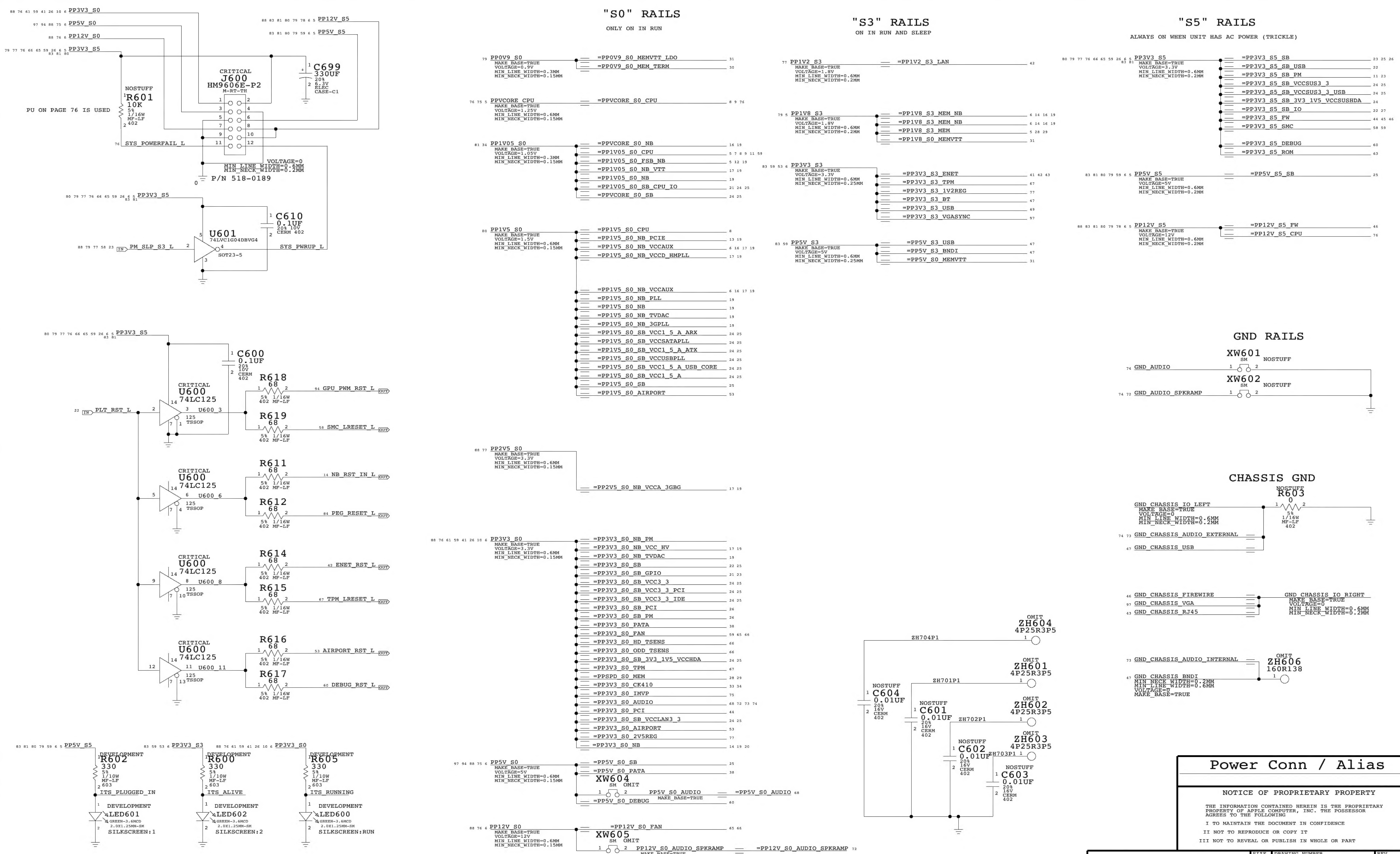



# FUNC TEST 1 OF 2



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Power Conn / Alias

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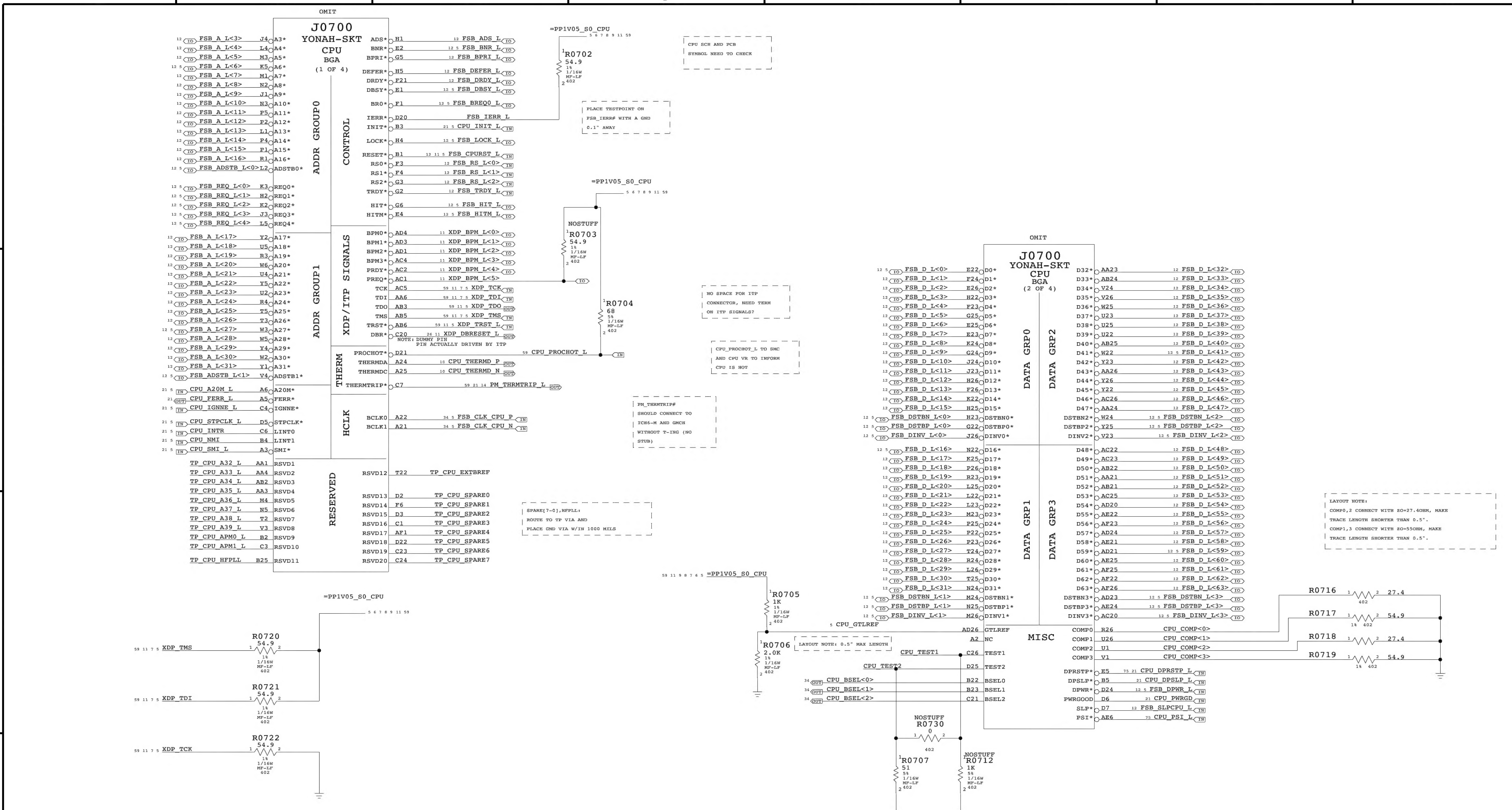
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CPU 1 OF 2-FSB

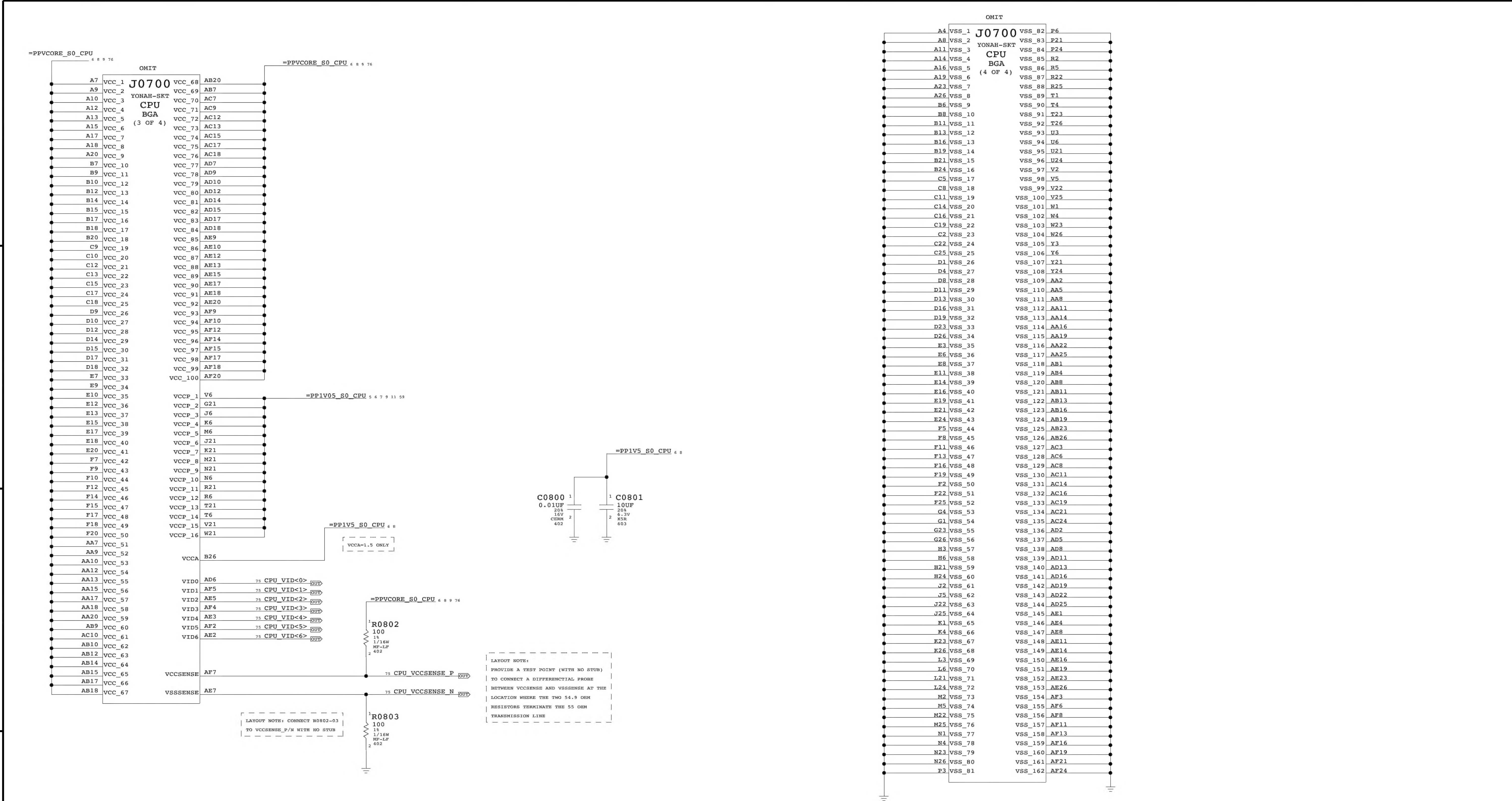
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# CPU 2 OF 2-PWR/GND

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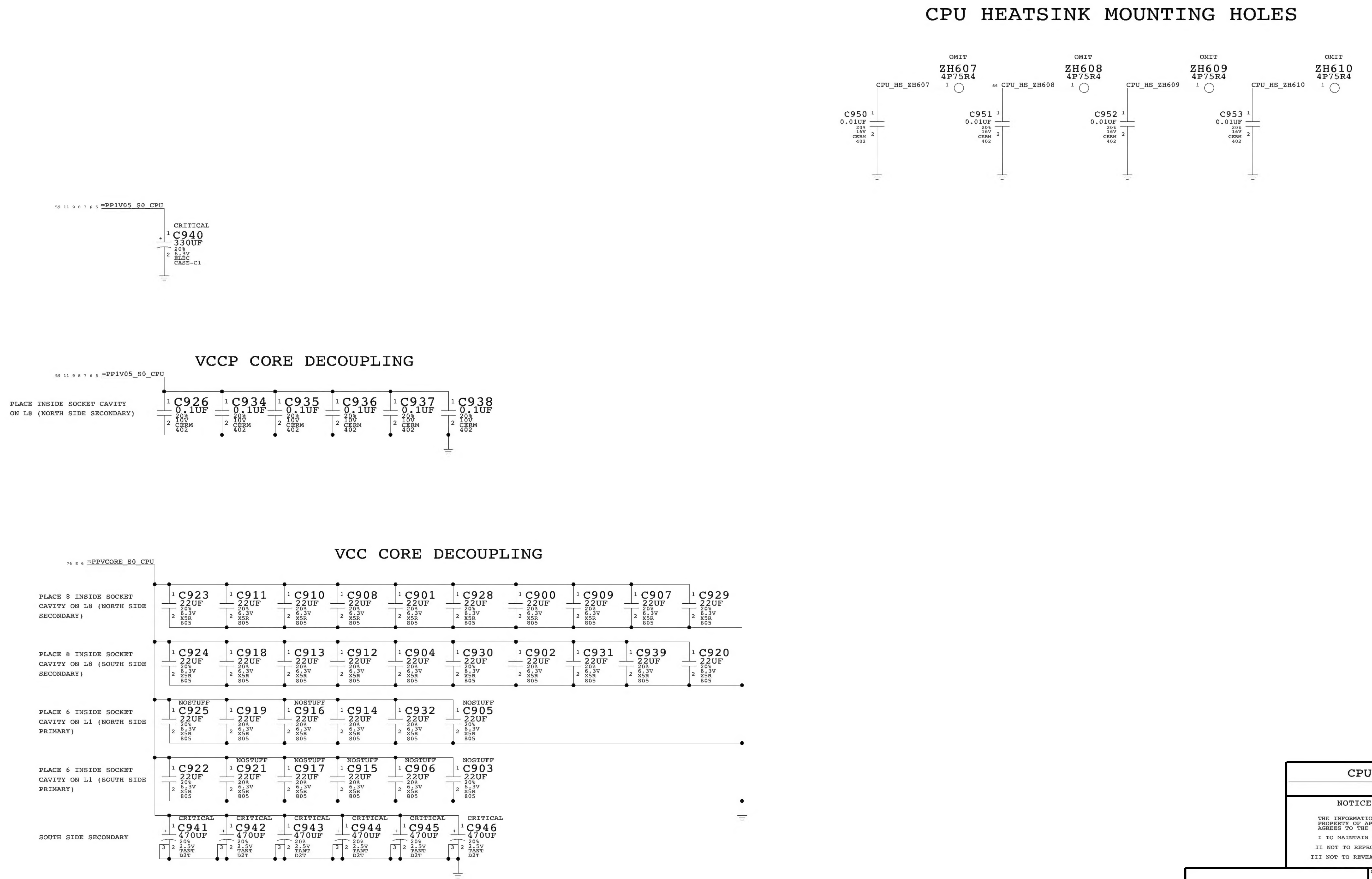
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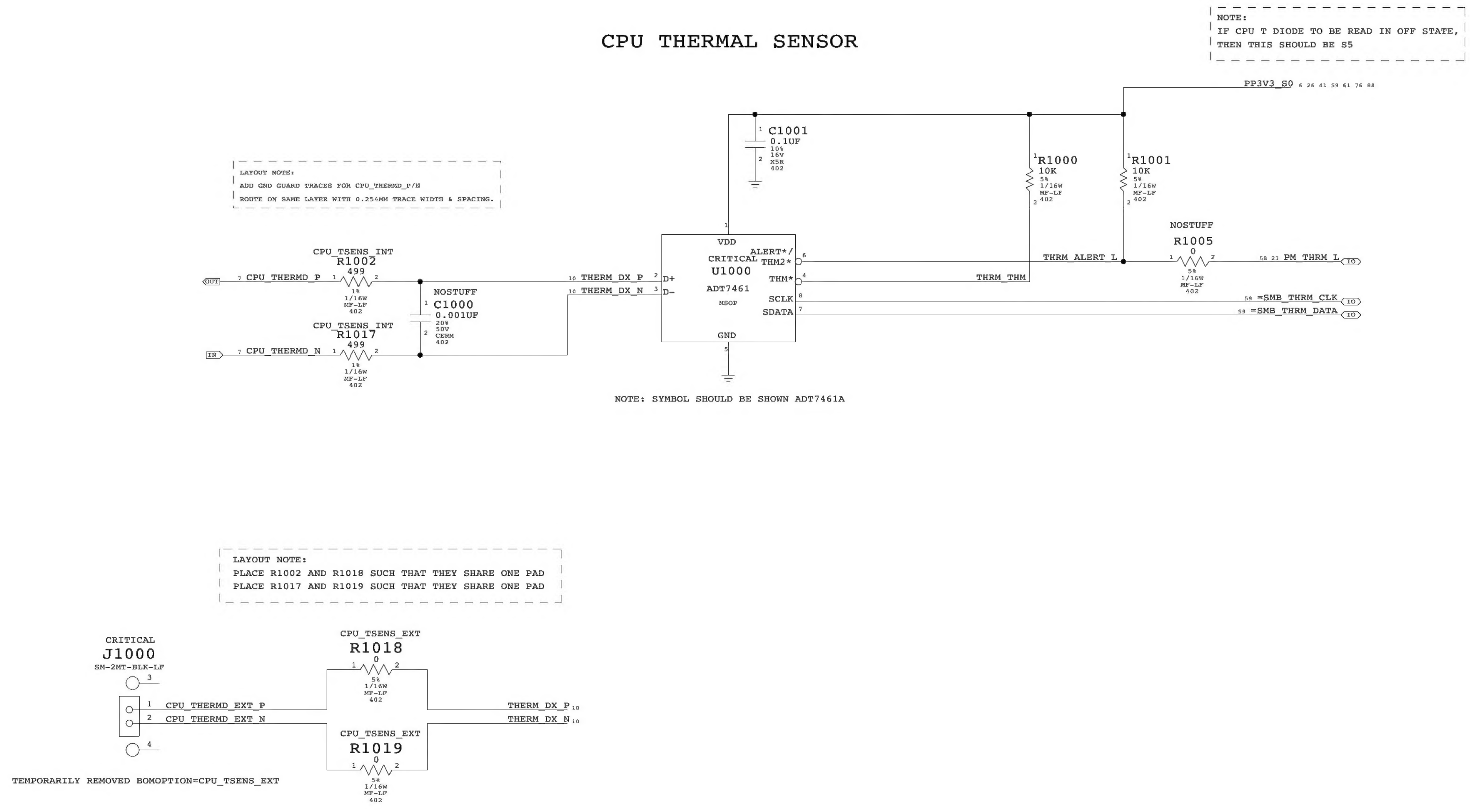

CPU DECAPS & VID<>



APPLE COMPUTER INC.

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# CPU THERMAL SENSOR




CPU TEMP SENSOR

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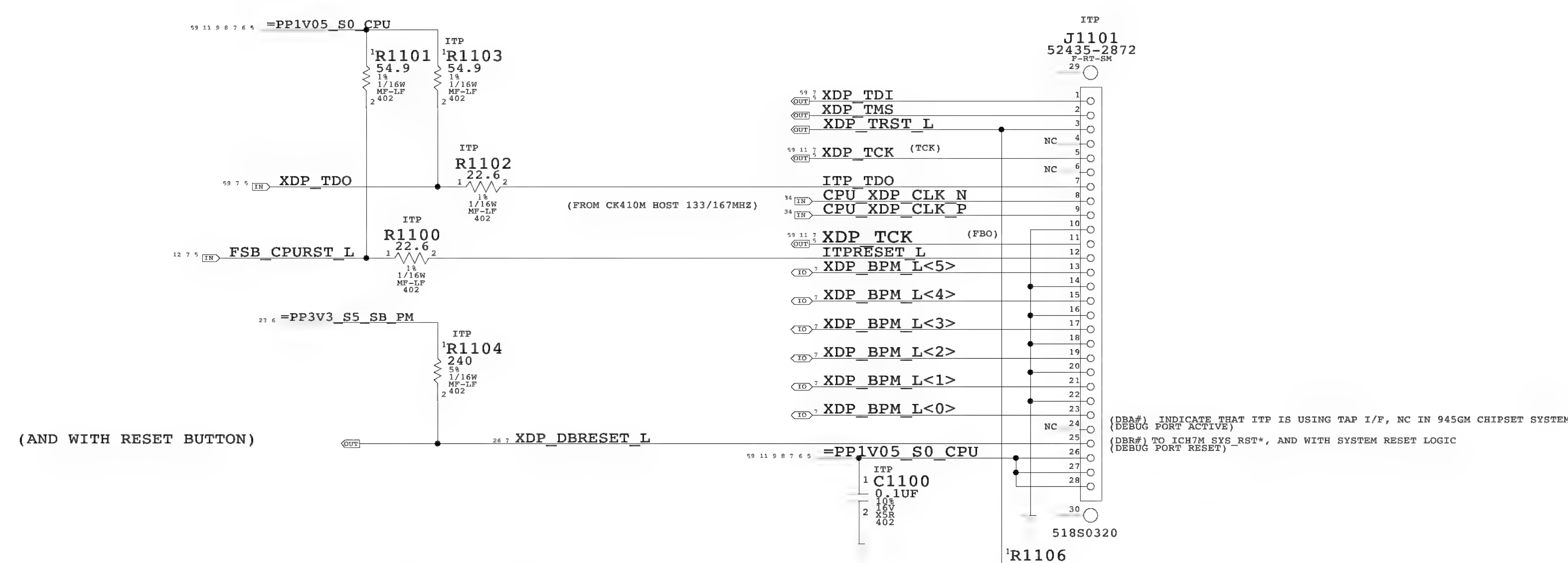
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|---|---|---|
| D | 051-6949 | 09 |

SCALE: NONE | SHT 10 | OF 111

# CPU ITP700FLEX DEBUG SUPPORT




ITP TCK SIGNAL LAYOUT NOTE:
ROUTE THE TCK SIGNAL FROM ITP700FLEX CONNECTOR'S TCK PIN TO CPU'S TCK PIN AND THEN FORK BACK FROM CPU TCK PIN AND ROUTE BACK TO ITP700FLEX CONNECTOR'S FBO PIN.

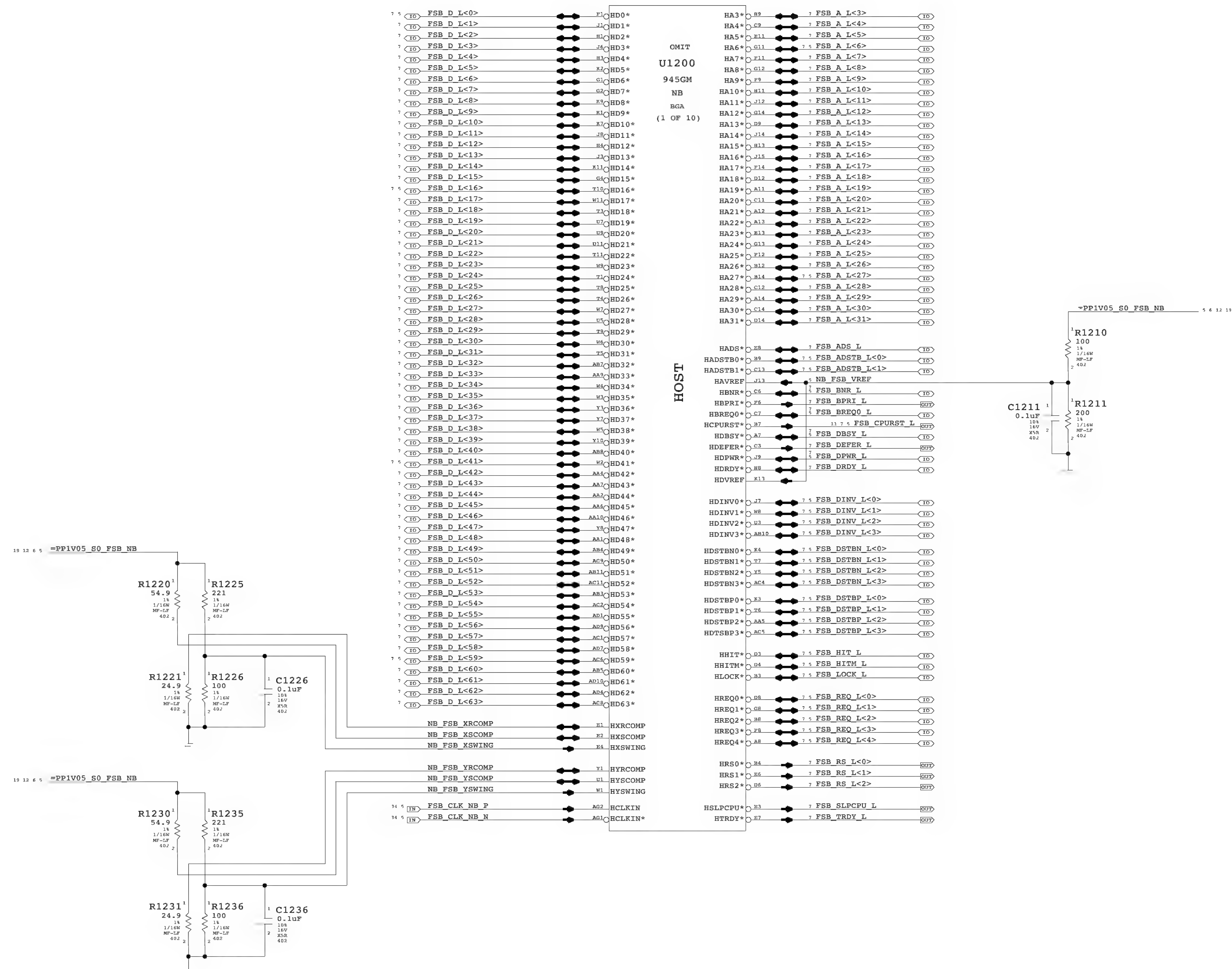
CPU ITP700FLEX DEBUG

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# NB CPU Interface

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| SCALE NONE | SHT 12 | OF 111 |

LVDS Disable

Can leave all signals NC if LVDS is not implemented
Tie VCC_TXLVDS and VCCA_LVDS to GND. If SDVO is used
VCCD_LVDS must remain powered with proper decoupling.
Otherwise, tie VCCD_LVDS to GND also.

TV-Out Signal Usage:

Composite: DACA only
S-Video: DACB & DACC only
Component: DACA, DACB & DACC

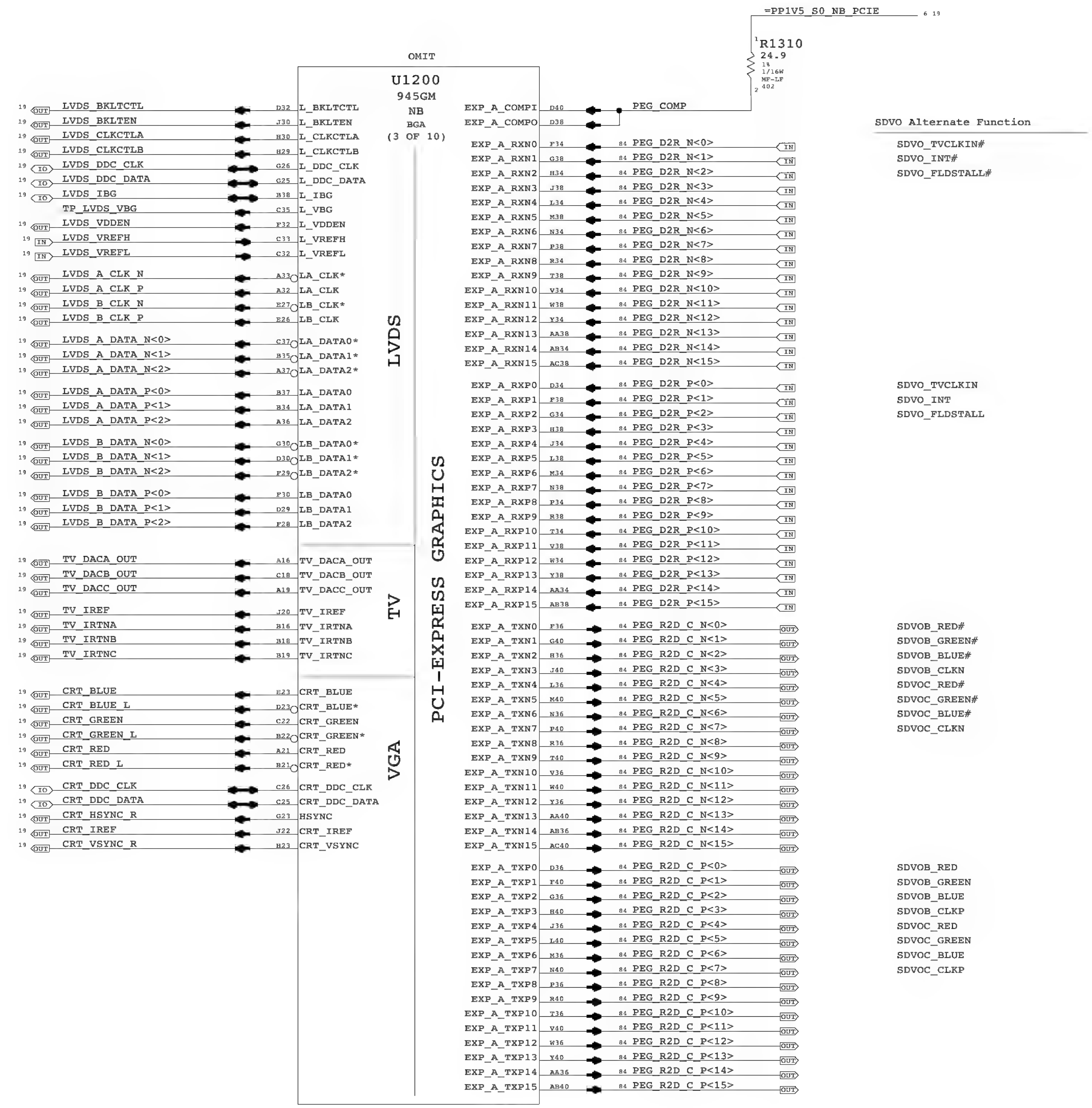
Unused DAC outputs must remain powered, but can omit
filtering components. Unused DAC outputs should
connect to GND through 75-ohm resistors.

TV-Out Disable

Tie DACx_OUT, IRTNx, and IREF to 1.5V power rail.
Tie VCCD_TVDAC, VCCD_QTVDAC, VCCA_TVDACx, and
VCCA_TVBG to 1.5V power rail. Tie VSSA_TVBG to GND.

CRT Disable

Tie R/R#/G/G#/B/B# and IREF to VCC Core rail, tie
HSYNC and VSYNC to GND. Tie VCCA_CRTDAC to VCC Core
rail, and tie VSSA_CRTDAC and VCC_SYNC to GND.




NB PEG / Video Interfaces

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NB Misc Interfaces

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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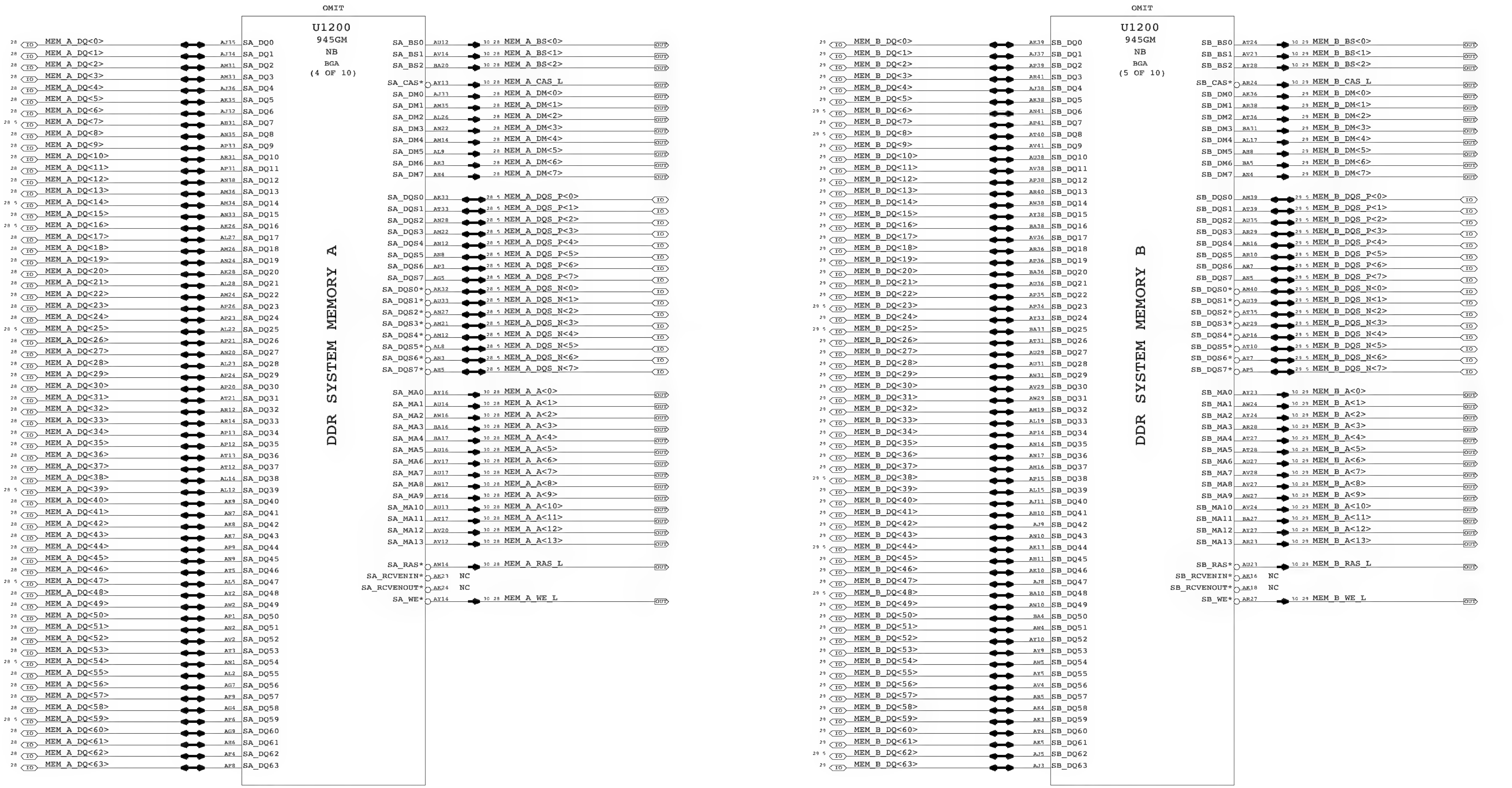
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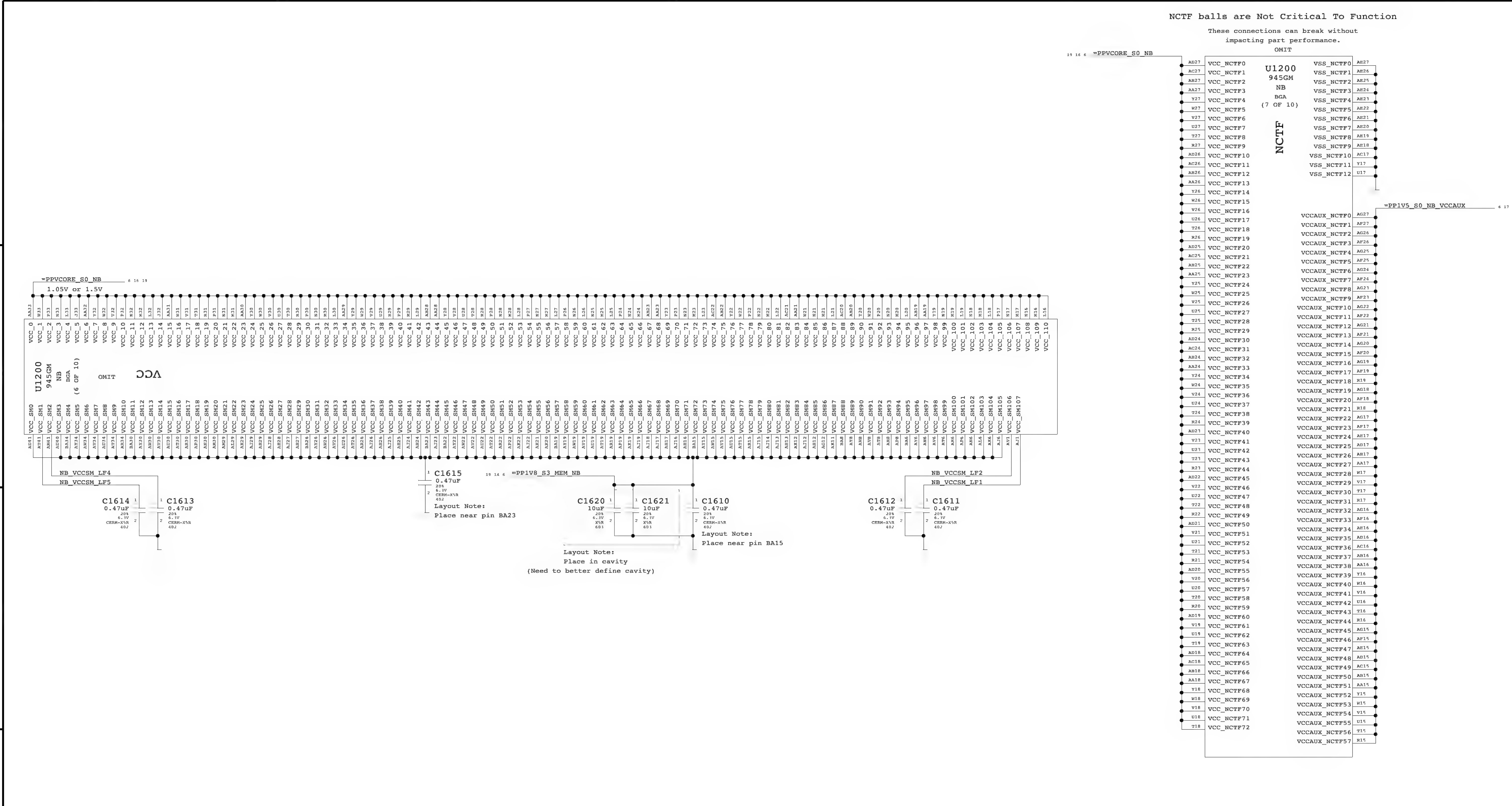

# NB DDR2 Interfaces

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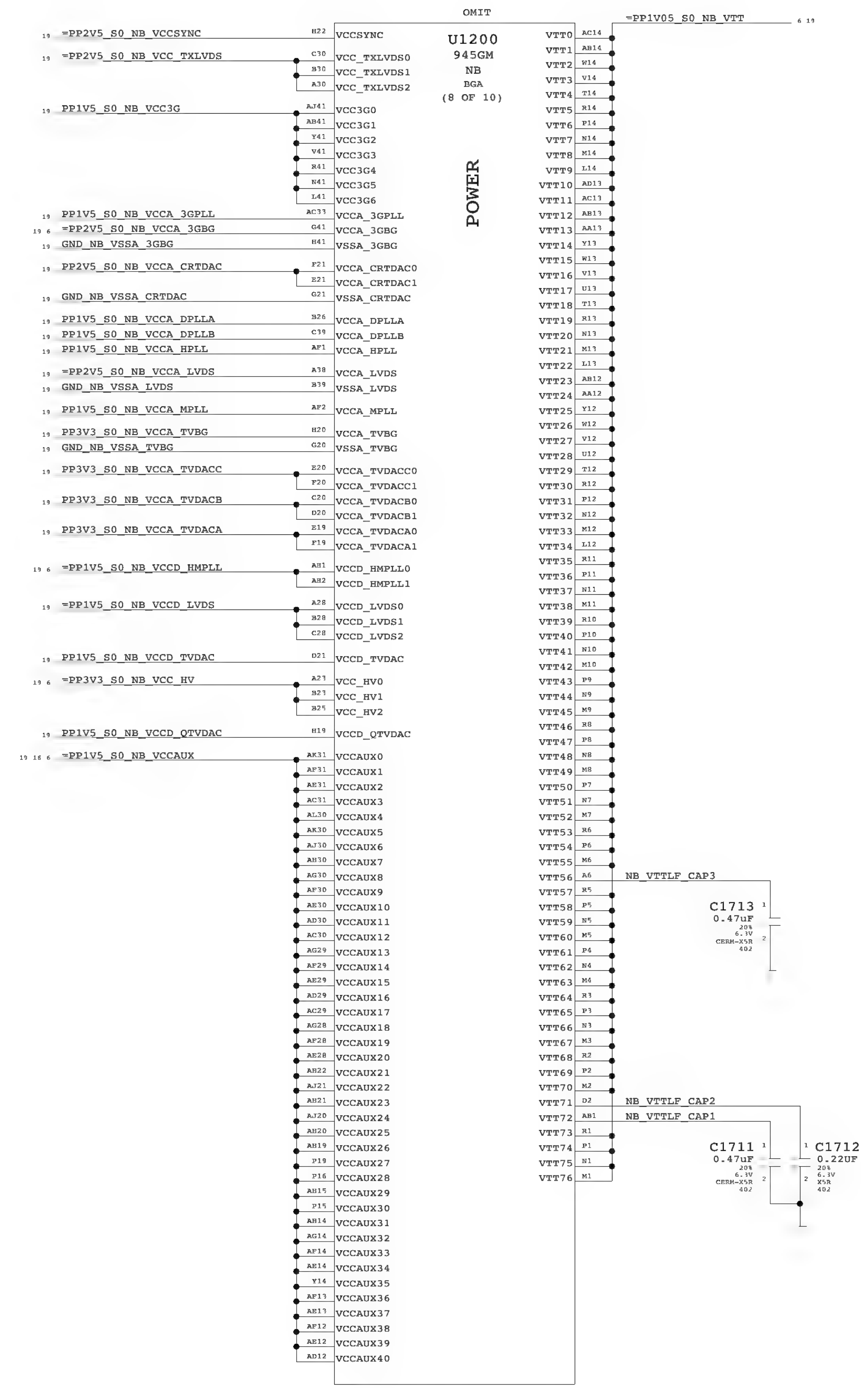

NB Power 1

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)



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# NB Power 2

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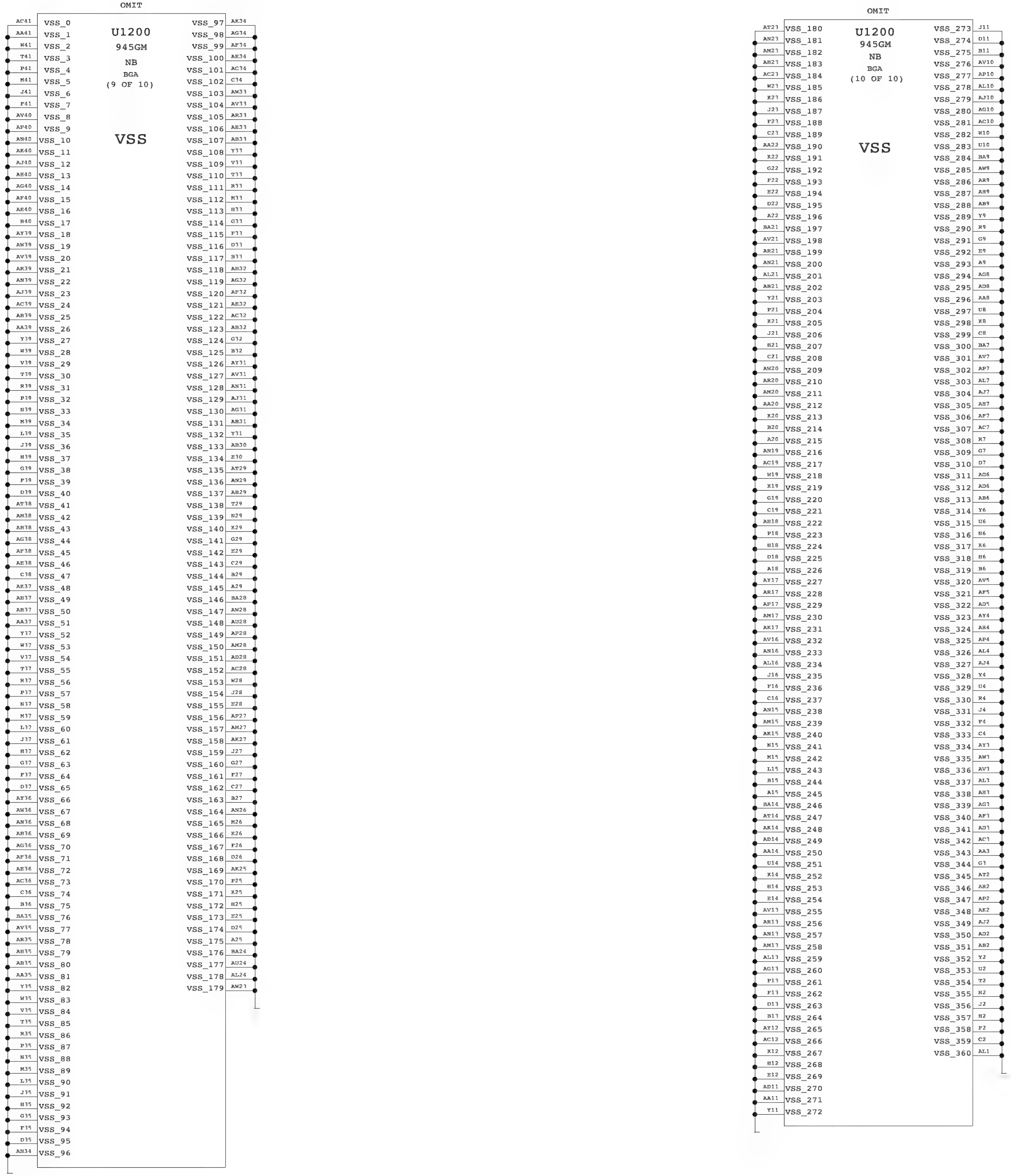
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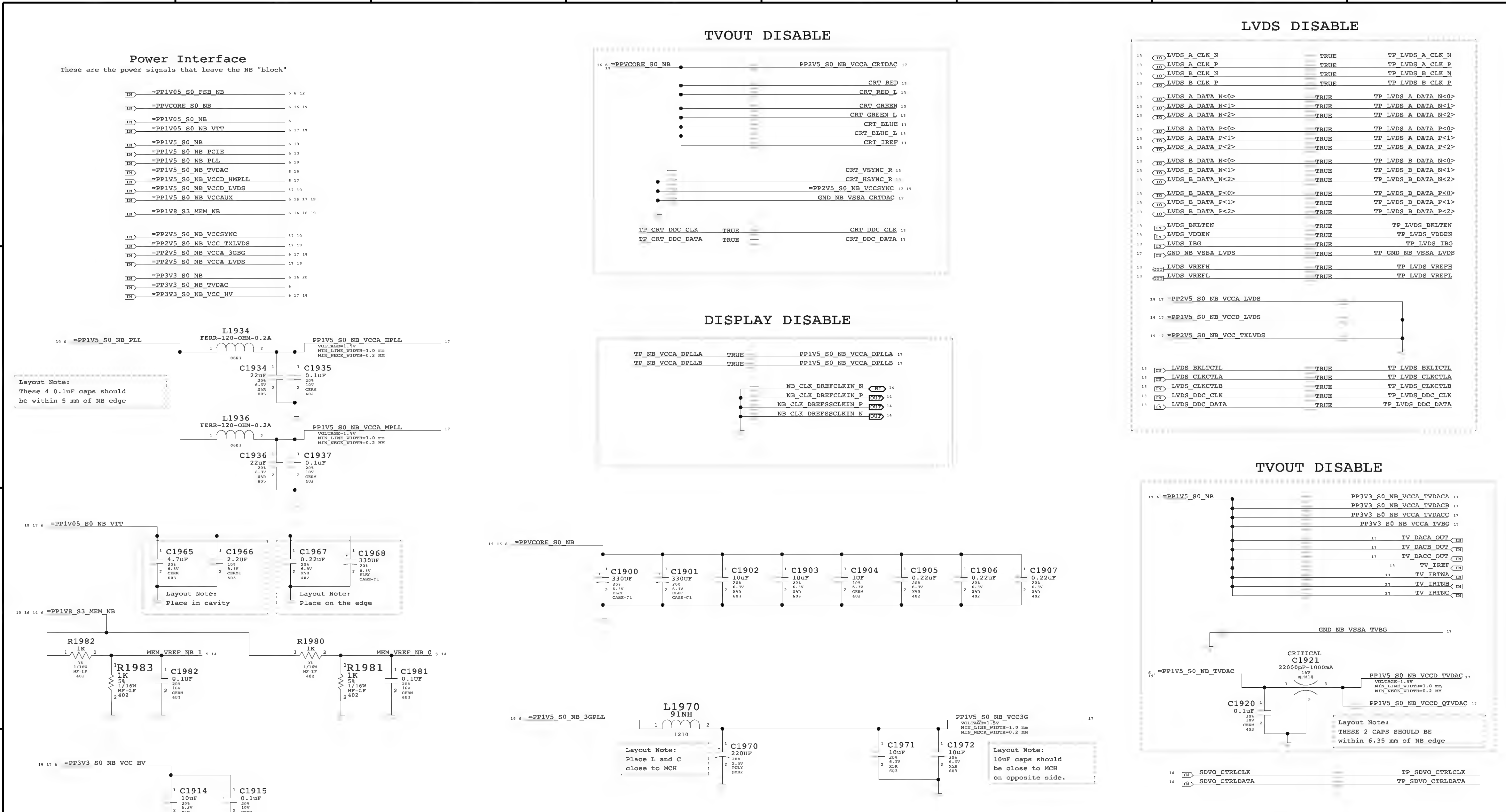

## NB Grounds

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NB (GM) Decoupling

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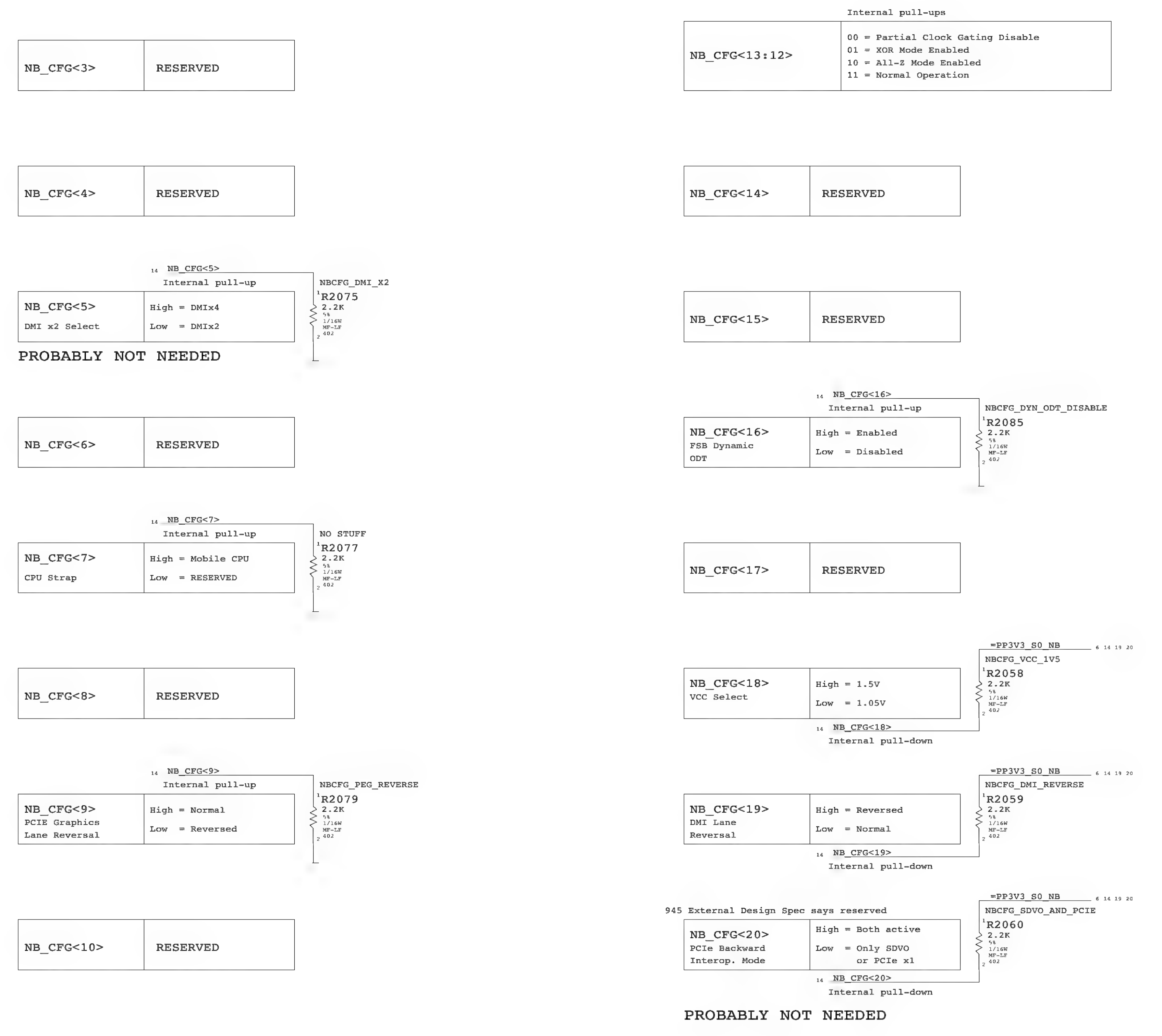
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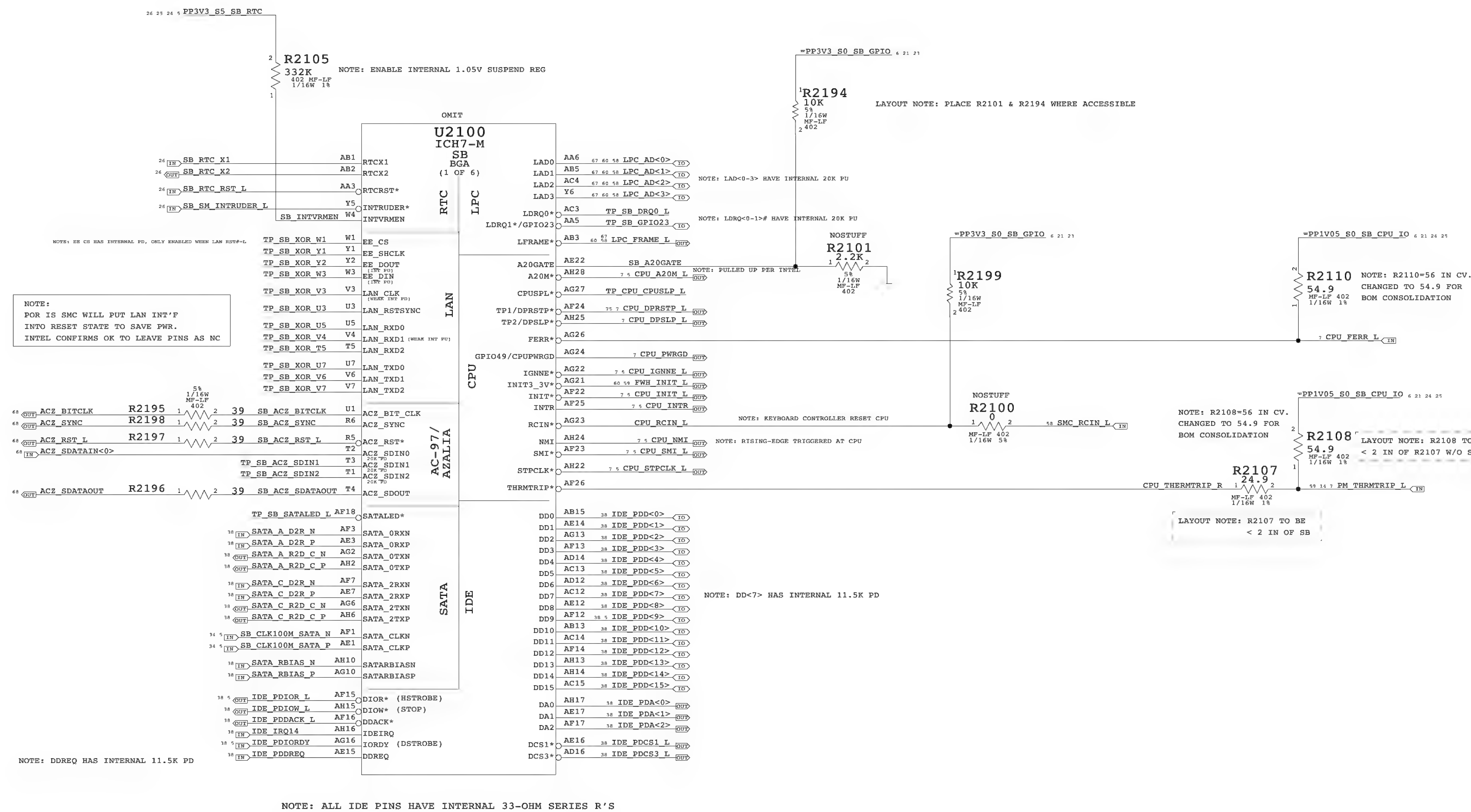

NB Config Straps

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|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 20 | OF 111 |




NOTE: ALL IDE PINS HAVE INTERNAL 33-OHM SERIES R'S

| | AC '07 | INTEL HIGH DEFINITION AUDIO |
|---|---|---|
| ACZ_BIT_CLK | INTERNAL 20K PD ENABLED WHEN<br>- LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR<br>- BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED | INTERNAL 20K PD ONLY ENABLED IN S3COLD |
| ACZ_RST# | | NONE |
| ACZ_SDIN[0-2] | INTERNAL 20K PD | INTERNAL 20K PD |
| ACZ_SDOUT | INTERNAL 20K PD ENABLED DURING RESET AND WHEN<br>- LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR<br>- BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED | INTERNAL 20K PD ENABLED WHEN<br>- LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR<br>- BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED |
| ACZ_SYNC | | INTERNAL 20K PD |

# SB: 1 OF 4

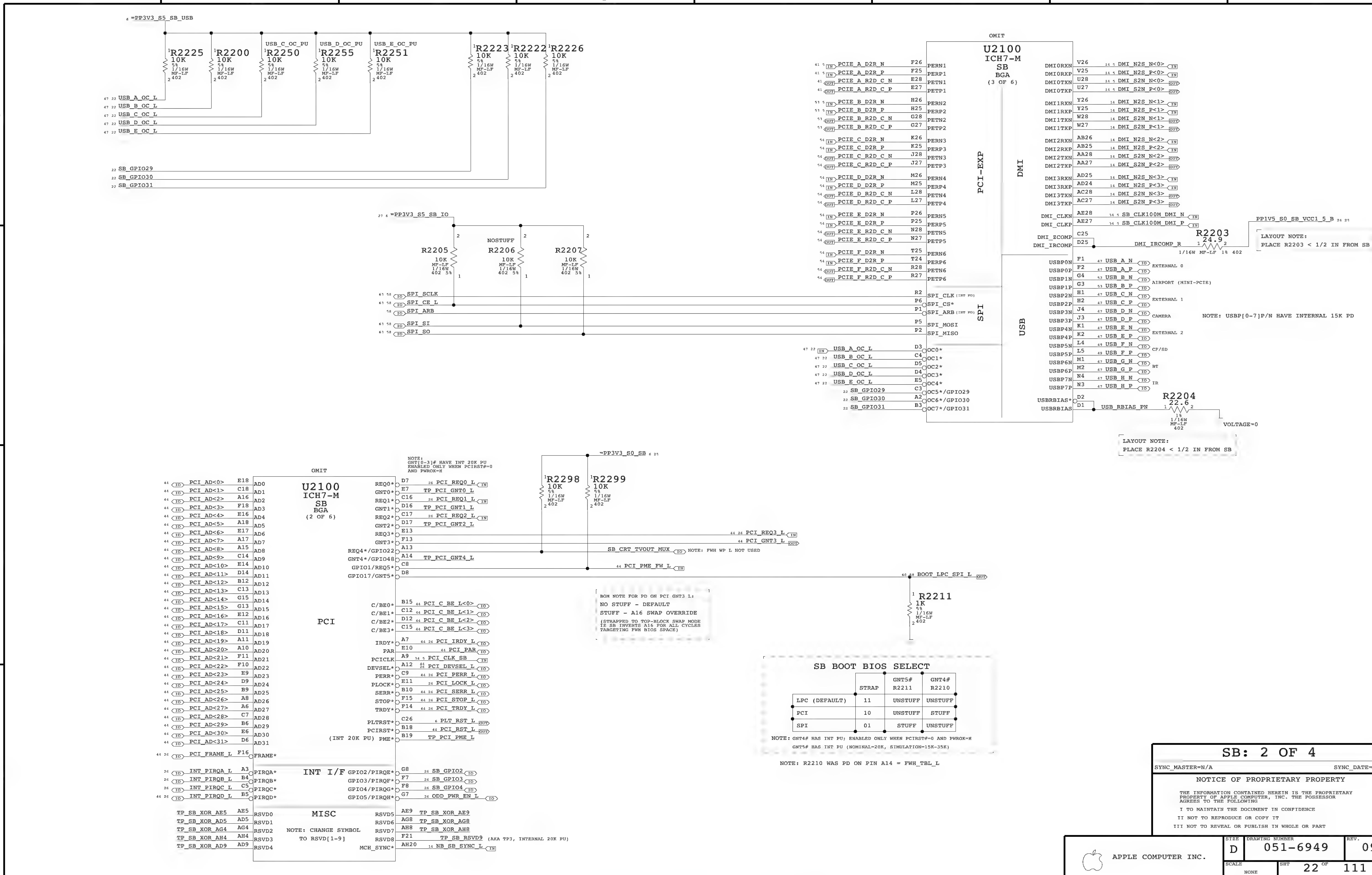
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SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 21 OF 111




## SB BOOT BIOS SELECT

| | STRAP | GNT5# R2211 | GNT4# R2210 |
|---|---|---|---|
| LPC (DEFAULT) | 11 | UNSTUFF | UNSTUFF |
| PCI | 10 | UNSTUFF | STUFF |
| SPI | 01 | STUFF | UNSTUFF |

NOTE: GNT4# HAS INT PU; ENABLED ONLY WHEN PCIRST#=0 AND PWROK=H
GNT5# HAS INT PU (NOMINAL=20K, SIMULATION=15K-35K)

NOTE: R2210 WAS PD ON PIN A14 = FWH_TBL_L

SB: 2 OF 4

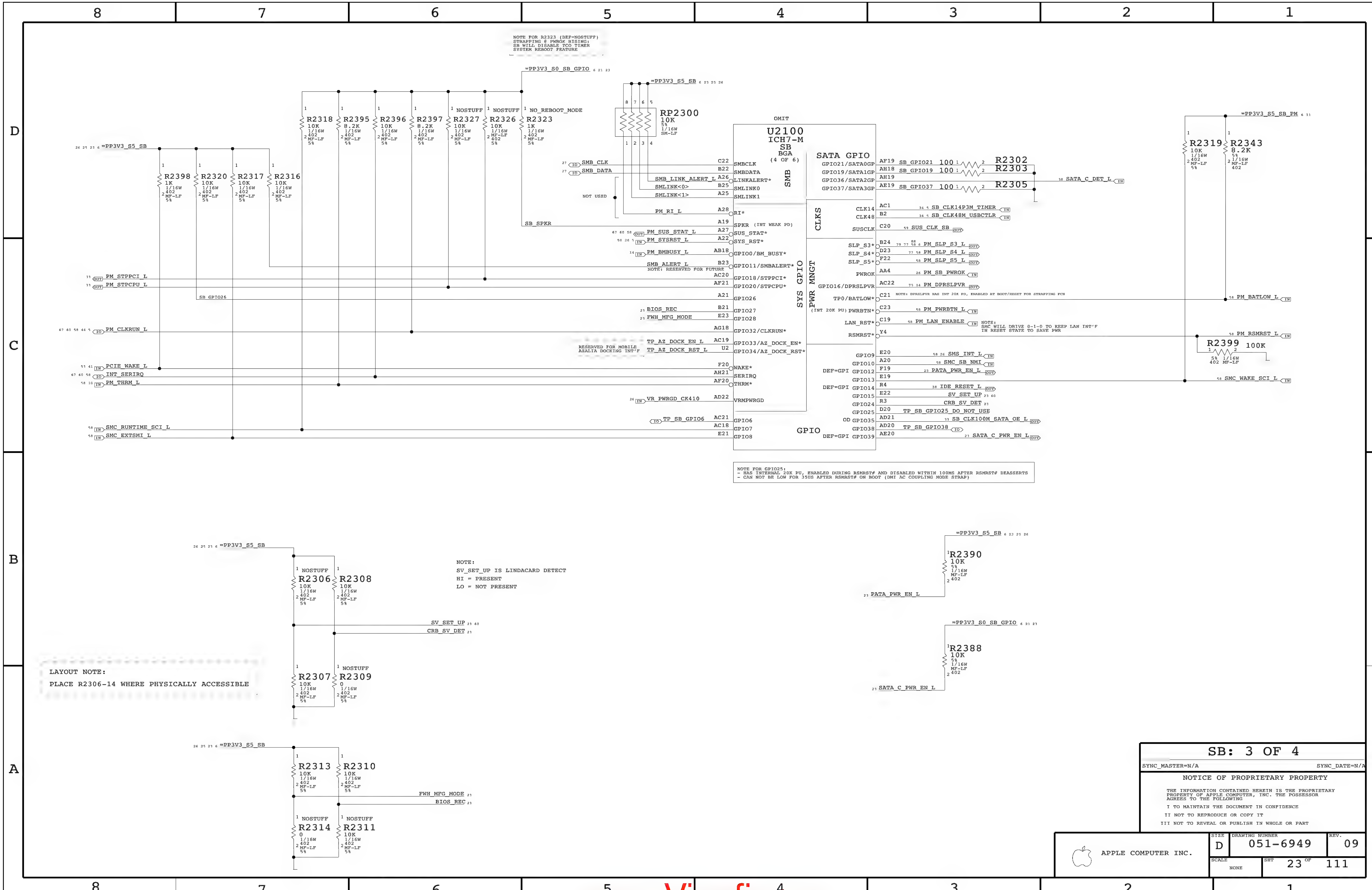
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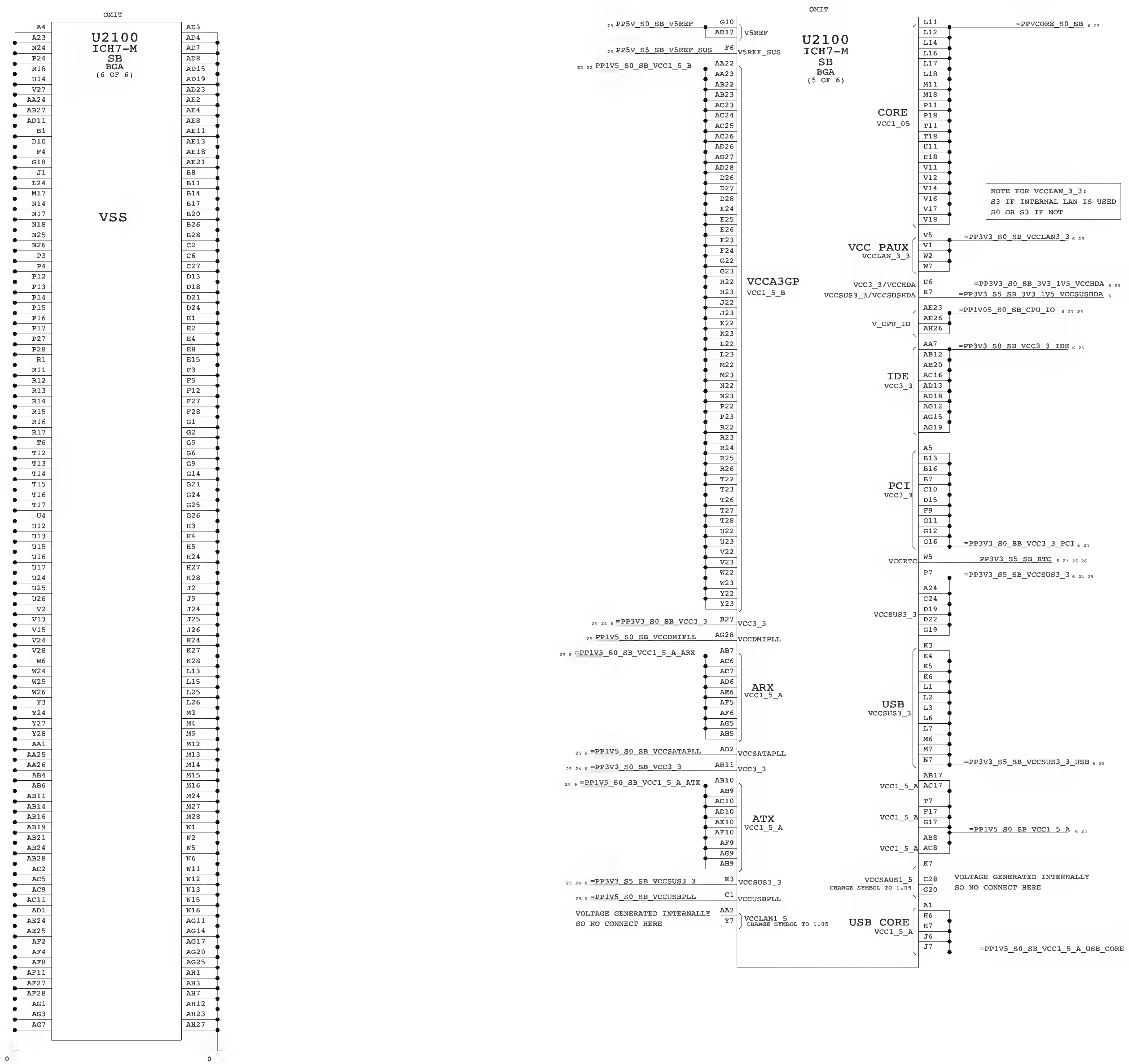
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SCALE NONE | SHT 22 OF 111



NOTE FOR R2323 (DEF=NOSTUFF) STRAPPING @ PWROK RISING: SB WILL DISABLE TCO TIMER SYSTEM REBOOT FEATURE
=PP3V3_S0_SB_GPIO
=PP3V3_S5_SB
=PP3V3_S5_SB_PM
R2318 10K
R2395 8.2K
R2396 10K
R2397 8.2K
NOSTUFF R2327 10K
NOSTUFF R2326 10K
NO_REBOOT_MODE R2323 1K
RP2300 10K
R2398 1K
R2320 10K
R2317 10K
R2316 10K
R2319 10K
R2343 8.2K
OMIT
U2100
ICH7-M
SB
BGA
(4 OF 6)
SMB
SATA GPIO
CLKS
SYS GPIO
PWR MNGT
GPIO
SMB_CLK
SMB_DATA
SMB_LINK_ALERT_L
SMLINK<0>
SMLINK<1>
NOT USED
PM_RI_L
SB_SPKR
PM_SUS_STAT_L
PM_SYSRST_L
PM_BMBUSY_L
SMB_ALERT_L
NOTE: RESERVED FOR FUTURE
PM_STPPCI_L
PM_STPCPU_L
SB_GPIO26
BIOS_REC
FWH_MFG_MODE
PM_CLKRUN_L
TP_AZ_DOCK_EN_L
TP_AZ_DOCK_RST_L
RESERVED FOR MOBILE AZALIA DOCKING INT'F
PCIE_WAKE_L
INT_SERIRQ
PM_THRM_L
VR_PWRGD_CK410
TP_SB_GPIO6
SMC_RUNTIME_SCI_L
SMC_EXTSMI_L
SB_GPIO21
SB_GPIO19
SB_GPIO37
R2302
R2303
R2305
SATA_C_DET_L
SB_CLK14P3M_TIMER
SB_CLK48M_USBCTLR
SUS_CLK_SB
PM_SLP_S3_L
PM_SLP_S4_L
PM_SLP_S5_L
PM_SB_PWROK
PM_DPRSLPVR
NOTE: DPRSLPVR HAS INT 20K PD, ENABLED AT BOOT/RESET FOR STRAPPING PCH
PM_BATLOW_L
PM_PWRBTN_L
PM_LAN_ENABLE
NOTE: SMC WILL DRIVE 0-1-0 TO KEEP LAN INT'F IN RESET STATE TO SAVE PWR
PM_RSMRST_L
R2399 100K
SMS_INT_L
SMC_SB_NMI
PATA_PWR_EN_L
SMC_WAKE_SCI_L
IDE_RESET_L
SV_SET_UP
CRB_SV_DET
TP_SB_GPIO25_DO_NOT_USE
SB_CLK100M_SATA_OE_L
TP_SB_GPIO38
SATA_C_PWR_EN_L
NOTE FOR GPIO25:
- HAS INTERNAL 20K PU, ENABLED DURING RSMRST# AND DISABLED WITHIN 100MS AFTER RSMRST# DEASSERTS
- CAN NOT BE LOW FOR 35US AFTER RSMRST# ON BOOT (DMI AC COUPLING MODE STRAP)
NOSTUFF R2306 10K
R2308 10K
R2307 10K
NOSTUFF R2309 0
NOTE:
SV_SET_UP IS LINDACARD DETECT
HI = PRESENT
LO = NOT PRESENT
LAYOUT NOTE:
PLACE R2306-14 WHERE PHYSICALLY ACCESSIBLE
R2390 10K
R2388 10K
R2313 10K
R2310 10K
NOSTUFF R2314 0
NOSTUFF R2311 10K
FWH_MFG_MODE
BIOS_REC
SB: 3 OF 4
SYNC_MASTER=N/A
SYNC_DATE=N/A
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APPLE COMPUTER INC.
D
051-6949
09
NONE
23 OF 111




SB: 4 OF 4

SYNC_MASTER=N/A SYNC_DATE=N/A

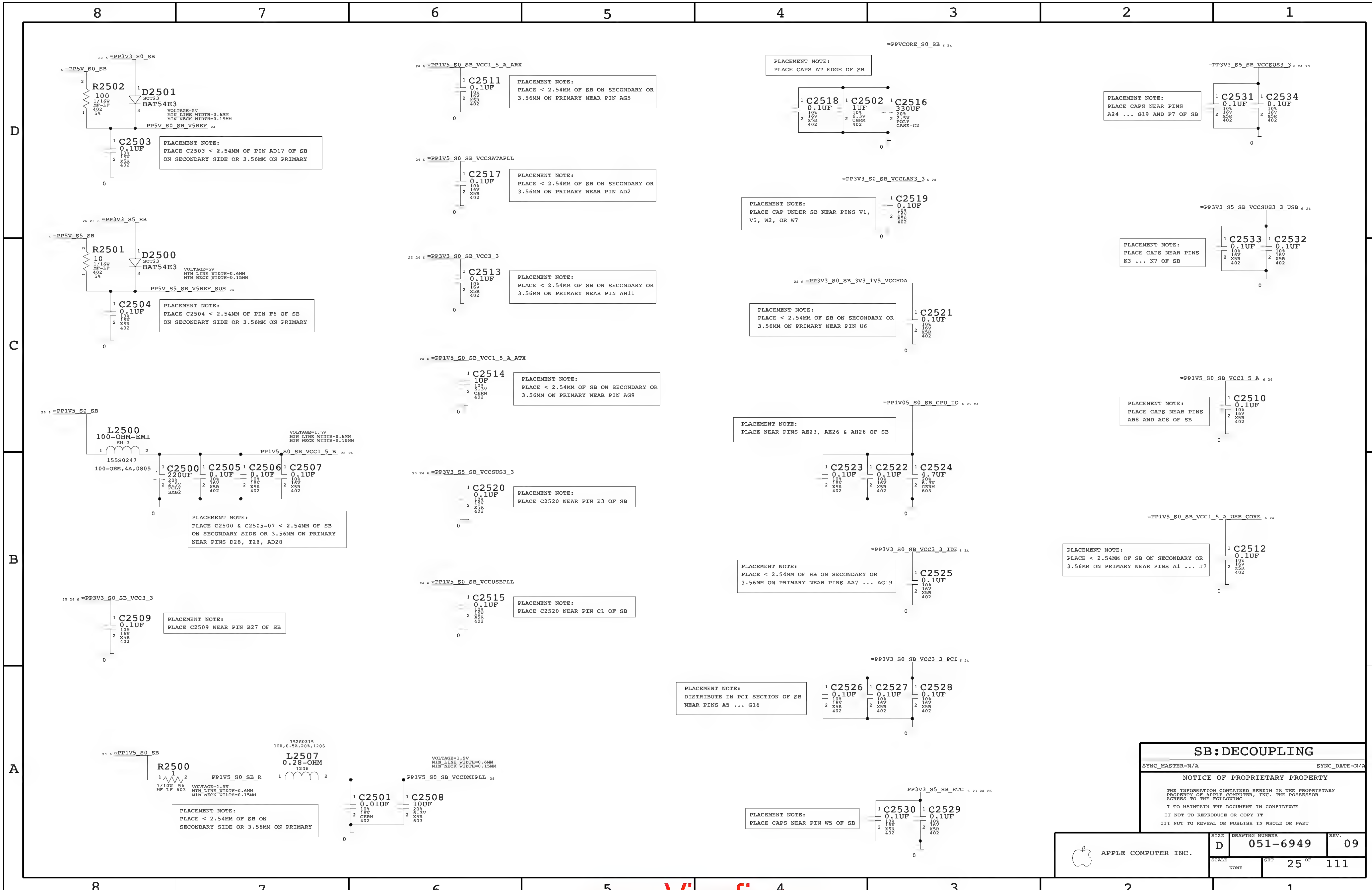
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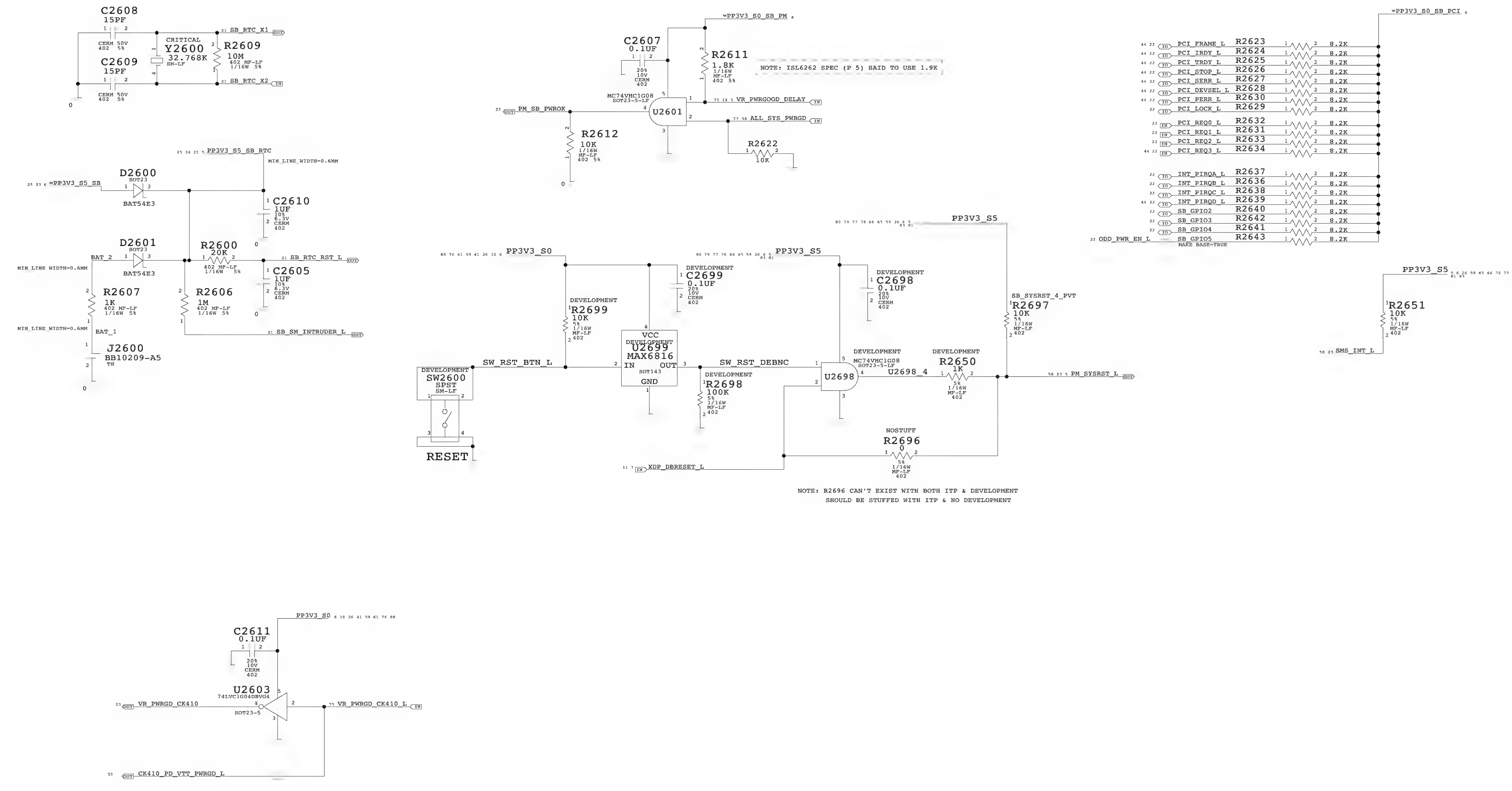
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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 24 OF 111 | |


=PP3V3_S0_SB
=PP5V_S0_SB
R2502
100
1/16W
MF-LF
402
5%
D2501
SOT23
BAT54E3
VOLTAGE=5V
MIN LINE WIDTH=0.6MM
MIN NECK WIDTH=0.15MM
PP5V_S0_SB_V5REF
C2503
0.1UF
10%
16V
X5R
402
PLACEMENT NOTE:
PLACE C2503 < 2.54MM OF PIN AD17 OF SB
ON SECONDARY SIDE OR 3.56MM ON PRIMARY
=PP3V3_S5_SB
=PP5V_S5_SB
R2501
10
1/16W
MF-LF
402
5%
D2500
SOT23
BAT54E3
PP5V_S5_SB_V5REF_SUS
C2504
0.1UF
PLACEMENT NOTE:
PLACE C2504 < 2.54MM OF PIN F6 OF SB
ON SECONDARY SIDE OR 3.56MM ON PRIMARY
=PP1V5_S0_SB
L2500
100-OHM-EMI
SM-3
155S0247
100-OHM,4A,0805
PP1V5_S0_SB_VCC1_5_B
VOLTAGE=1.5V
C2500
220UF
20%
2.5V
POLY
SMB2
C2505
0.1UF
C2506
0.1UF
C2507
0.1UF
PLACEMENT NOTE:
PLACE C2500 & C2505-07 < 2.54MM OF SB
ON SECONDARY SIDE OR 3.56MM ON PRIMARY
NEAR PINS D28, T28, AD28
=PP3V3_S0_SB_VCC3_3
C2509
0.1UF
PLACEMENT NOTE:
PLACE C2509 NEAR PIN B27 OF SB
=PP1V5_S0_SB
R2500
1/10W 5%
MF-LF 603
PP1V5_S0_SB_R
152S0315
10UH,0.5A,20%,1206
L2507
0.28-OHM
1206
PP1V5_S0_SB_VCCDMIPLL
C2501
0.01UF
10%
16V
CERM
402
C2508
10UF
20%
6.3V
X5R
603
PLACEMENT NOTE:
PLACE < 2.54MM OF SB ON
SECONDARY SIDE OR 3.56MM ON PRIMARY
=PP1V5_S0_SB_VCC1_5_A_ARX
C2511
0.1UF
PLACEMENT NOTE:
PLACE < 2.54MM OF SB ON SECONDARY OR
3.56MM ON PRIMARY NEAR PIN AG5
=PP1V5_S0_SB_VCCSATAPLL
C2517
0.1UF
PLACEMENT NOTE:
PLACE < 2.54MM OF SB ON SECONDARY OR
3.56MM ON PRIMARY NEAR PIN AD2
=PP3V3_S0_SB_VCC3_3
C2513
0.1UF
PLACEMENT NOTE:
PLACE < 2.54MM OF SB ON SECONDARY OR
3.56MM ON PRIMARY NEAR PIN AH11
=PP1V5_S0_SB_VCC1_5_A_ATX
C2514
1UF
10%
6.3V
CERM
402
PLACEMENT NOTE:
PLACE < 2.54MM OF SB ON SECONDARY OR
3.56MM ON PRIMARY NEAR PIN AG9
=PP3V3_S5_SB_VCCSUS3_3
C2520
0.1UF
PLACEMENT NOTE:
PLACE C2520 NEAR PIN E3 OF SB
=PP1V5_S0_SB_VCCUSBPLL
C2515
0.1UF
PLACEMENT NOTE:
PLACE C2520 NEAR PIN C1 OF SB
=PPVCORE_S0_SB
PLACEMENT NOTE:
PLACE CAPS AT EDGE OF SB
C2518
0.1UF
C2502
1UF
10%
6.3V
CERM
402
C2516
330UF
20%
2.5V
POLY
CASE-C2
=PP3V3_S0_SB_VCCLAN3_3
C2519
0.1UF
PLACEMENT NOTE:
PLACE CAP UNDER SB NEAR PINS V1,
V5, W2, OR W7
=PP3V3_S0_SB_3V3_1V5_VCCHDA
C2521
0.1UF
PLACEMENT NOTE:
PLACE < 2.54MM OF SB ON SECONDARY OR
3.56MM ON PRIMARY NEAR PIN U6
=PP1V05_S0_SB_CPU_IO
PLACEMENT NOTE:
PLACE NEAR PINS AE23, AE26 & AH26 OF SB
C2523
0.1UF
C2522
0.1UF
C2524
4.7UF
20%
6.3V
CERM
603
=PP3V3_S0_SB_VCC3_3_IDE
C2525
0.1UF
PLACEMENT NOTE:
PLACE < 2.54MM OF SB ON SECONDARY OR
3.56MM ON PRIMARY NEAR PINS AA7 ... AG19
=PP3V3_S0_SB_VCC3_3_PCI
PLACEMENT NOTE:
DISTRIBUTE IN PCI SECTION OF SB
NEAR PINS A5 ... G16
C2526
0.1UF
C2527
0.1UF
C2528
0.1UF
PP3V3_S5_SB_RTC
PLACEMENT NOTE:
PLACE CAPS NEAR PIN W5 OF SB
C2530
0.1UF
C2529
0.1UF
=PP3V3_S5_SB_VCCSUS3_3
PLACEMENT NOTE:
PLACE CAPS NEAR PINS
A24 ... G19 AND P7 OF SB
C2531
0.1UF
C2534
0.1UF
=PP3V3_S5_SB_VCCSUS3_3_USB
PLACEMENT NOTE:
PLACE CAPS NEAR PINS
K3 ... N7 OF SB
C2533
0.1UF
C2532
0.1UF
=PP1V5_S0_SB_VCC1_5_A
PLACEMENT NOTE:
PLACE CAPS NEAR PINS
AB8 AND AC8 OF SB
C2510
0.1UF
=PP1V5_S0_SB_VCC1_5_A_USB_CORE
PLACEMENT NOTE:
PLACE < 2.54MM OF SB ON SECONDARY OR
3.56MM ON PRIMARY NEAR PINS A1 ... J7
C2512
0.1UF
SB:DECOUPLING
SYNC_MASTER=N/A
SYNC_DATE=N/A
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SIZE D
DRAWING NUMBER 051-6949
REV. 09
SCALE NONE
SHT 25 OF 111




SB: MISC

SYNC_MASTER=N/A SYNC_DATE=N/A

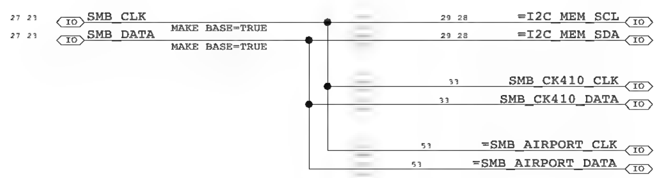
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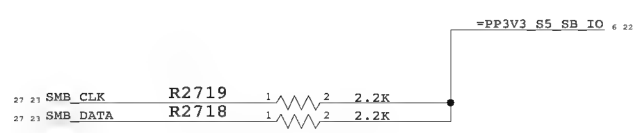


APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 26 OF 111 | |

# SB I2C BUSSES




SB: SMB HUB

SYNC_MASTER=N/A SYNC_DATE=N/A



APPLE COMPUTER INC.

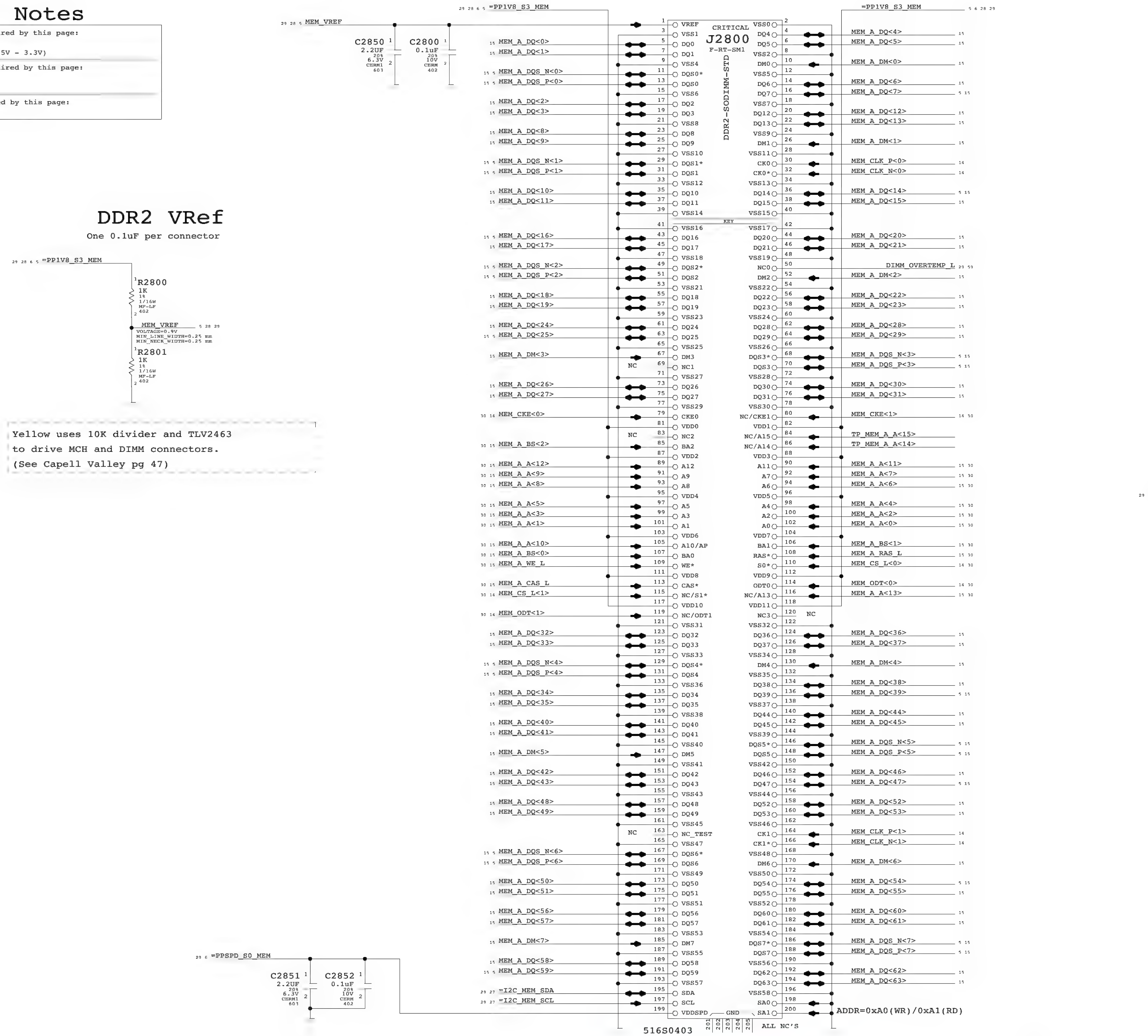
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|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 27 OF 111 | |

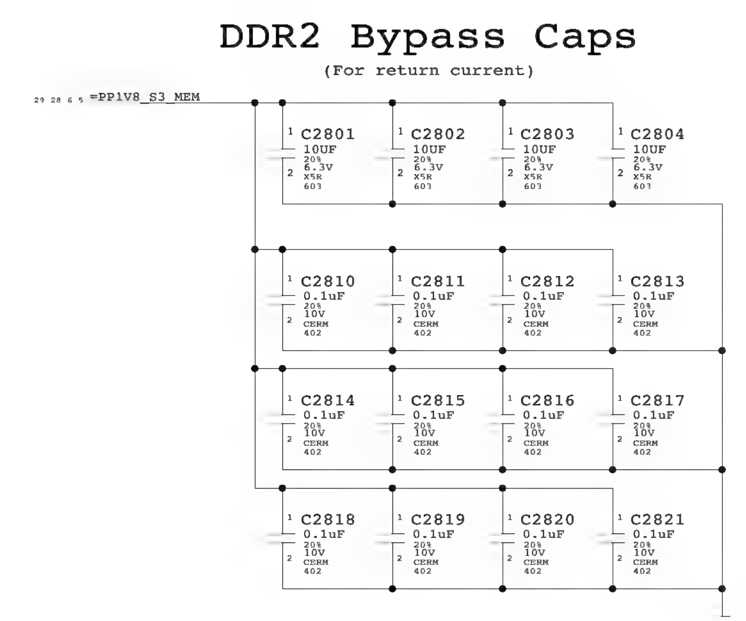
# Page Notes

Power aliases required by this page:
- =PP1V8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:
- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:
(NONE)




DDR2 SO-DIMM Connector A

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE: NONE | SHT 28 | OF 111 |

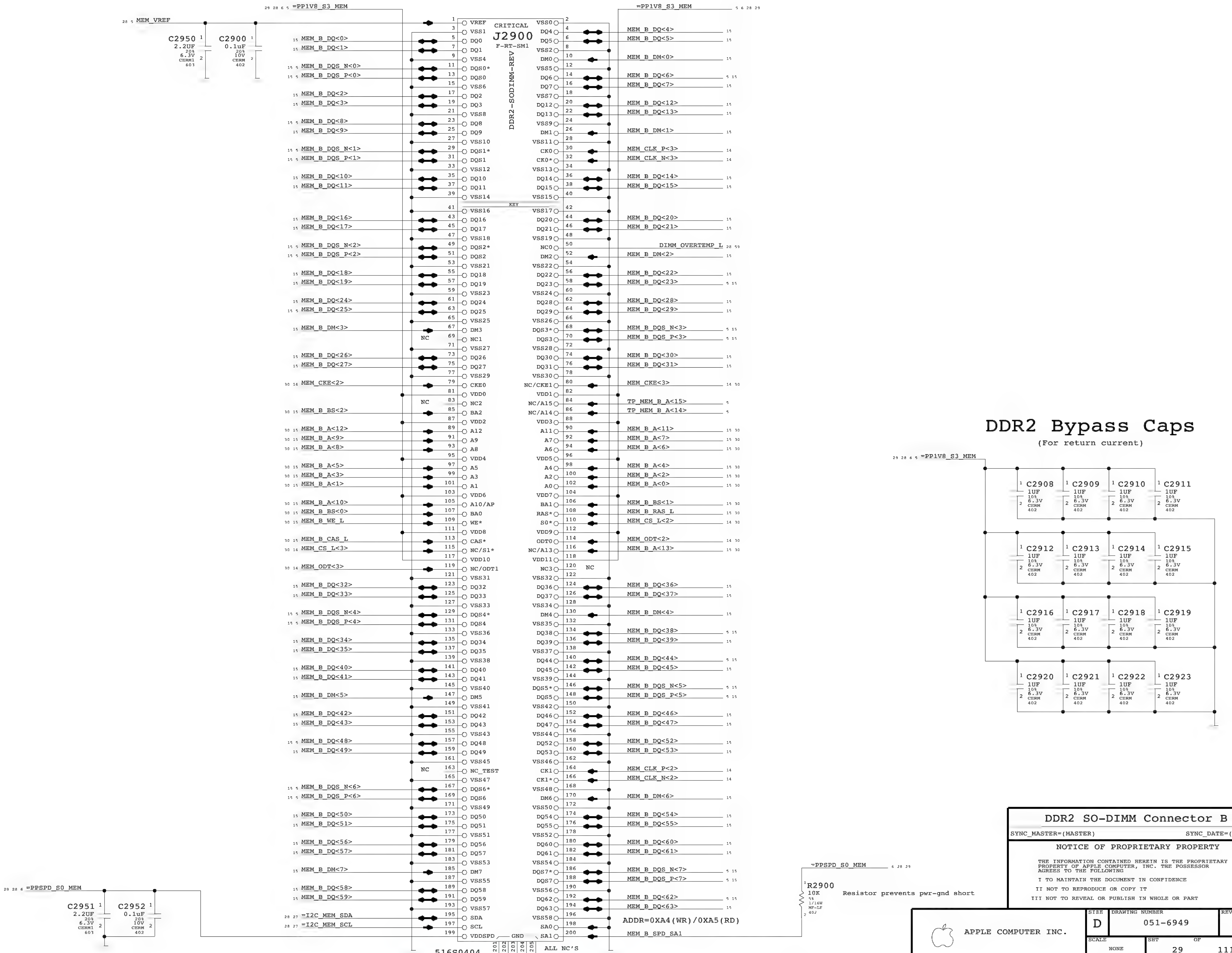
# Page Notes

Power aliases required by this page:
- =PP1V8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:
- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:
(NONE)

NOTE: This page does not supply VREF.
The reference voltage must be provided
by another page.




## DDR2 SO-DIMM Connector B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 29 | OF 111 |

One cap for each side of every RPAK, one cap for every two discrete resistors
BOMOPTION shown at the top of each group applies to every part below it




## Memory Active Termination

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|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 30 | OF 111 |

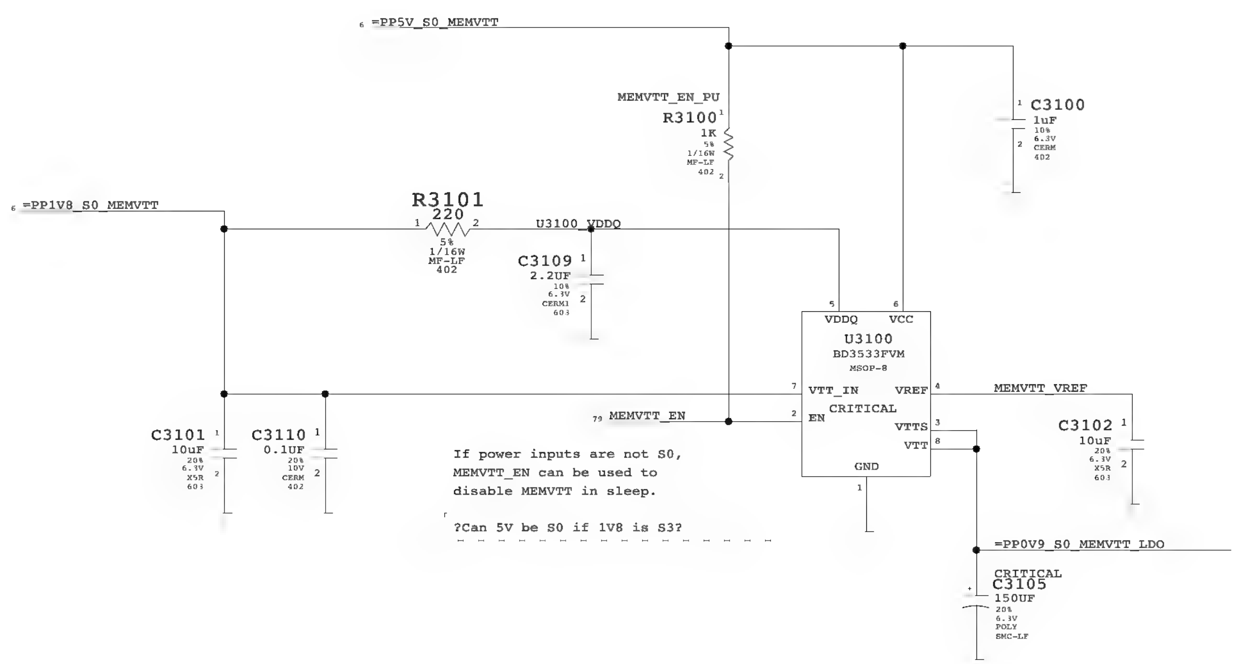
## Page Notes

Power aliases required by this page:
- =PP5V_S0_MEMVTT
- =PP1V8_S0_MEMVTT
- =PP0V9_S0_MEMVTT_LDO

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

# DDR2 Vtt Regulator




| Memory Vtt Supply | | | |
|---|---|---|---|
| SYNC_MASTER=(MASTER) | | | SYNC_DATE=(MASTER) |

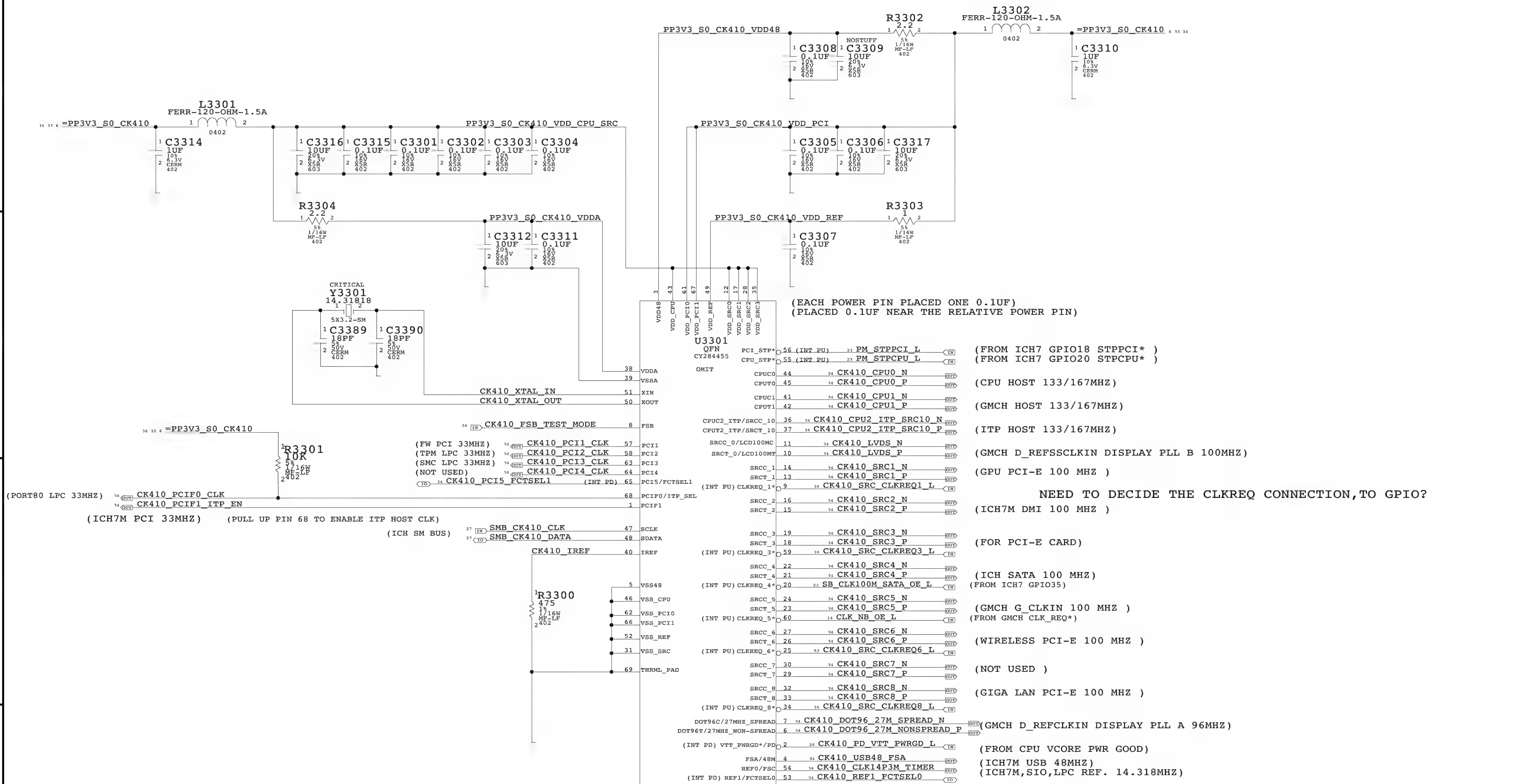
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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 31 | OF 111 |




| FCTSEL1 | FCTSEL0 | PIN 6 | PIN 7 | PIN 10 | PIN 11 |
|---|---|---|---|---|---|
| 0 | 0 | DOT96T | DOT96C | 100MT_SST | 100MC_SST |
| 0 | 1 | DOT96T | DOT96C | SRCT0 | SRCC0 |
| 1 | 0 | 27M NON SPREAD | 27M SPREAD | SRCT0 | SRCC0 |
| 1 | 1 | OFF LOW | TBD | SRCT0 | SRCC0 |

* FOR INT. GRAPHIC SYSTEM

* FOR EXT. GRAPHIC SYSTEM

# CLOCKS

SYNC_MASTER=CLOCK SYNC_DATE=06/03/2005



APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 33 OF 111




# CLOCKS: TERMINATIONS

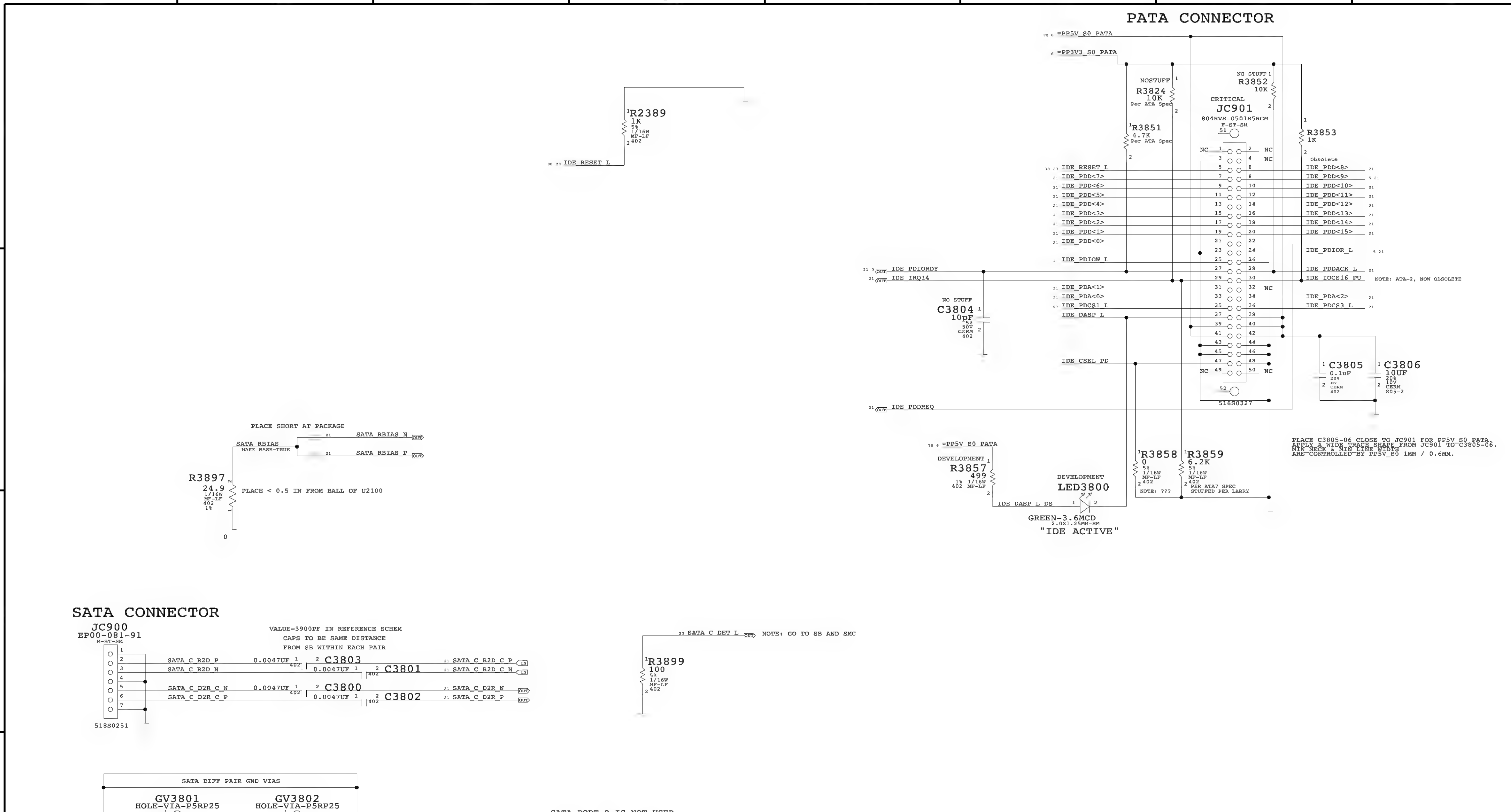
SYNC_MASTER=N/A SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY



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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 34 | OF 111 |




# Disk Connectors

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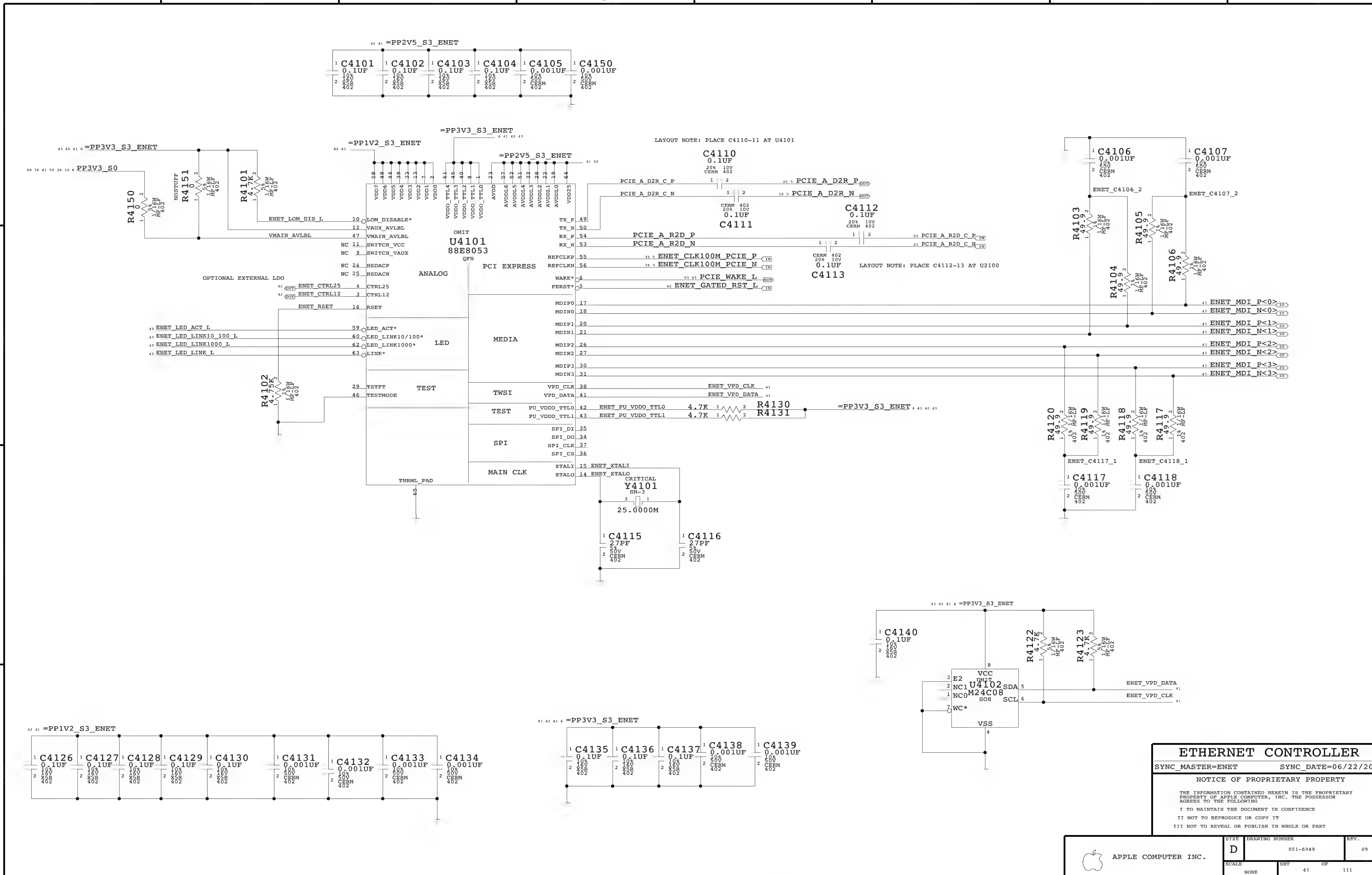
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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 38 | OF 111 |




# ETHERNET CONTROLLER

SYNC_MASTER=ENET SYNC_DATE=06/22/2005

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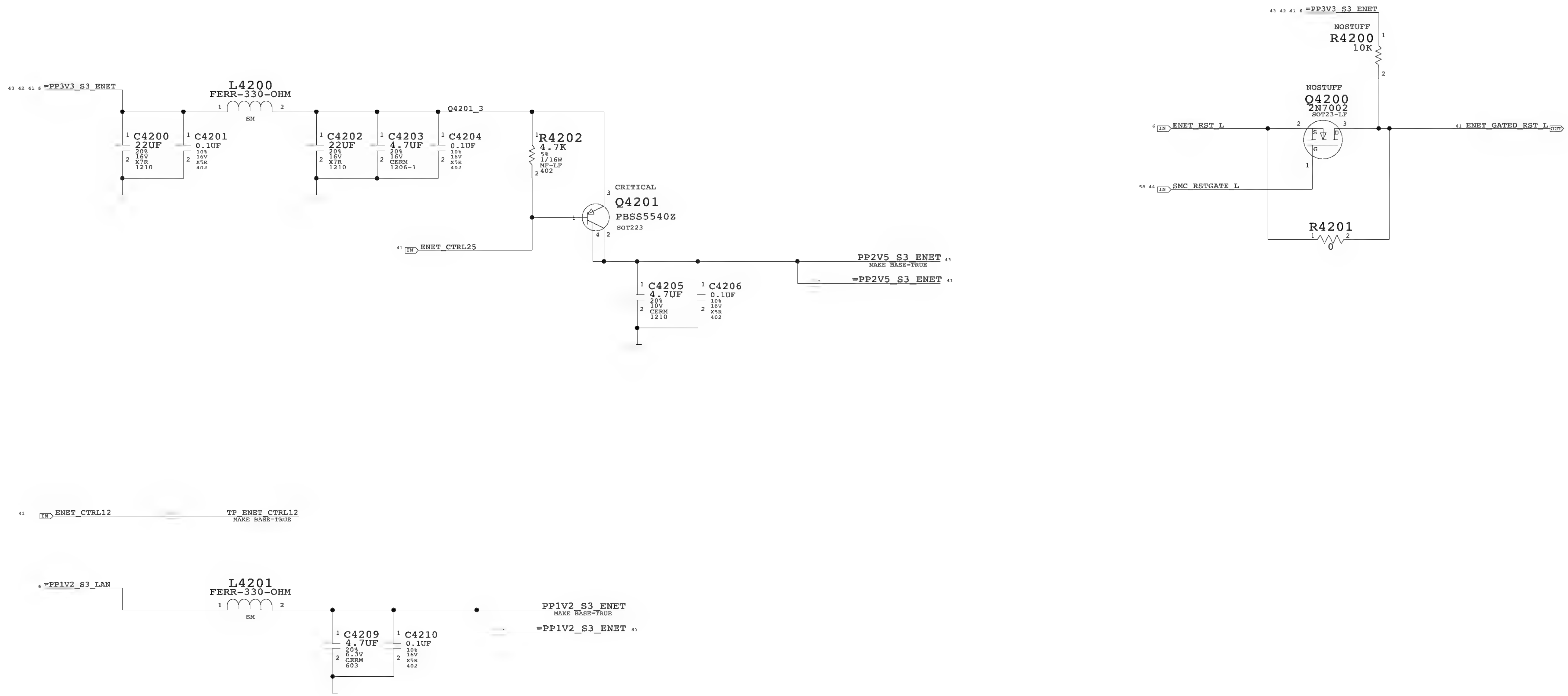
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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |

SCALE NONE SHT 41 OF 111



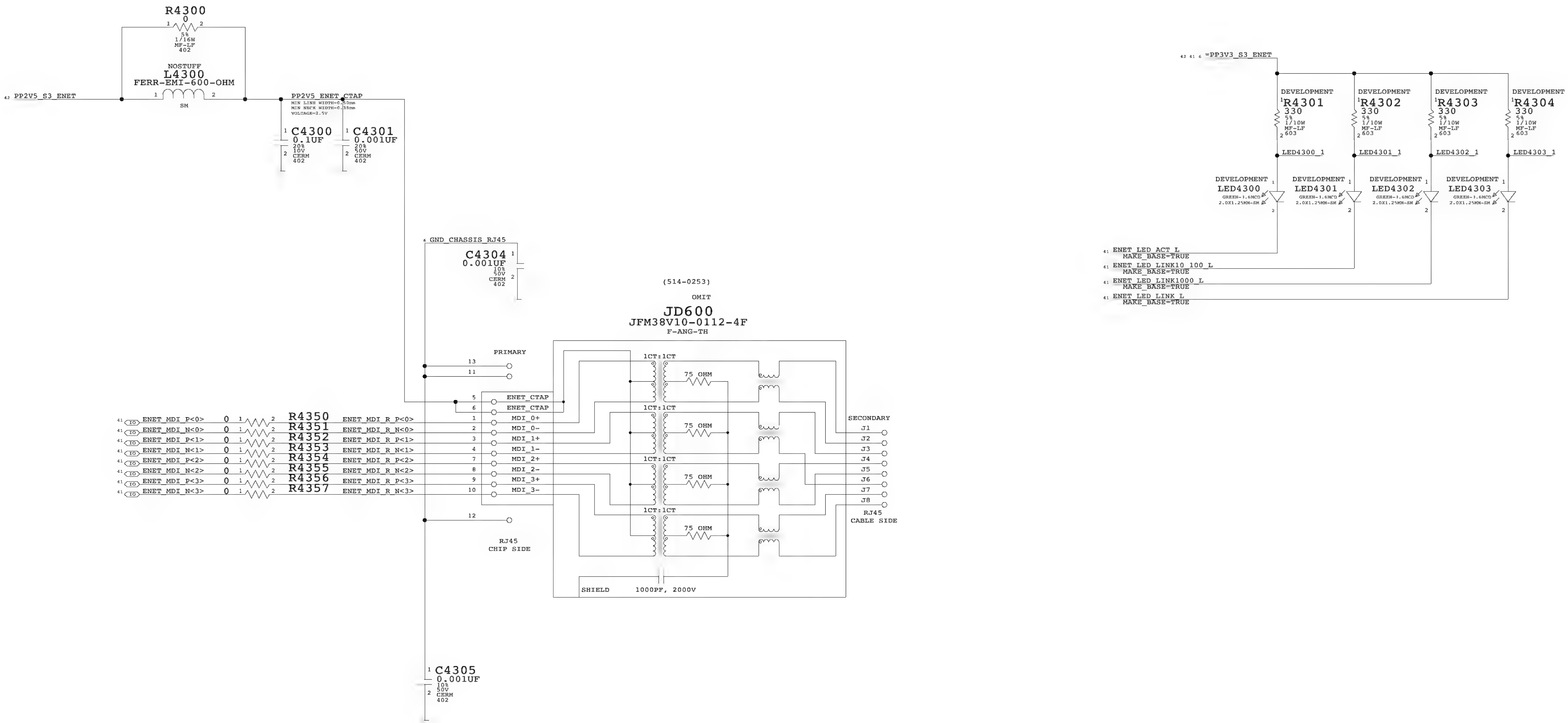

ETHERNET MISC



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |

| SCALE | SHT | OF |
|---|---|---|
| NONE | 42 | 111 |




| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 514-0300 | 1 | CON,RJ-45 7 DEGRESS | JD600 | CRITICAL | 17 INCH LCD |
| 514-0301 | 1 | CON,RJ-45 7 DEGRESS | JD600 | CRITICAL | 20 INCH LCD |

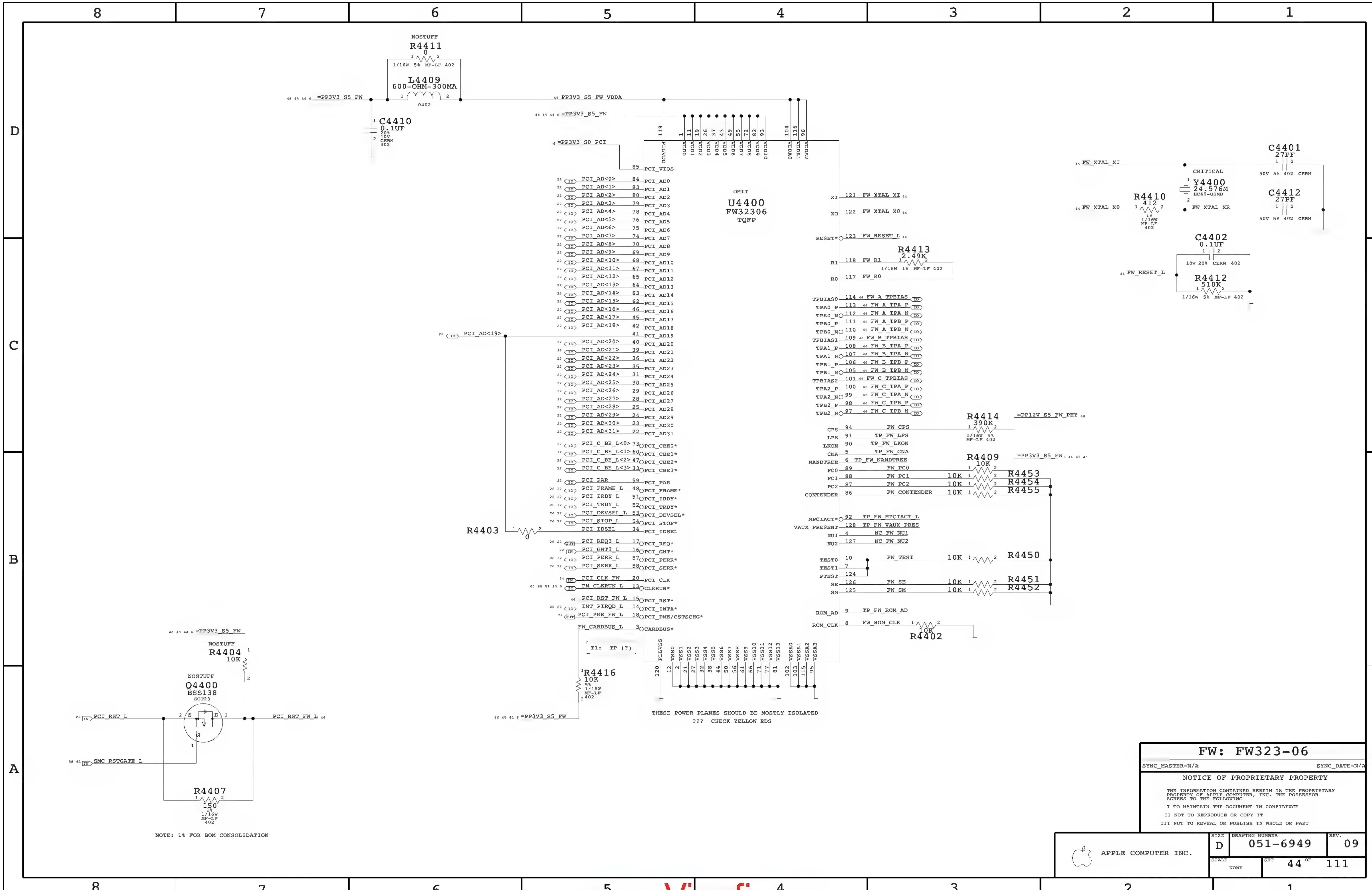
# ETHERNET CONNECTOR

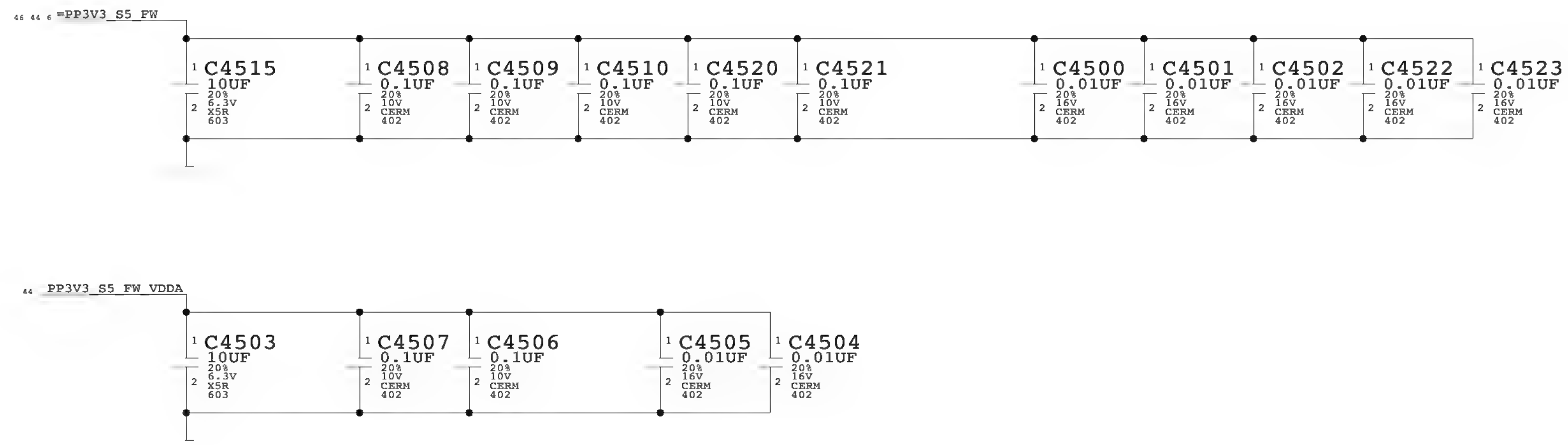


APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 43 OF 111


NOSTUFF
R4411
0
1/16W 5% MF-LF 402
L4409
600-OHM-300MA
0402
C4410
0.1UF
20%
10V
CERM
402
=PP3V3_S5_FW
PP3V3_S5_FW_VDDA
=PP3V3_S0_PCI
OMIT
U4400
FW32306
TQFP
PLLVDD
PCI_VIOS
PCI_AD<0>
PCI_AD<19>
PCI_AD<31>
PCI_C_BE_L<0>
PCI_C_BE_L<1>
PCI_C_BE_L<2>
PCI_C_BE_L<3>
PCI_PAR
PCI_FRAME_L
PCI_IRDY_L
PCI_TRDY_L
PCI_DEVSEL_L
PCI_STOP_L
PCI_IDSEL
R4403
PCI_REQ3_L
PCI_GNT3_L
PCI_PERR_L
PCI_SERR_L
PCI_CLK_FW
PM_CLKRUN_L
PCI_RST_FW_L
INT_PIRQD_L
PCI_PME_FW_L
FW_CARDBUS_L
PCI_PME/CSTSCHG*
CARDBUS*
T1: TP (?)
R4416
10K
5%
1/16W
MF-LF
402
FW_XTAL_XI
FW_XTAL_XO
FW_RESET_L
FW_R1
FW_R0
R4413
2.49K
1/16W 1% MF-LF 402
FW_A_TPBIAS
FW_A_TPA_P
FW_A_TPA_N
FW_A_TPB_P
FW_A_TPB_N
FW_B_TPBIAS
FW_B_TPA_P
FW_B_TPA_N
FW_B_TPB_P
FW_B_TPB_N
FW_C_TPBIAS
FW_C_TPA_P
FW_C_TPA_N
FW_C_TPB_P
FW_C_TPB_N
FW_CPS
TP_FW_LPS
TP_FW_LKON
TP_FW_CNA
TP_FW_NANDTREE
FW_PC0
FW_PC1
FW_PC2
FW_CONTENDER
TP_FW_MPCIACT_L
TP_FW_VAUX_PRES
NC_FW_NU1
NC_FW_NU2
FW_TEST
FW_SE
FW_SM
TP_FW_ROM_AD
FW_ROM_CLK
R4414
390K
1/16W 5%
MF-LF 402
=PP12V_S5_FW_PHY
R4409
10K
R4453
R4454
R4455
R4450
R4451
R4452
R4402
10K
C4401
27PF
50V 5% 402 CERM
CRITICAL
Y4400
24.576M
HC49-USMD
R4410
412
1%
1/16W
MF-LF
402
FW_XTAL_XR
C4412
27PF
C4402
0.1UF
10V 20% CERM 402
R4412
510K
1/16W 5% MF-LF 402
THESE POWER PLANES SHOULD BE MOSTLY ISOLATED
??? CHECK YELLOW EDS
NOSTUFF
R4404
10K
NOSTUFF
Q4400
BSS138
SOT23
PCI_RST_L
SMC_RSTGATE_L
R4407
150
1%
1/16W
MF-LF
402
NOTE: 1% FOR BOM CONSOLIDATION
FW: FW323-06
SYNC_MASTER=N/A
SYNC_DATE=N/A
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SIZE D
DRAWING NUMBER 051-6949
REV. 09
SCALE NONE
SHT 44 OF 111



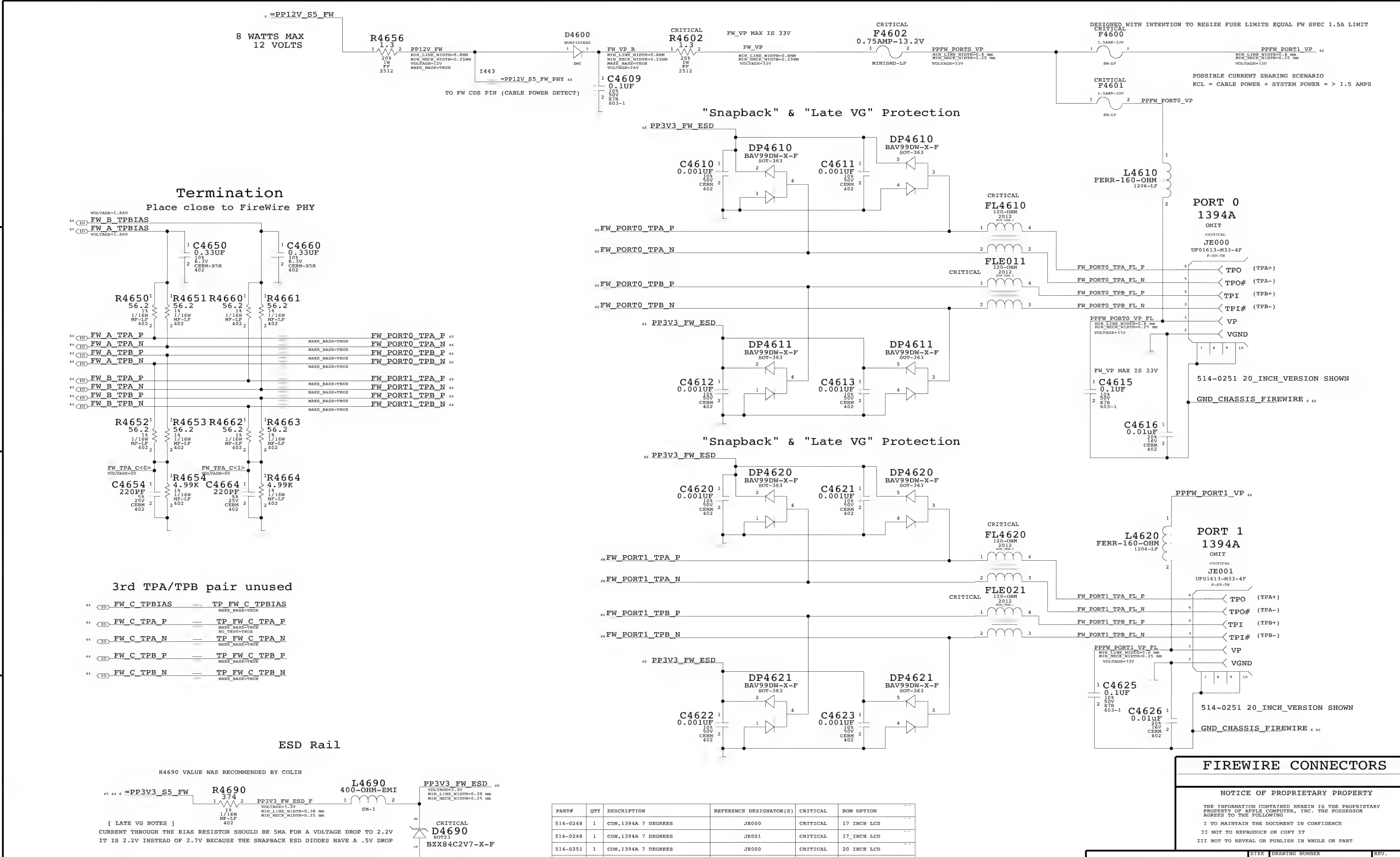

# FW: DECAPS

SYNC_MASTER=N/A SYNC_DATE=N/A



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 45 OF 111 | |




| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 514-0248 | 1 | CON,1394A 7 DEGREES | JE000 | CRITICAL | 17 INCH LCD |
| 514-0248 | 1 | CON,1394A 7 DEGREES | JE001 | CRITICAL | 17_INCH LCD |
| 514-0251 | 1 | CON,1394A 7 DEGREES | JE000 | CRITICAL | 20 INCH LCD |
| 514-0251 | 1 | CON,1394A 7 DEGREES | JE001 | CRITICAL | 20 INCH LCD |

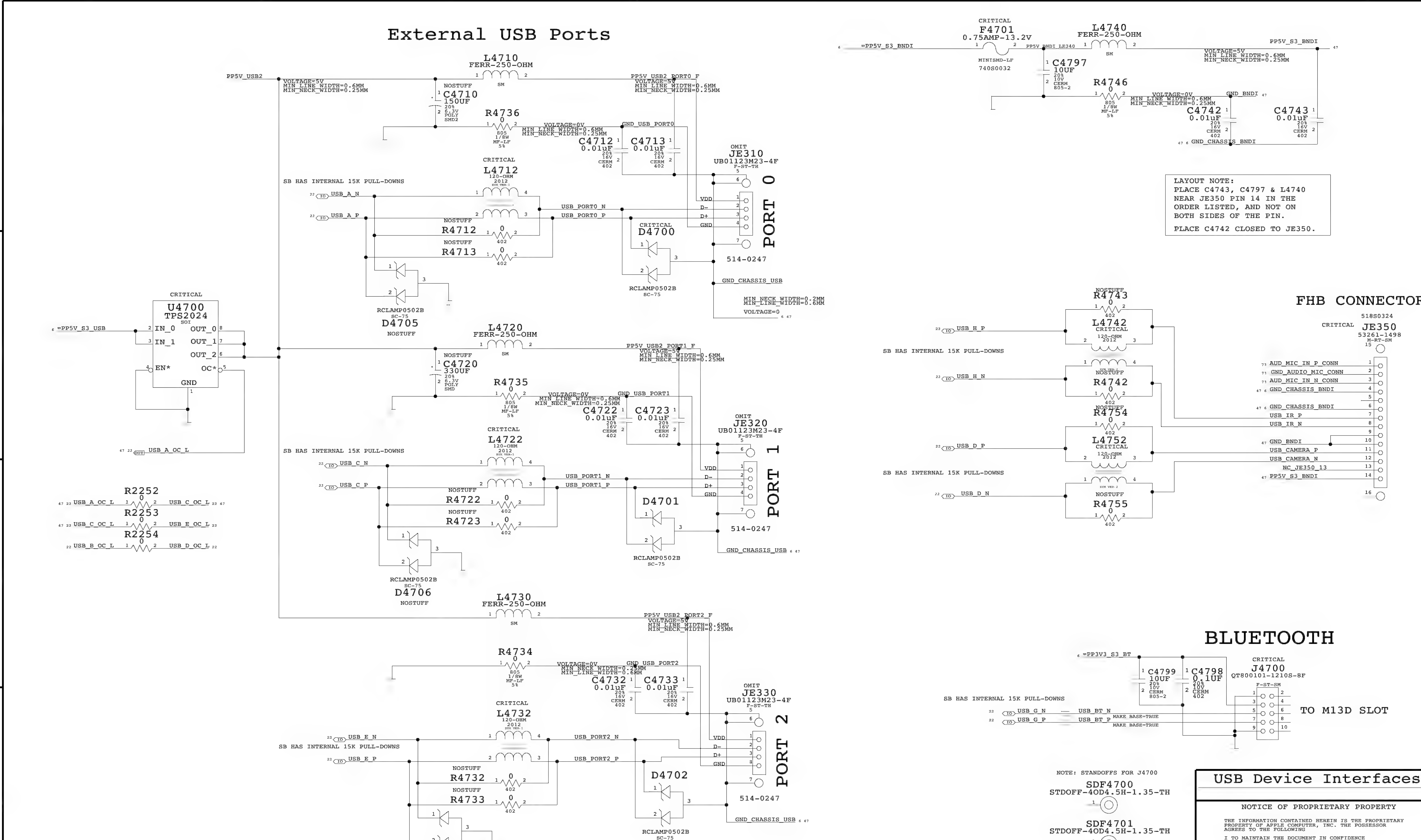
# FIREWIRE CONNECTORS



APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 46 OF 111




| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 514-0294 | 3 | USB RECEPTACLE,4P,UB1123-M23-4F | JE310,JE320,JE330 | CRITICAL | 17 INCH LCD |
| 514-0295 | 3 | USB RECEPTACLE,4P,UB1123-M33-4F | JE310,JE320,JE330 | CRITICAL | 20 INCH LCD |

# USB Device Interfaces



APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 47 OF 111

| | | | | | | | |
|---|---|---|---|---|---|---|---|
| 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 |

D

C

B

A

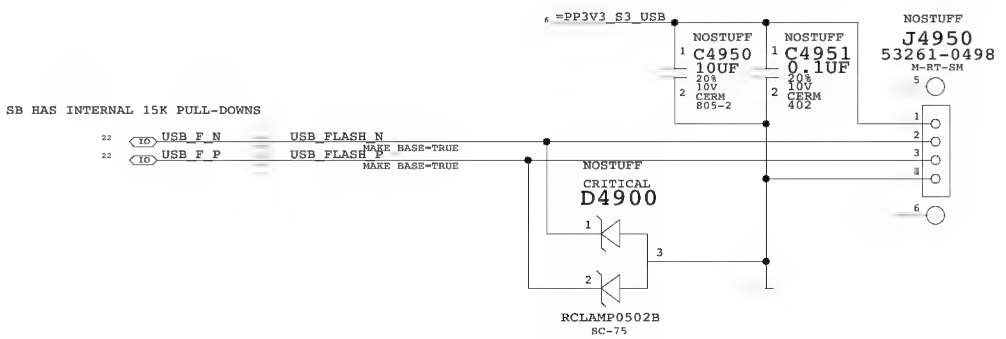
BLANK



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 48 OF 111 | |

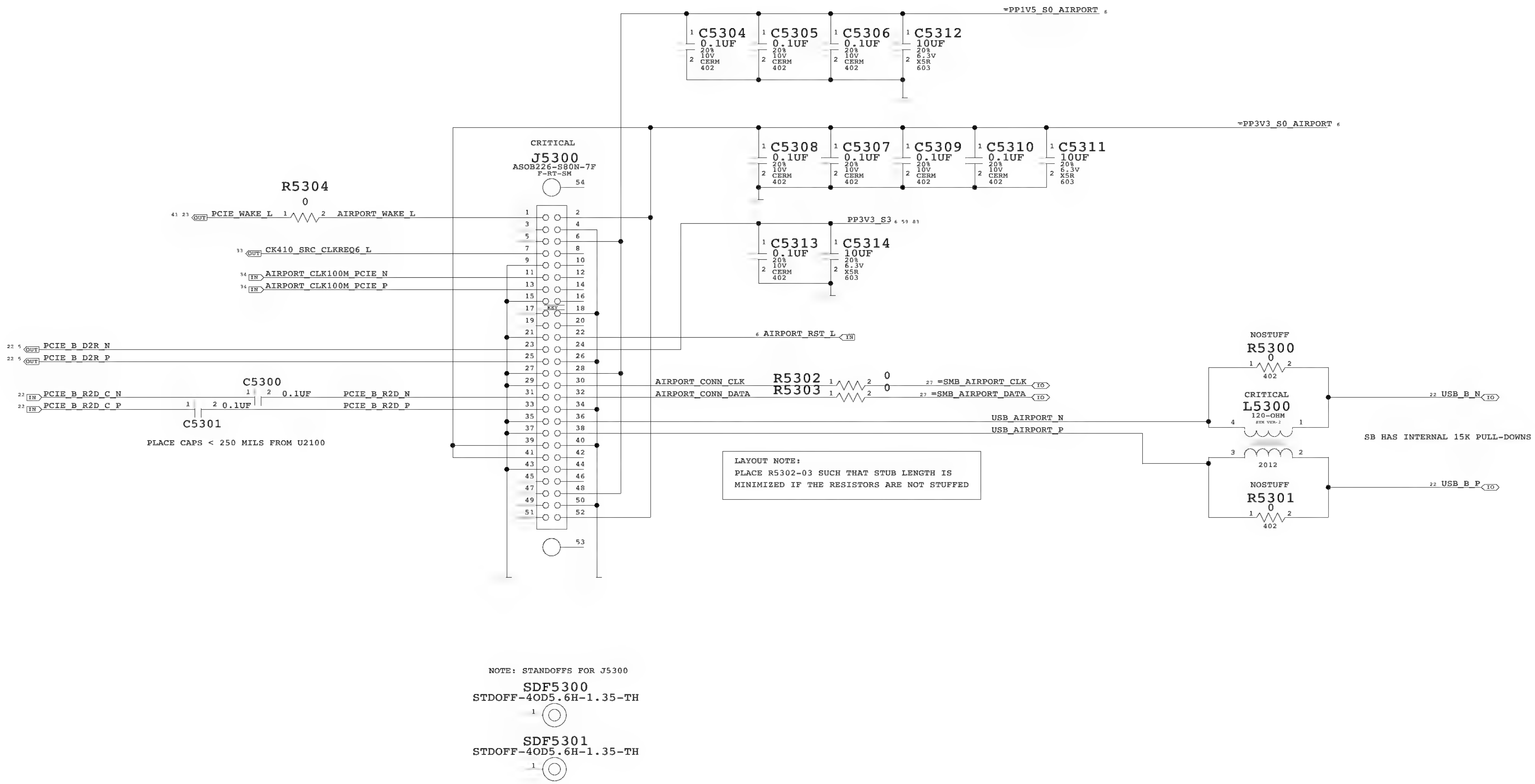
# CARD-READER USB CONN




Flash Connector



| | SIZE | DRAWING NUMBER | REV. |
|---|---|---|---|
| APPLE COMPUTER INC. | D | 051-6949 | 09 |
| | SCALE NONE | SHT 49 OF 111 | |



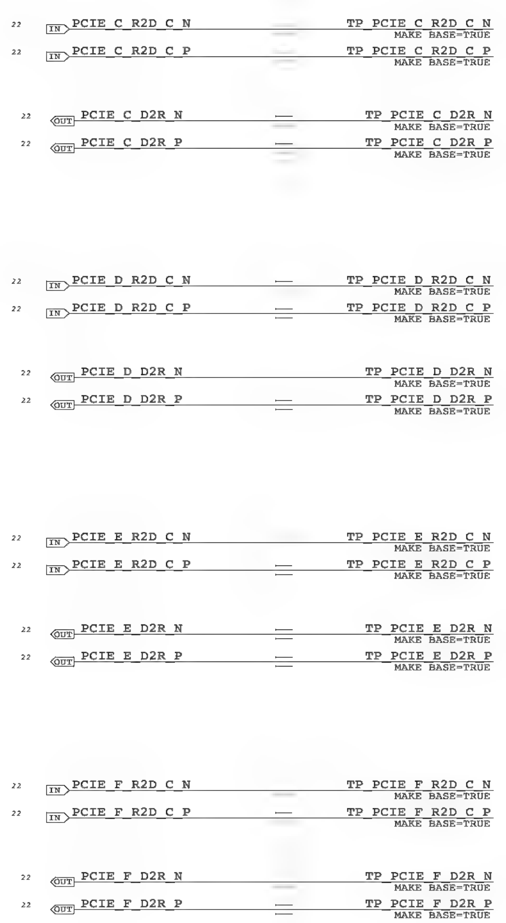

# AIRPORT CONN

SYNC_MASTER=N/A SYNC_DATE=N/A



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 53 OF 111 | |




# PCIE UNUSED PORTS

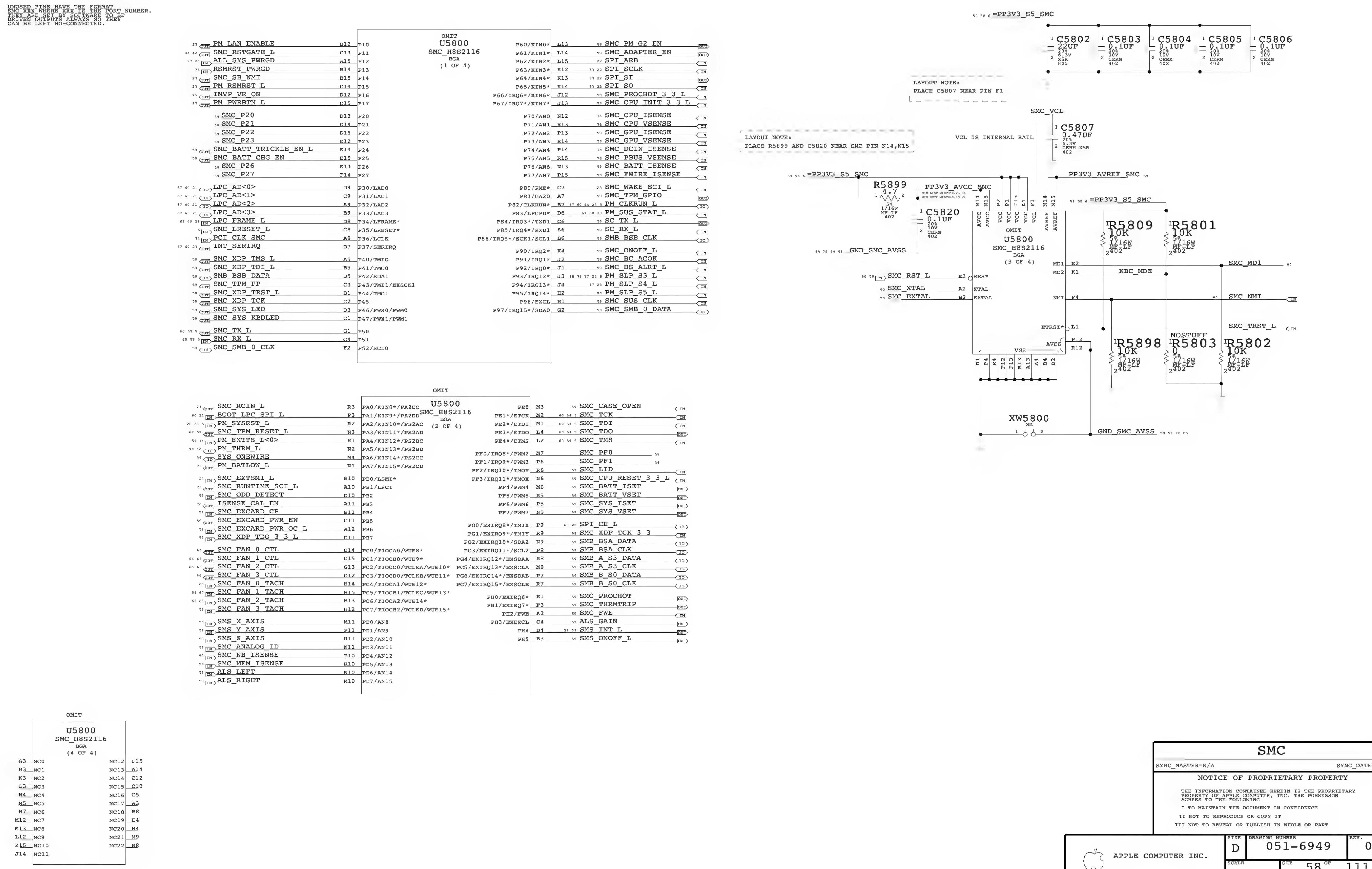
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APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 54 OF 111




| SMC | |
|---|---|
| SYNC_MASTER=N/A | SYNC_DATE=N/A |

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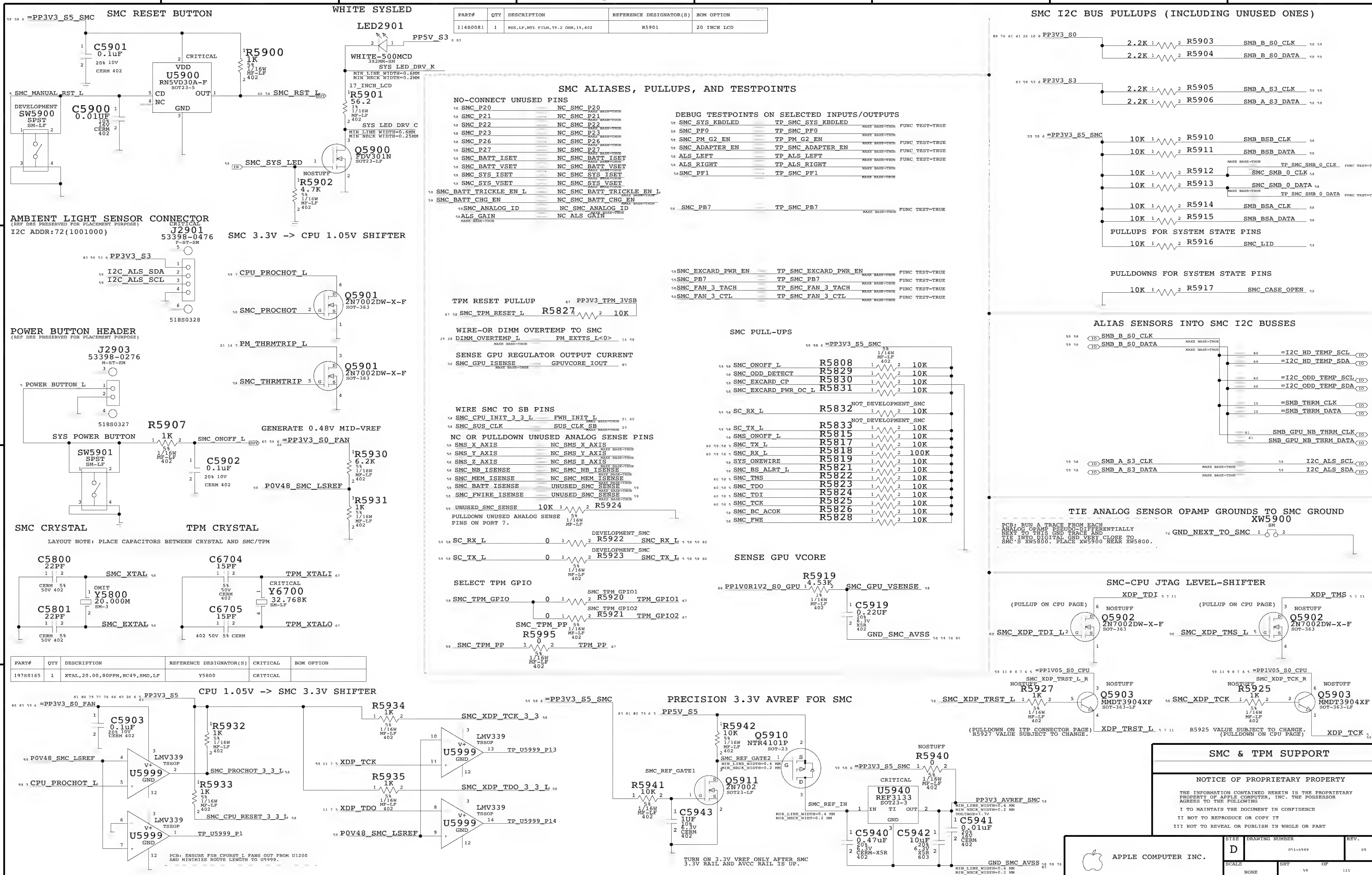
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

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APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 58 OF 111 | |



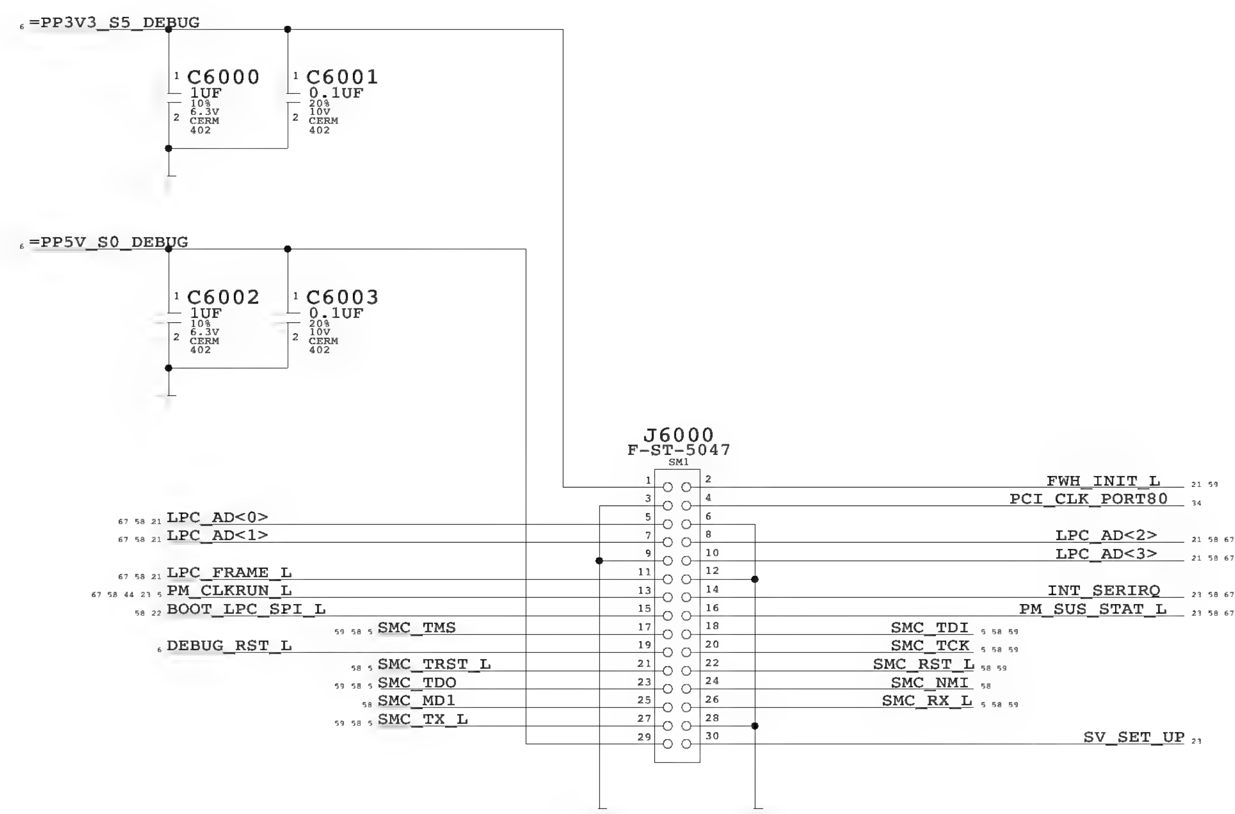

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | BOM OPTION |
|---|---|---|---|---|
| 114S0081 | 1 | RES,LF,MTL FILM,59.2 OHM,1%,402 | R5901 | 20 INCH LCD |

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 197S0165 | 1 | XTAL,20.00,80PPM,8C49,SMD,LF | Y5800 | CRITICAL | |

SMC & TPM SUPPORT



APPLE COMPUTER INC.




# LPC+ CONN

SYNC_MASTER=N/A SYNC_DATE=N/A

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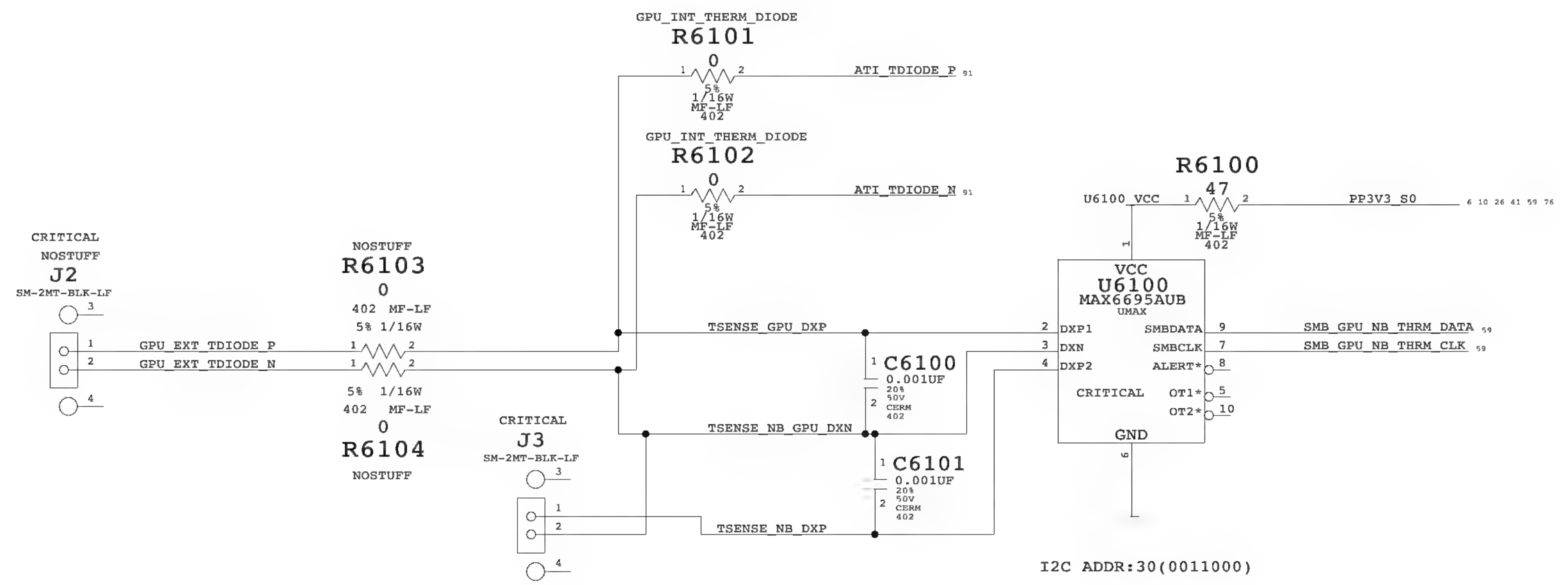
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APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 60 OF 111 | |



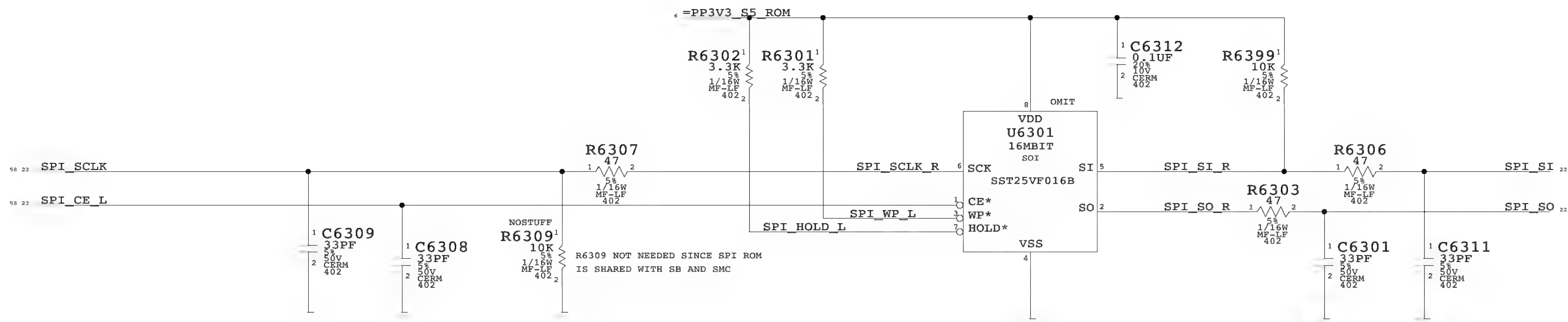

# GPU+NB THERMAL

SYNC_MASTER=N/A SYNC_DATE=N/A



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 61 OF 111 | |




R6306-07 SHOULD BE PLACED LESS THAN 2.54MM FROM U2100

R6303 SHOULD BE PLACED LESS THAN 2.54MM FROM U6301

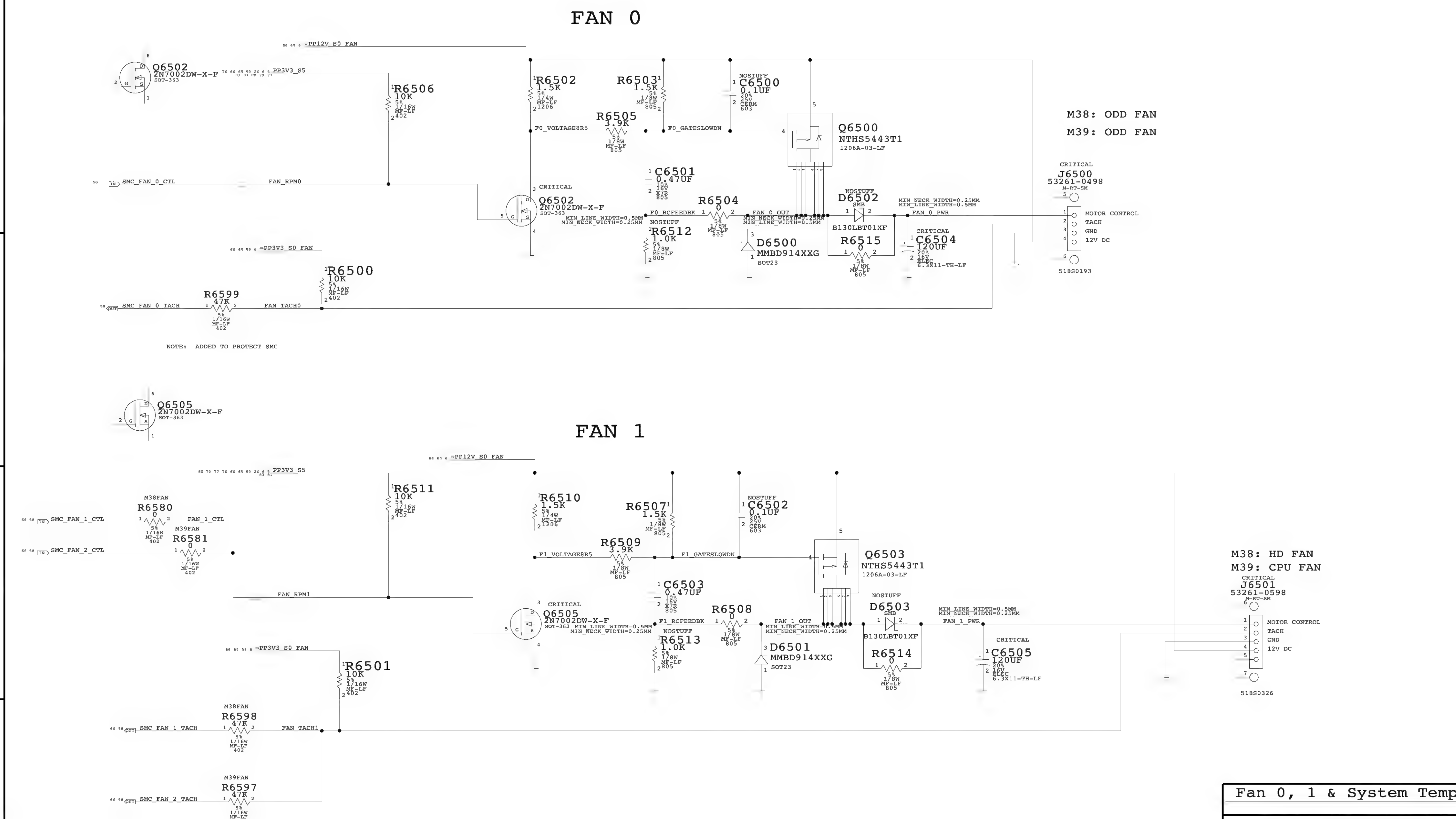
# SPI BOOTROM

SYNC_MASTER=MASTER SYNC_DATE=5/23/05



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 63 | OF 111 |




# Fan 0, 1 & System Temp

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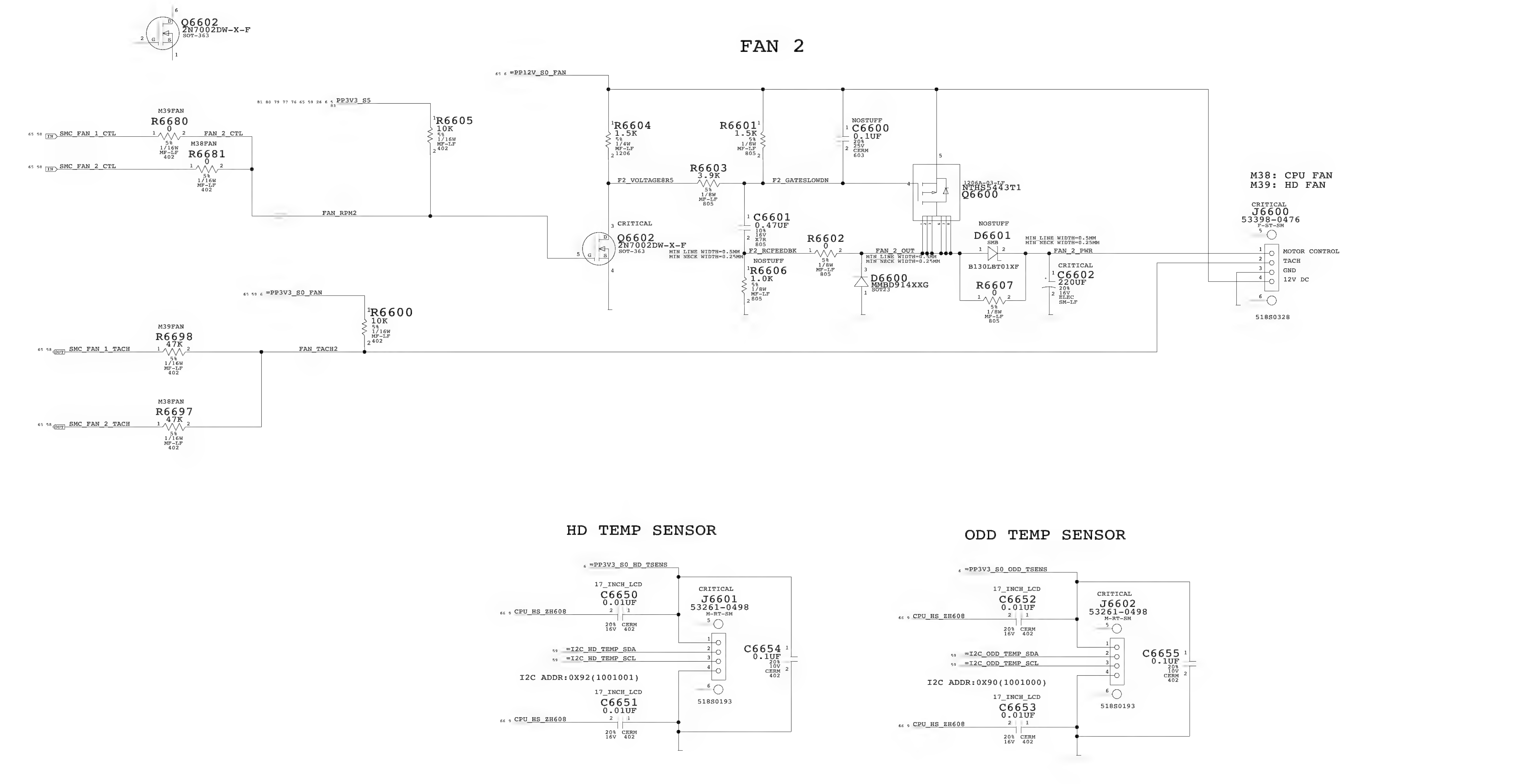
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
| --- | --- | --- |
| D | 051-6949 | 09 |

SCALE NONE — SHT 65 OF 111

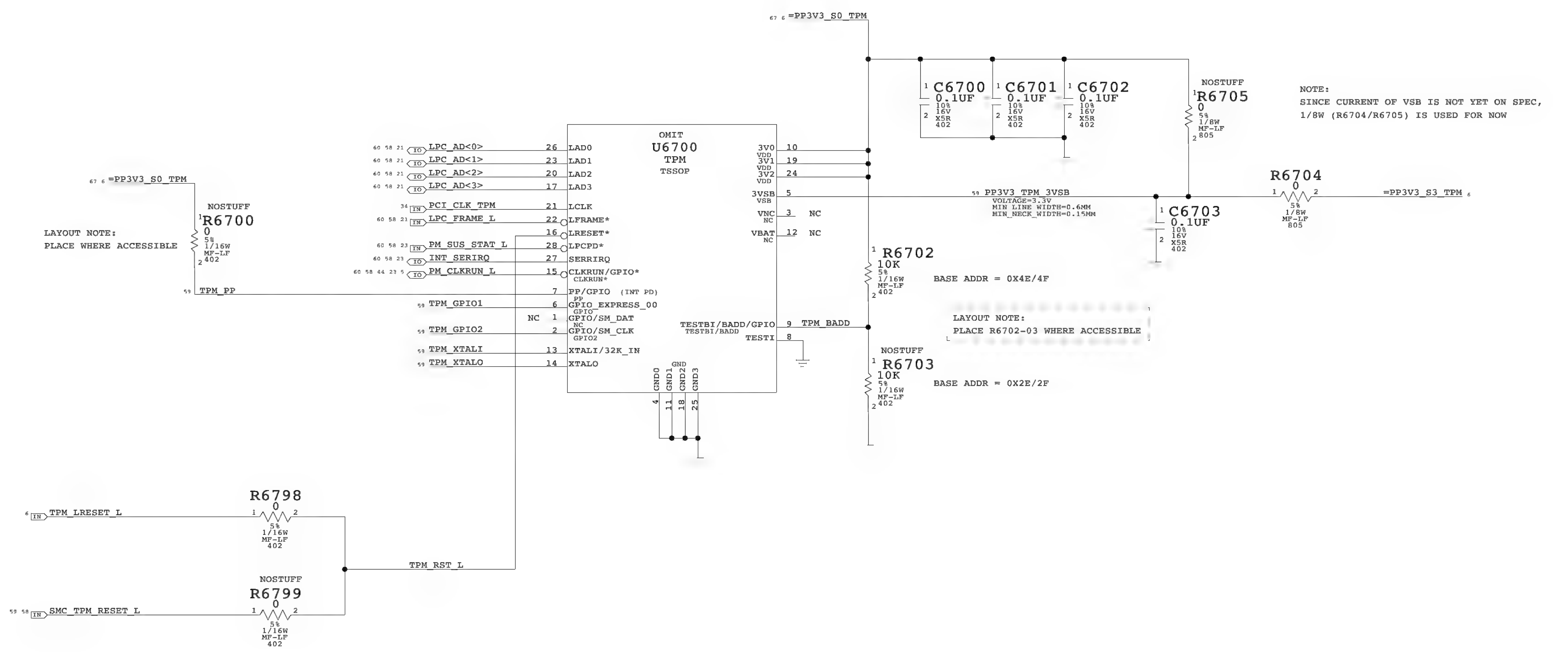



# Fan 2 & HD Temp



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 66 OF 111 | |



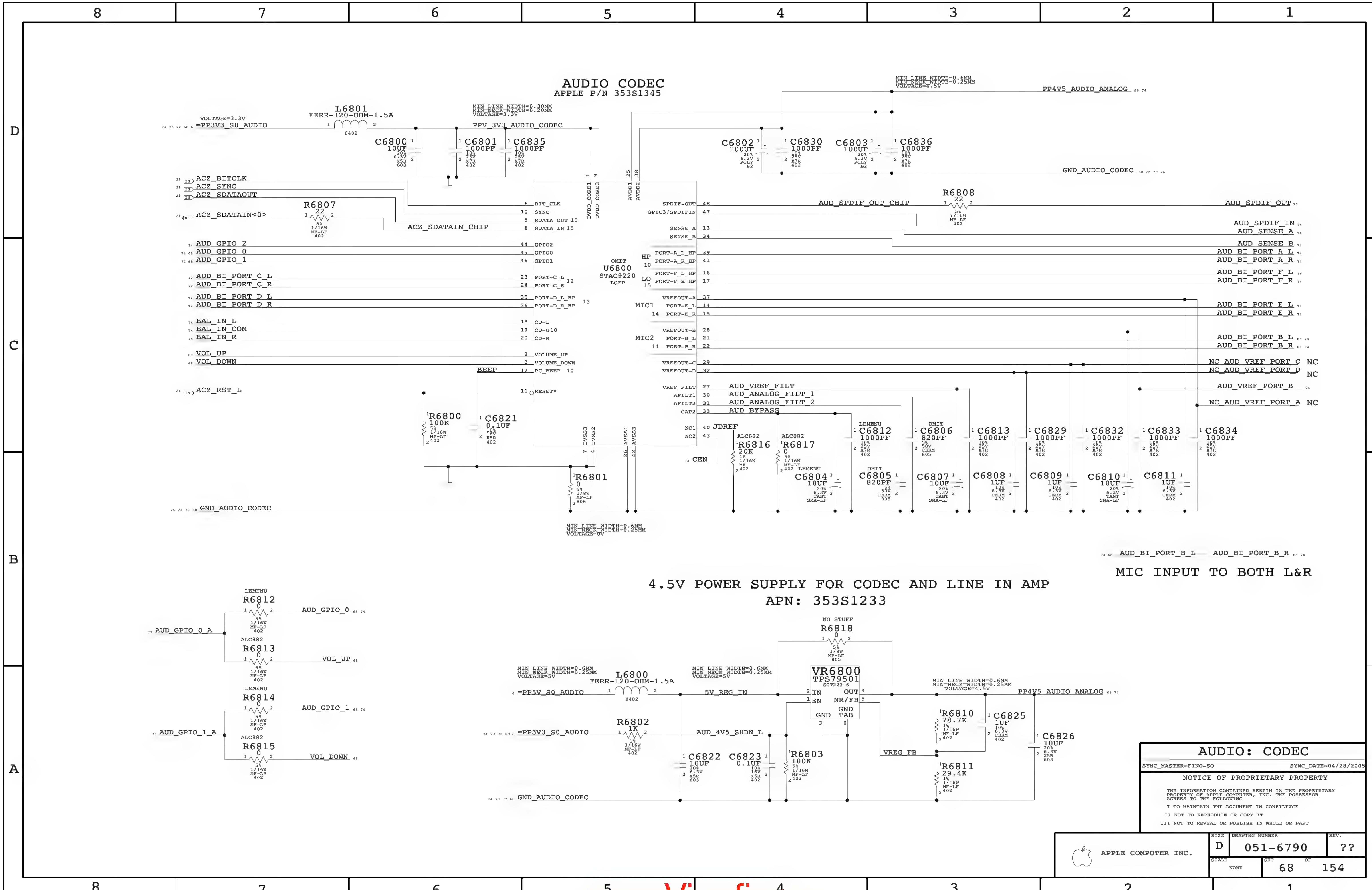

# TPM

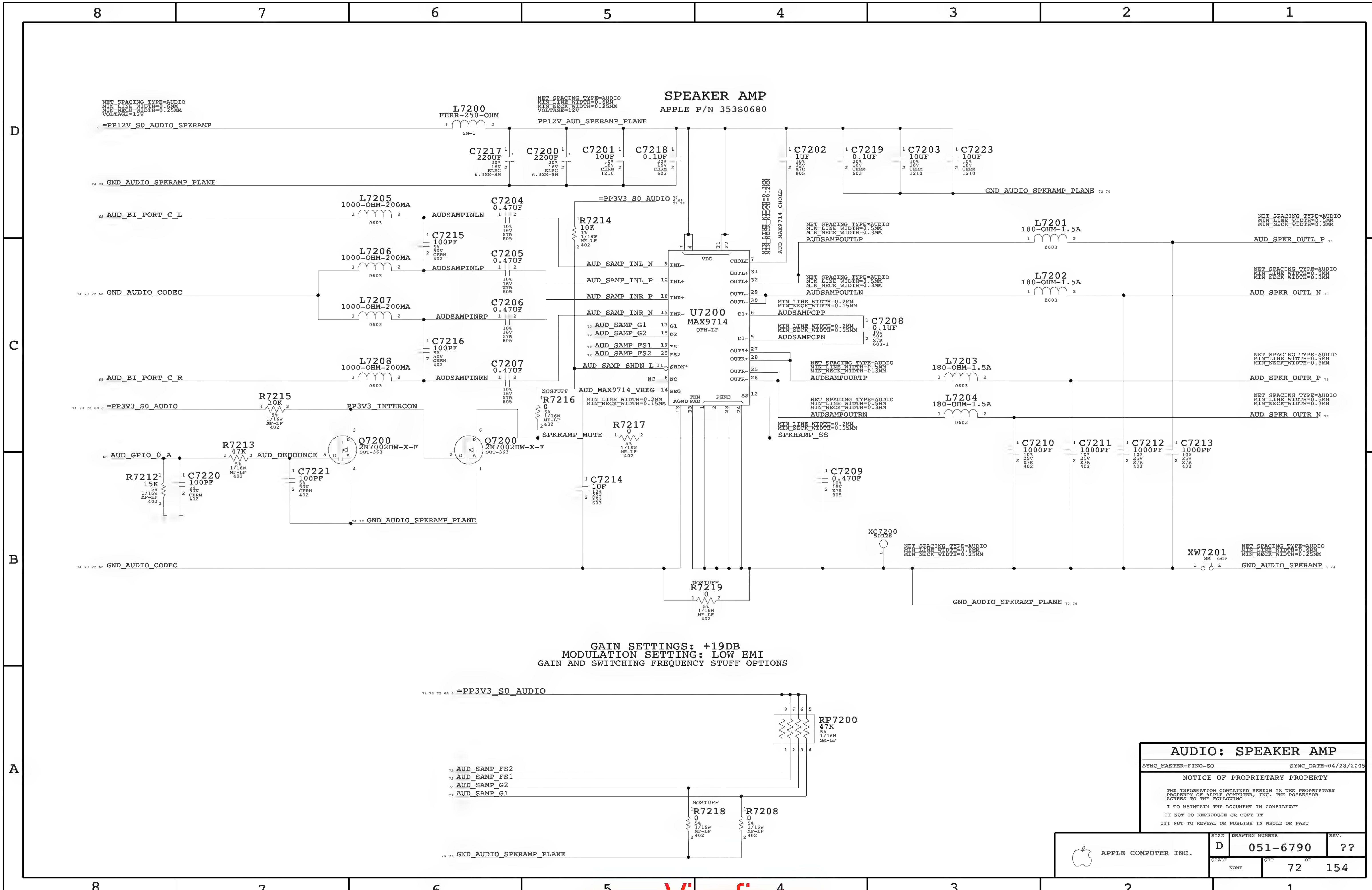
SYNC_MASTER=N/A SYNC_DATE=N/A

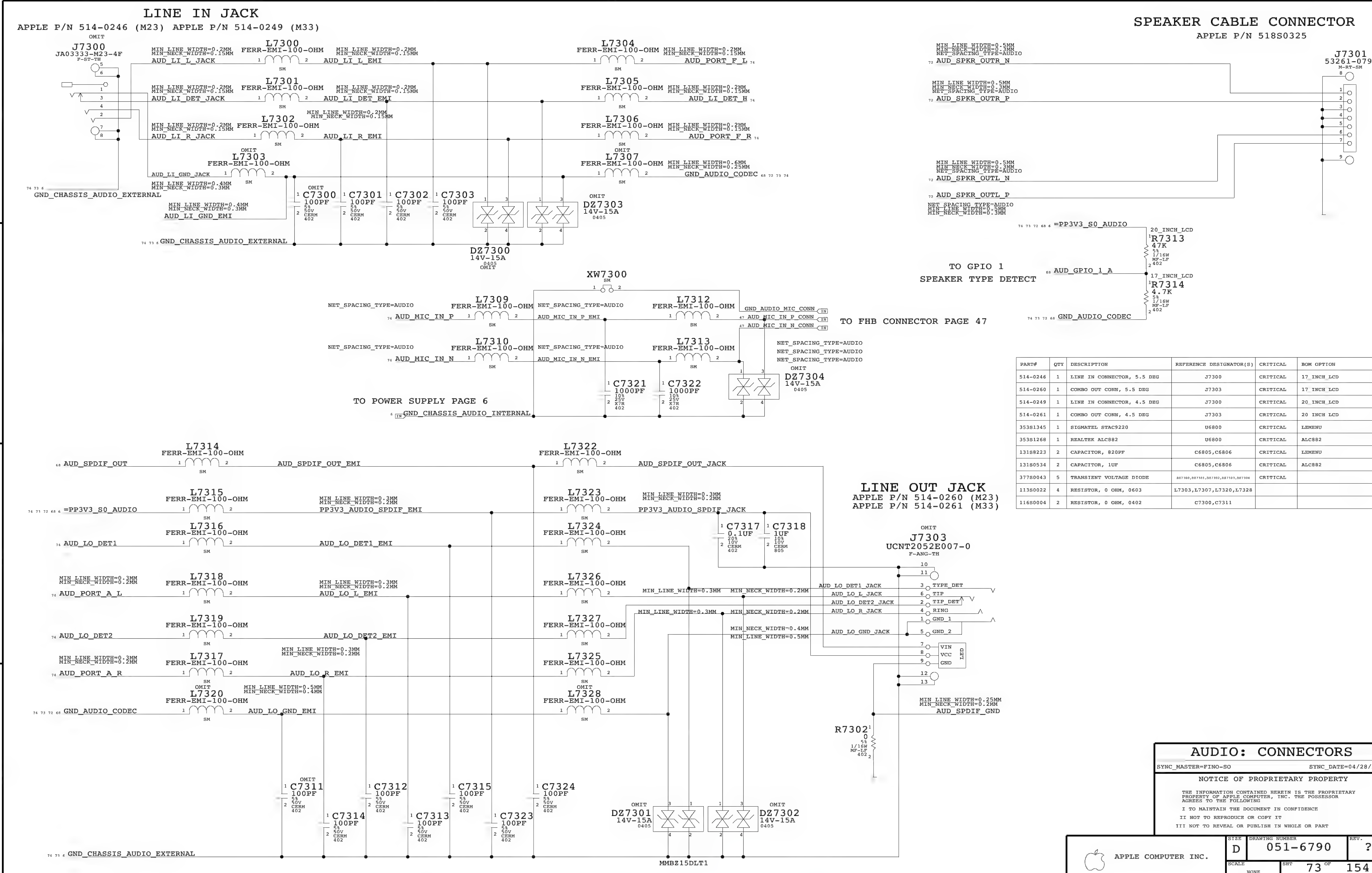


APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 67 OF 111 | |


AUDIO CODEC
APPLE P/N 353S1345
L6801
FERR-120-OHM-1.5A
=PP3V3_S0_AUDIO
PPV_3V3_AUDIO_CODEC
PP4V5_AUDIO_ANALOG
GND_AUDIO_CODEC
ACZ_BITCLK
ACZ_SYNC
ACZ_SDATAOUT
ACZ_SDATAIN<0>
ACZ_SDATAIN_CHIP
R6807
AUD_GPIO_2
AUD_GPIO_0
AUD_GPIO_1
AUD_BI_PORT_C_L
AUD_BI_PORT_C_R
AUD_BI_PORT_D_L
AUD_BI_PORT_D_R
BAL_IN_L
BAL_IN_COM
BAL_IN_R
VOL_UP
VOL_DOWN
BEEP
ACZ_RST_L
U6800
STAC9220
LQFP
AUD_SPDIF_OUT_CHIP
R6808
AUD_SPDIF_OUT
AUD_SPDIF_IN
AUD_SENSE_A
AUD_SENSE_B
AUD_BI_PORT_A_L
AUD_BI_PORT_A_R
AUD_BI_PORT_F_L
AUD_BI_PORT_F_R
AUD_BI_PORT_E_L
AUD_BI_PORT_E_R
AUD_BI_PORT_B_L
AUD_BI_PORT_B_R
NC_AUD_VREF_PORT_C
NC_AUD_VREF_PORT_D
AUD_VREF_PORT_B
NC_AUD_VREF_PORT_A
AUD_VREF_FILT
AUD_ANALOG_FILT_1
AUD_ANALOG_FILT_2
AUD_BYPASS
JDREF
CEN
R6800
C6821
R6801
R6816
R6817
C6800
C6801
C6835
C6802
C6830
C6803
C6836
C6812
C6806
C6813
C6829
C6832
C6833
C6834
C6804
C6805
C6807
C6808
C6809
C6810
C6811
MIC INPUT TO BOTH L&R
4.5V POWER SUPPLY FOR CODEC AND LINE IN AMP
APN: 353S1233
R6812
AUD_GPIO_0_A
R6813
R6814
AUD_GPIO_1_A
R6815
L6800
FERR-120-OHM-1.5A
=PP5V_S0_AUDIO
5V_REG_IN
R6818
VR6800
TPS79501
SOT223-6
R6802
AUD_4V5_SHDN_L
VREG_FB
R6810
R6811
R6803
C6822
C6823
C6825
C6826
PP4V5_AUDIO_ANALOG
AUDIO: CODEC
SYNC_MASTER=FINO-SO
SYNC_DATE=04/28/2005
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154


SPEAKER AMP
APPLE P/N 353S0680
=PP12V_S0_AUDIO_SPKRAMP
L7200
FERR-250-OHM
PP12V_AUD_SPKRAMP_PLANE
C7217 220UF
C7200 220UF
C7201 10UF
C7218 0.1UF
C7202 1UF
C7219 0.1UF
C7203 10UF
C7223 10UF
GND_AUDIO_SPKRAMP_PLANE
AUD_BI_PORT_C_L
L7205 1000-OHM-200MA
AUDSAMPINLN
C7204 0.47UF
C7215 100PF
L7206 1000-OHM-200MA
AUDSAMPINLP
C7205 0.47UF
GND_AUDIO_CODEC
L7207 1000-OHM-200MA
AUDSAMPINRP
C7206 0.47UF
C7216 100PF
L7208 1000-OHM-200MA
AUDSAMPINRN
C7207 0.47UF
AUD_BI_PORT_C_R
=PP3V3_S0_AUDIO
R7214 10K
AUD_SAMP_INL_N
AUD_SAMP_INL_P
AUD_SAMP_INR_P
AUD_SAMP_INR_N
AUD_SAMP_G1
AUD_SAMP_G2
AUD_SAMP_FS1
AUD_SAMP_FS2
AUD_SAMP_SHDN_L
AUD_MAX9714_VREG
AUD_MAX9714_CHOLD
U7200
MAX9714
QFN-LF
AUDSAMPOUTLP
AUDSAMPOUTLN
AUDSAMPCPP
AUDSAMPCPN
C7208 0.1UF
AUDSAMPOUTRP
AUDSAMPOUTRN
SPKRAMP_SS
L7201 180-OHM-1.5A
AUD_SPKR_OUTL_P
L7202 180-OHM-1.5A
AUD_SPKR_OUTL_N
L7203 180-OHM-1.5A
AUD_SPKR_OUTR_P
L7204 180-OHM-1.5A
AUD_SPKR_OUTR_N
C7210 1000PF
C7211 1000PF
C7212 1000PF
C7213 1000PF
R7215 10K
PP3V3_INTERCON
Q7200 2N7002DW-X-F
Q7200 2N7002DW-X-F
R7216 0
SPKRAMP_MUTE
R7217 0
AUD_GPIO_0_A
R7213 47K
AUD_DEBOUNCE
R7212 15K
C7220 100PF
C7221 100PF
C7214 1UF
C7209 0.47UF
XC7200
XW7201
GND_AUDIO_SPKRAMP
R7219 0
GAIN SETTINGS: +19DB
MODULATION SETTING: LOW EMI
GAIN AND SWITCHING FREQUENCY STUFF OPTIONS
RP7200 47K
R7218 0
R7208 0
AUDIO: SPEAKER AMP
SYNC_MASTER=FINO-SO
SYNC_DATE=04/28/2005
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72
154




| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 514-0246 | 1 | LINE IN CONNECTOR, 5.5 DEG | J7300 | CRITICAL | 17_INCH_LCD |
| 514-0260 | 1 | COMBO OUT CONN, 5.5 DEG | J7303 | CRITICAL | 17_INCH_LCD |
| 514-0249 | 1 | LINE IN CONNECTOR, 4.5 DEG | J7300 | CRITICAL | 20_INCH_LCD |
| 514-0261 | 1 | COMBO OUT CONN, 4.5 DEG | J7303 | CRITICAL | 20 INCH LCD |
| 353S1345 | 1 | SIGMATEL STAC9220 | U6800 | CRITICAL | LEMENU |
| 353S1268 | 1 | REALTEK ALC882 | U6800 | CRITICAL | ALC882 |
| 131S8223 | 2 | CAPACITOR, 820PF | C6805,C6806 | CRITICAL | LEMENU |
| 131S0534 | 2 | CAPACITOR, 1UF | C6805,C6806 | CRITICAL | ALC882 |
| 377S0043 | 5 | TRANSIENT VOLTAGE DIODE | DZ7300,DZ7301,DZ7302,DZ7303,DZ7304 | CRITICAL | |
| 113S0022 | 4 | RESISTOR, 0 OHM, 0603 | L7303,L7307,L7320,L7328 | | |
| 116S0004 | 2 | RESISTOR, 0 OHM, 0402 | C7300,C7311 | | |

# AUDIO: CONNECTORS

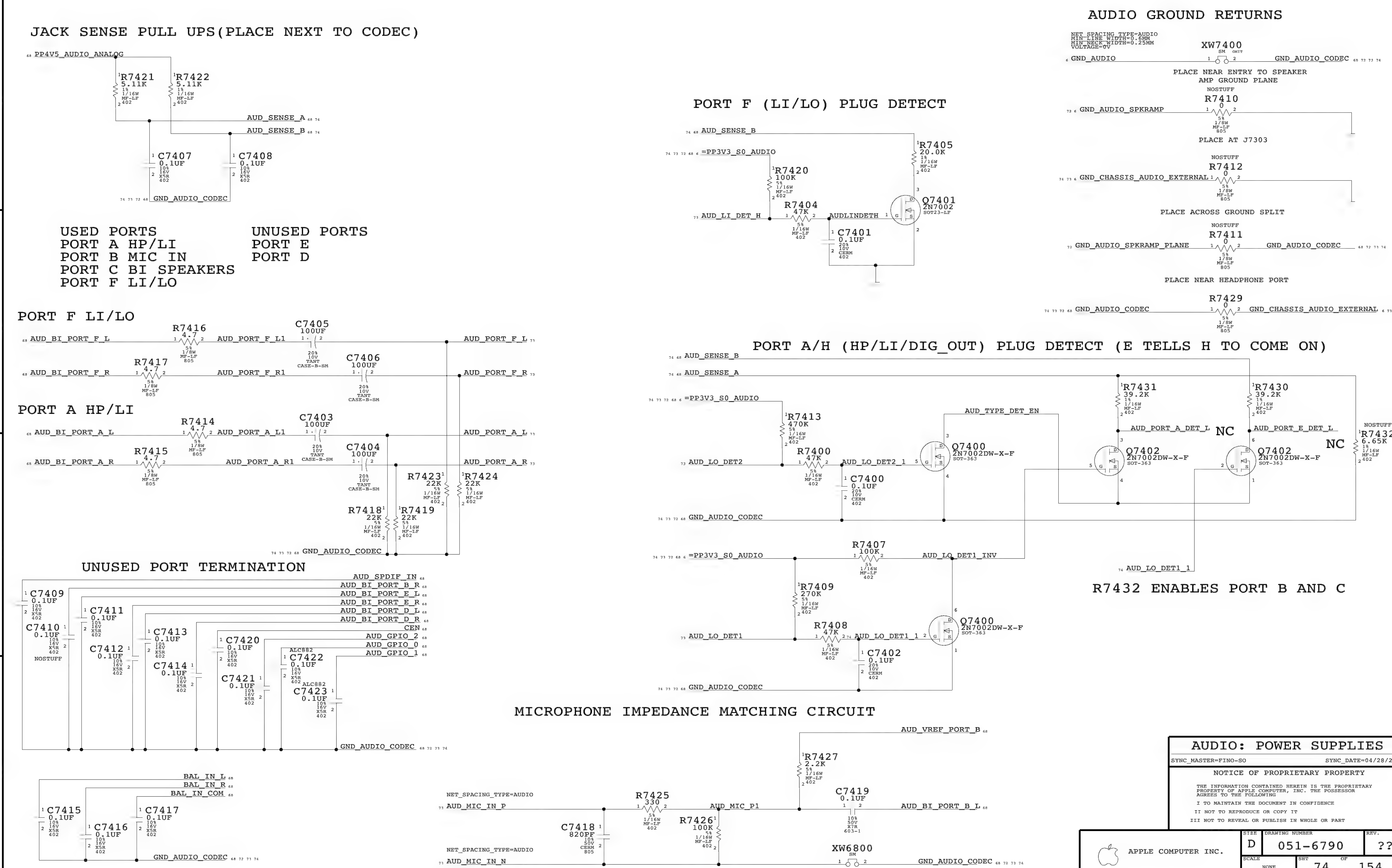
SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005



APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6790 | REV. ??

SCALE NONE | SHT 73 OF 154




AUDIO: POWER SUPPLIES

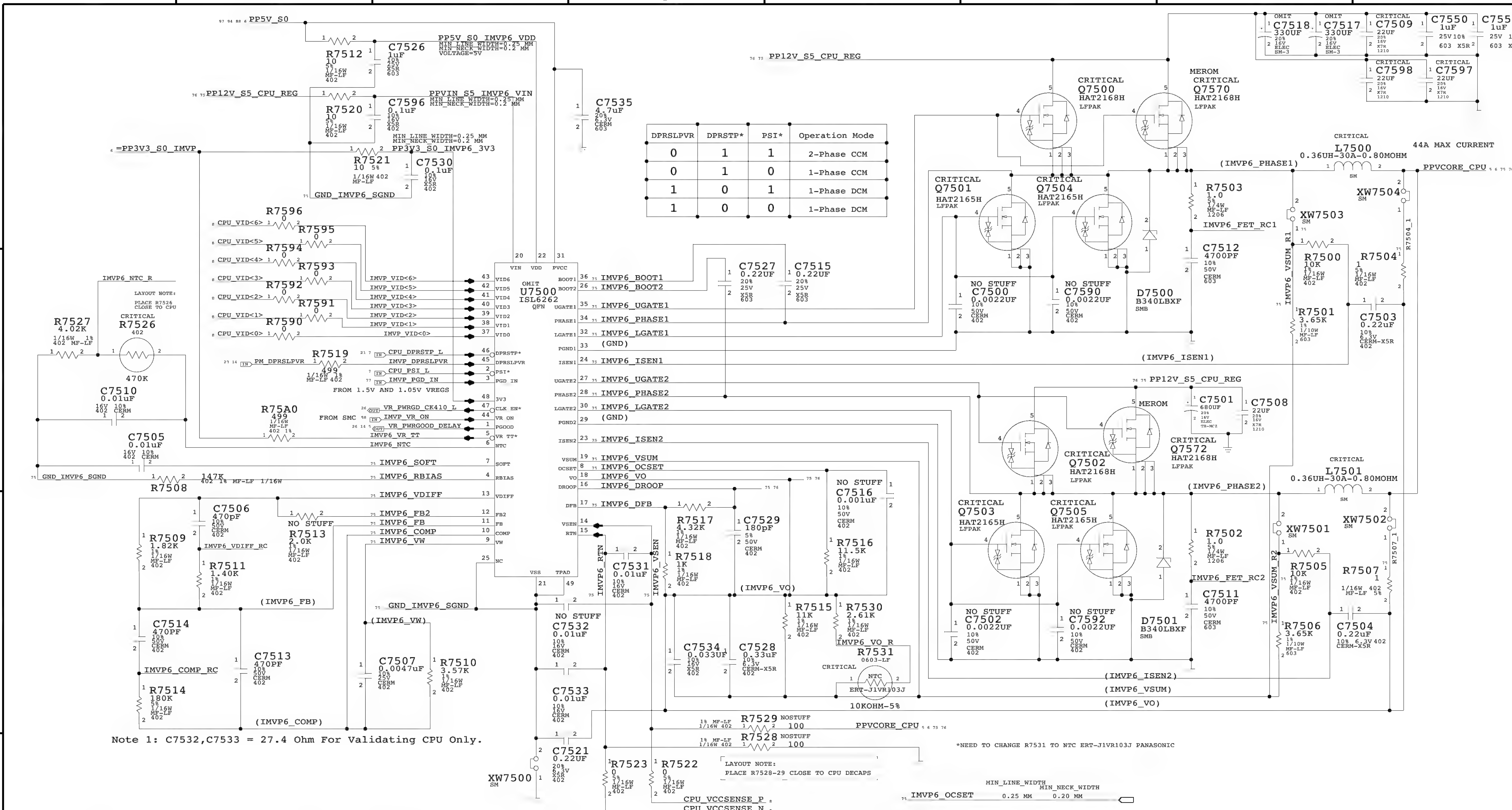
SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6790 | ?? |
| SCALE NONE | SHT 74 | OF 154 |

# IMVP6 CPU VCORE REGULATOR



| DPRSLPVR | DPRSTP* | PSI* | Operation Mode |
|---|---|---|---|
| 0 | 1 | 1 | 2-Phase CCM |
| 0 | 1 | 0 | 1-Phase CCM |
| 1 | 0 | 1 | 1-Phase DCM |
| 1 | 0 | 0 | 1-Phase DCM |

Note 1: C7532,C7533 = 27.4 Ohm For Validating CPU Only.

*NEED TO CHANGE R7531 TO NTC ERT-J1VR103J PANASONIC

LAYOUT NOTE: PLACE R7528-29 CLOSE TO CPU DECAPS

CPU_VCCSENSE_P & N ARE DIFF PAIRS ROUTE AS 18MIL WIDE, 7MIL SPACE

| Net | MIN_LINE_WIDTH | MIN_NECK_WIDTH |
|---|---|---|
| IMVP6_PHASE1 | 1.5 MM | 0.25 MM |
| IMVP6_BOOT1 | 0.25 MM | 0.25 MM |
| IMVP6_UGATE1 | 1.5 MM | 0.25 MM |
| IMVP6_LGATE1 | 1.5 MM | 0.25 MM |
| IMVP6_ISEN1 | 0.25 MM | 0.25 MM |
| IMVP6_FET_RC1 | 0.25 MM | 0.25 MM |
| IMVP6_VSUM_R1 | 0.25 MM | 0.25 MM |
| R7504_1 | 0.25 MM | 0.25 MM |

| Net | MIN_LINE_WIDTH | MIN_NECK_WIDTH |
|---|---|---|
| IMVP6_PHASE2 | 0.25 MM | 0.25 MM |
| IMVP6_BOOT2 | 0.25 MM | 0.25 MM |
| IMVP6_UGATE2 | 0.25 MM | 0.25 MM |
| IMVP6_LGATE2 | 0.25 MM | 0.25 MM |
| IMVP6_ISEN2 | 0.25 MM | 0.25 MM |
| IMVP6_FET_RC2 | 0.25 MM | 0.25 MM |
| IMVP6_VSUM_R2 | 0.60 MM | 0.25 MM |
| R7507_1 | 0.25 MM | 0.25 MM |

| Net | MIN_LINE_WIDTH | MIN_NECK_WIDTH |
|---|---|---|
| IMVP6_OCSET | 0.25 MM | 0.20 MM |
| IMVP6_VSUM | 0.25 MM | 0.20 MM |
| GND_IMVP6_SGND | 0.50 MM | 0.20 MM |
| IMVP6_VO | 0.25 MM | 0.20 MM |
| IMVP6_DROOP | 0.25 MM | 0.20 MM |
| IMVP6_DFB | 0.25 MM | 0.20 MM |
| IMVP6_SOFT | 0.25 MM | 0.20 MM |
| IMVP6_RBIAS | 0.25 MM | 0.20 MM |
| IMVP6_VDIFF | 0.25 MM | 0.20 MM |
| IMVP6_FB2 | 0.25 MM | 0.20 MM |
| IMVP6_FB | 0.25 MM | 0.20 MM |
| IMVP6_COMP | 0.25 MM | 0.20 MM |
| IMVP6_VW | 0.25 MM | 0.25 MM |
| IMVP6_RTN | 0.25 MM | 0.25 MM |
| IMVP6_VSEN | 0.25 MM | 0.25 MM |

IMVP6 CPU VCore Regulator

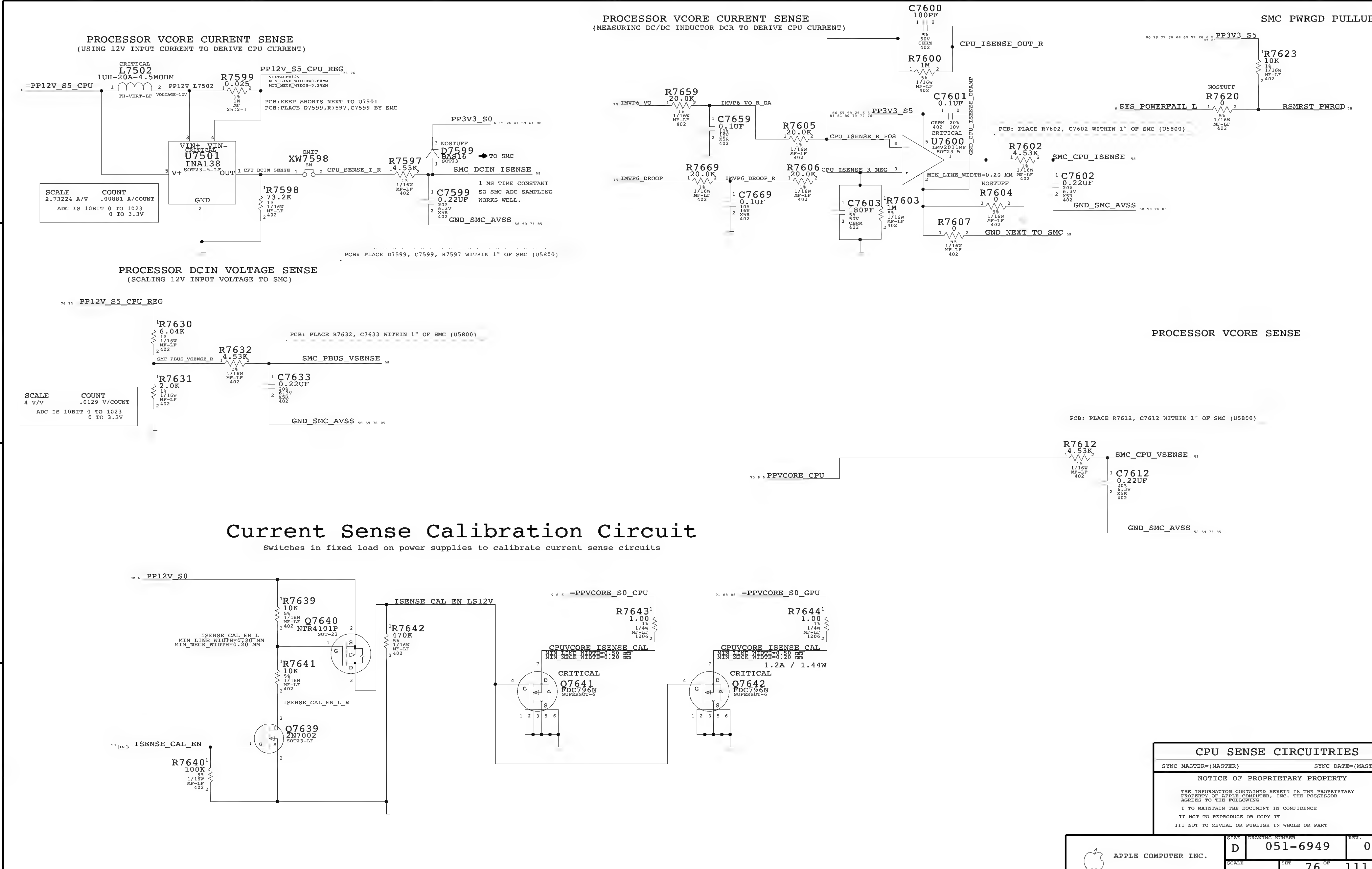
SYNC_MASTER=POWER SYNC_DATE=07/08/2005



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SIZE D DRAWING NUMBER 051-6949 REV. 09

SCALE NONE SHT 75 OF 111




CPU SENSE CIRCUITRIES

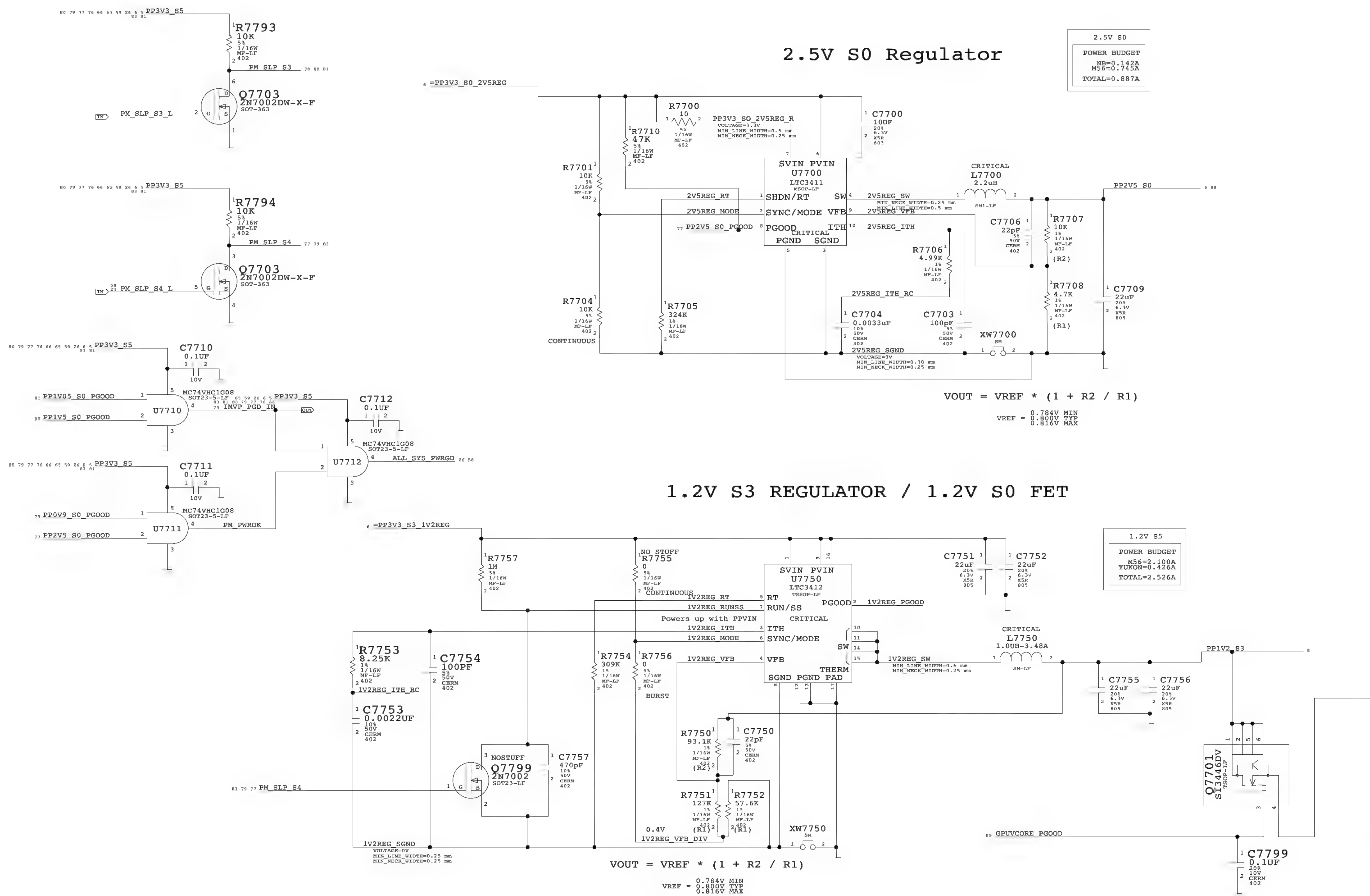
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APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 76 OF 111




## 2.5V & 1.2V GRAPHICS REGULATORS

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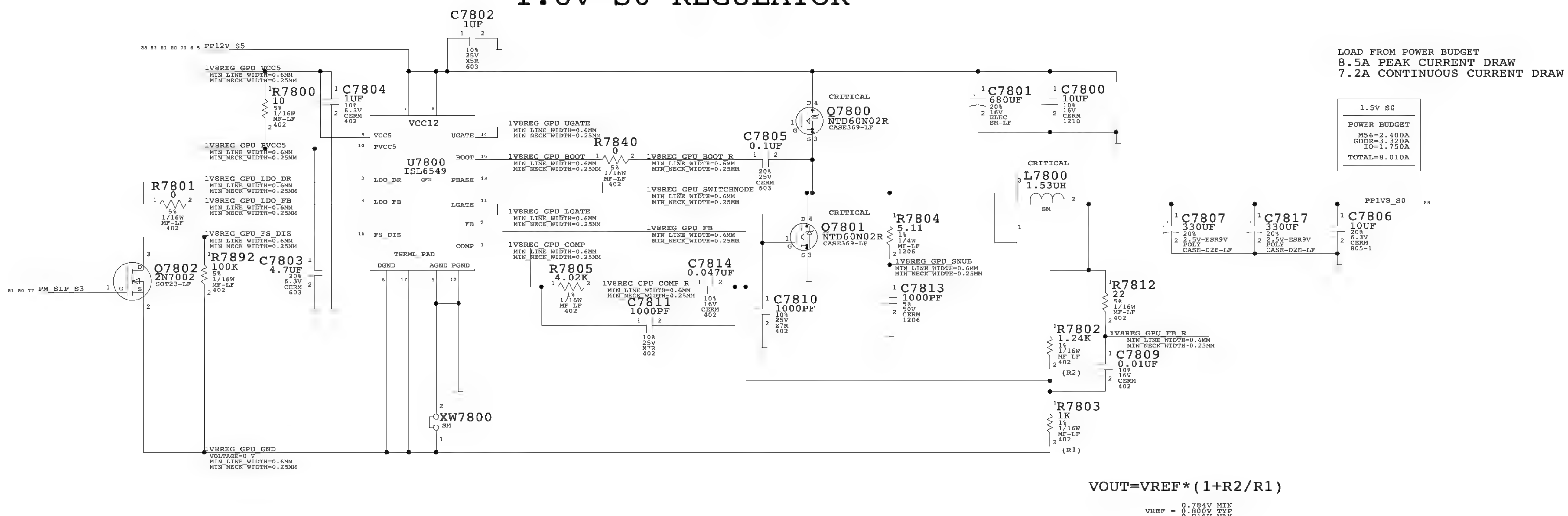


APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 77 OF 111

# 1.8V S0 REGULATOR




1.8V GDDR REGULATOR

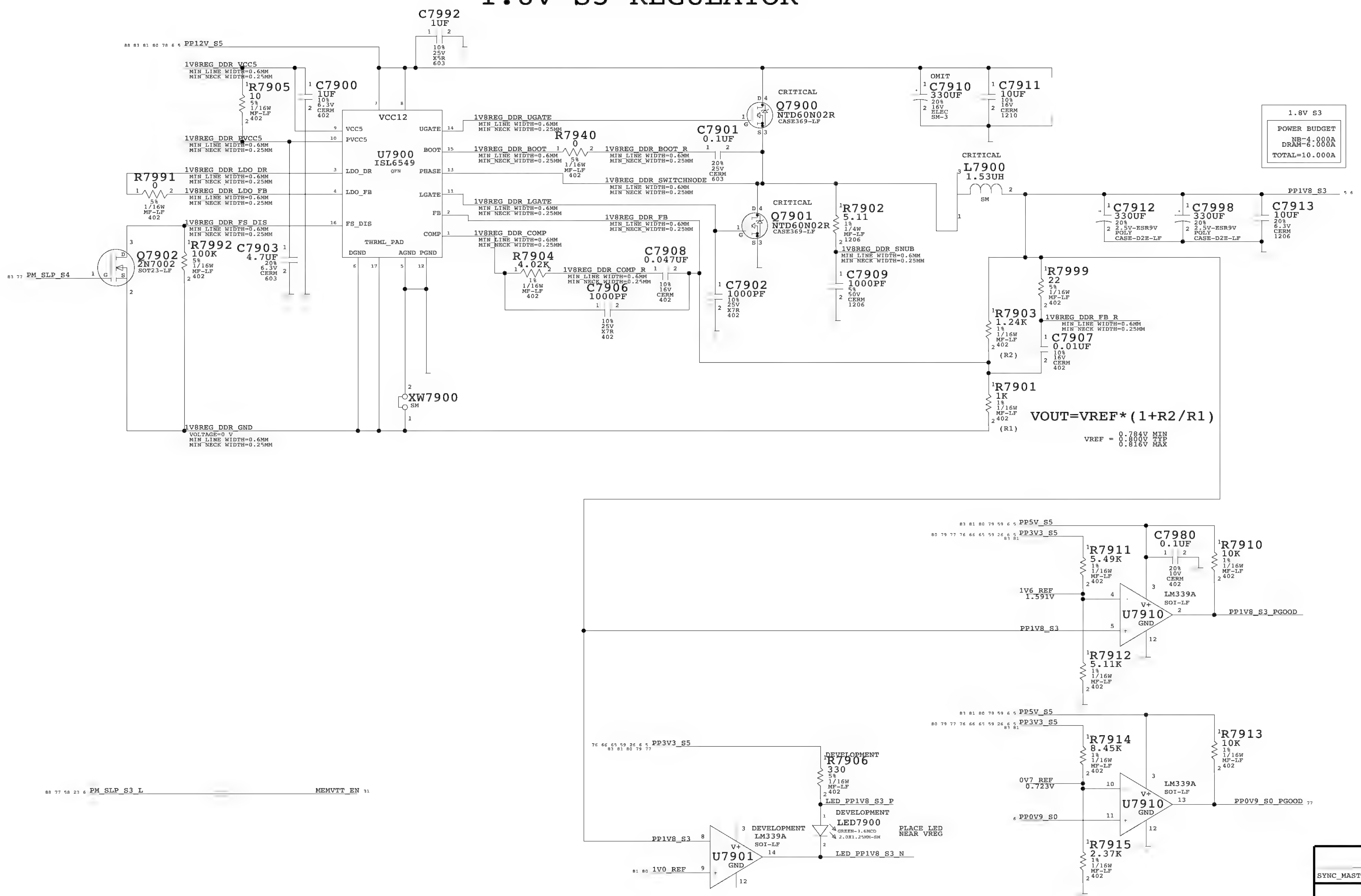
SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 78 OF 111 | |

# 1.8V S3 REGULATOR



VOUT=VREF*(1+R2/R1)

1.8V Vreg

SYNC_MASTER=M23-PC SYNC_DATE=04/12/2005

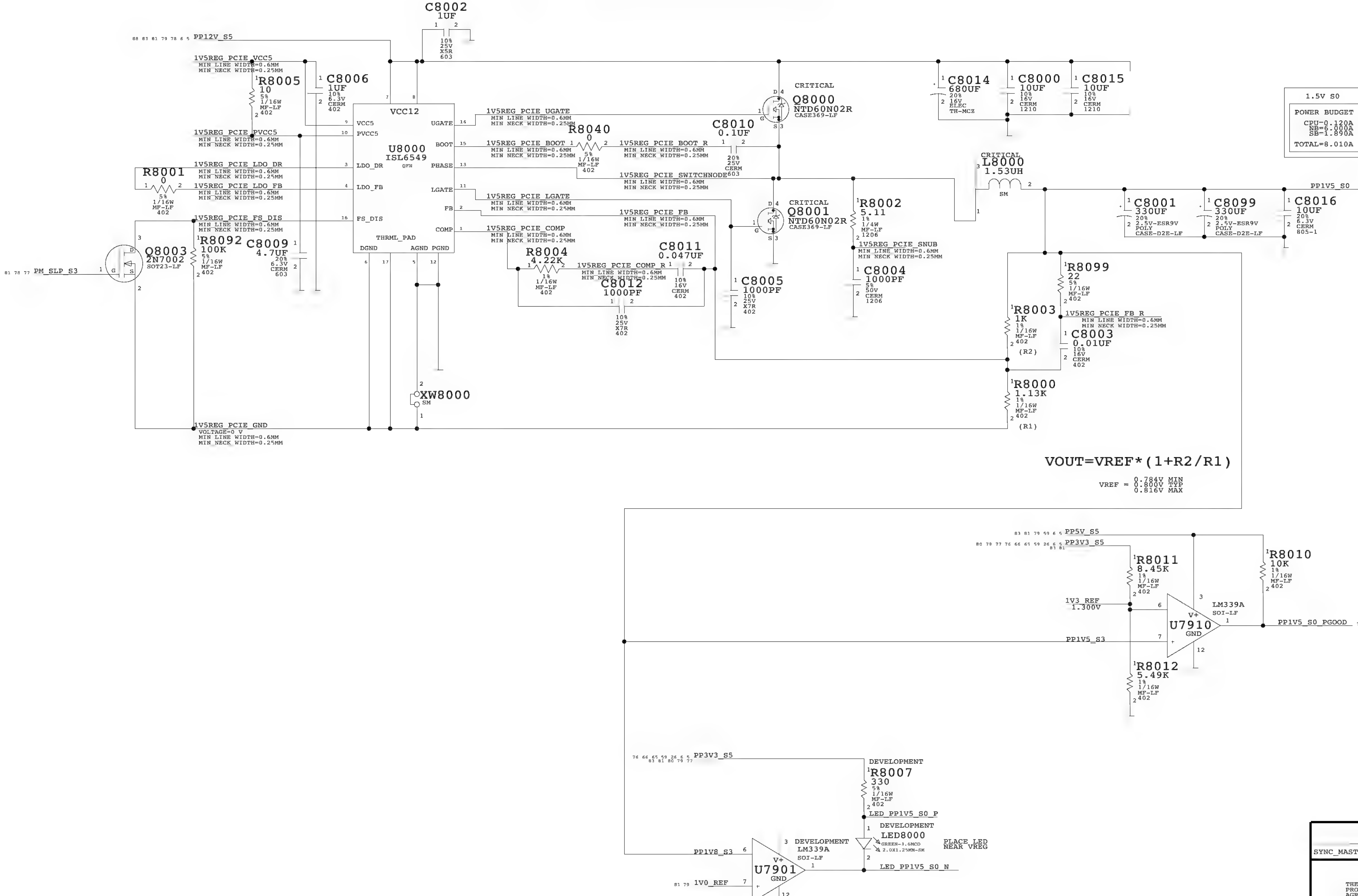


APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-6949 REV. 09

SCALE NONE SHT 79 OF 111

# 1.5V S0 REGULATOR




1.5V Vreg

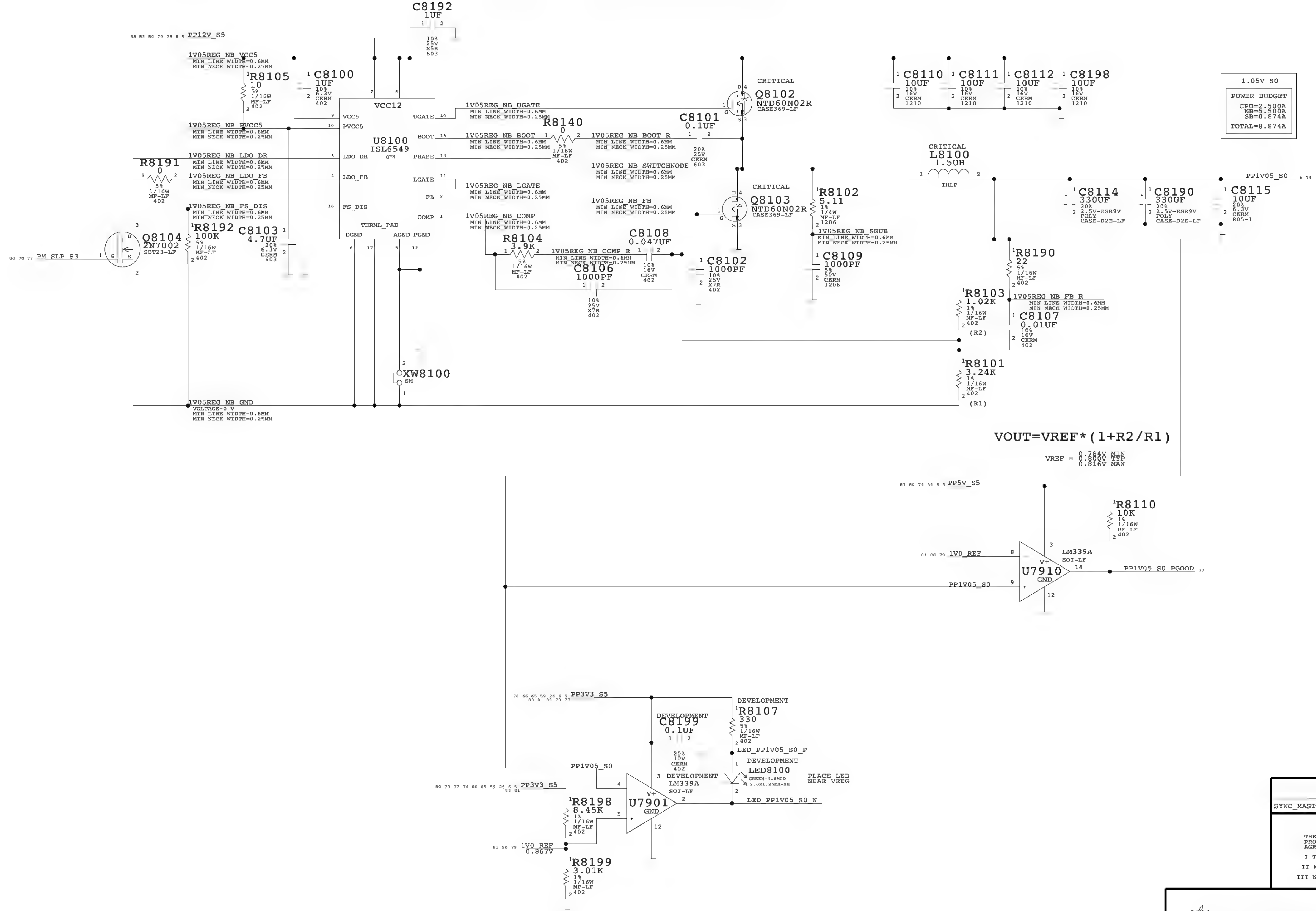
SYNC_MASTER=FINO-PC SYNC_DATE=05/18/2005



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 80 OF 111 | |

# 1.05V S0 REGULATOR



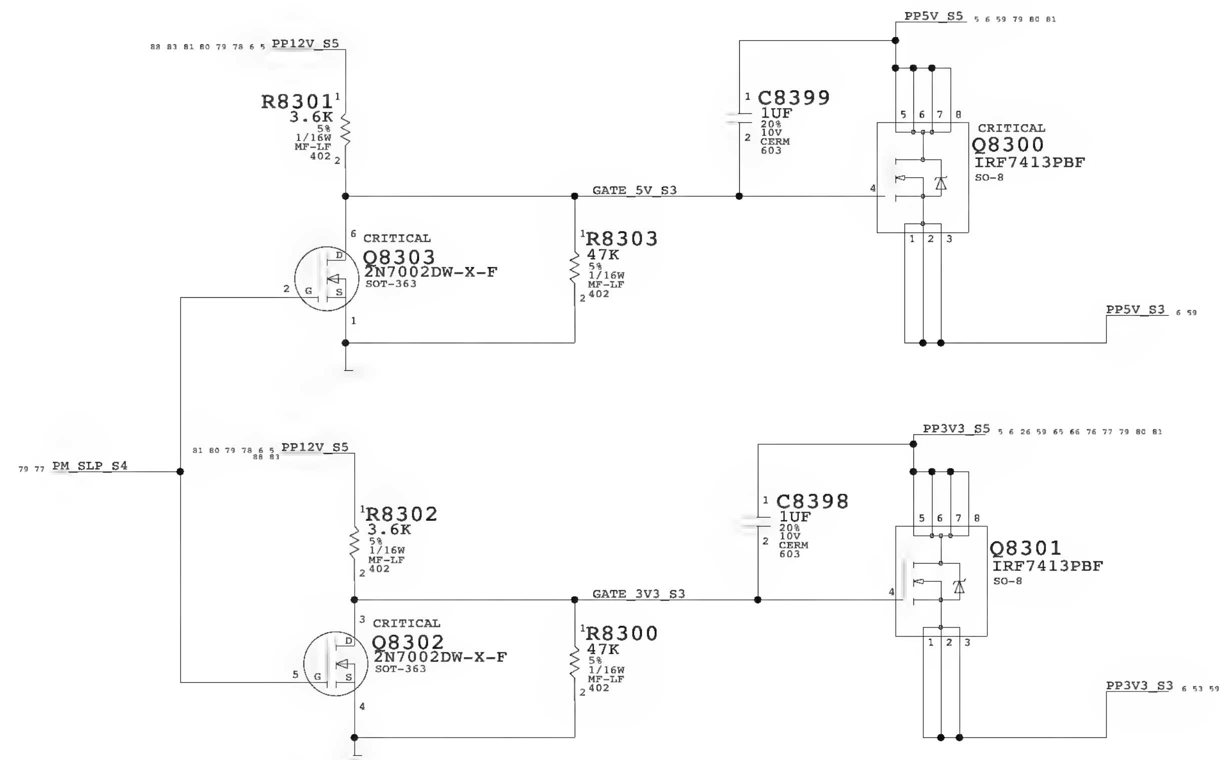

1.05V VREG

SYNC_MASTER=M38-RT SYNC_DATE=05/18/2005



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 81 OF 111 | |



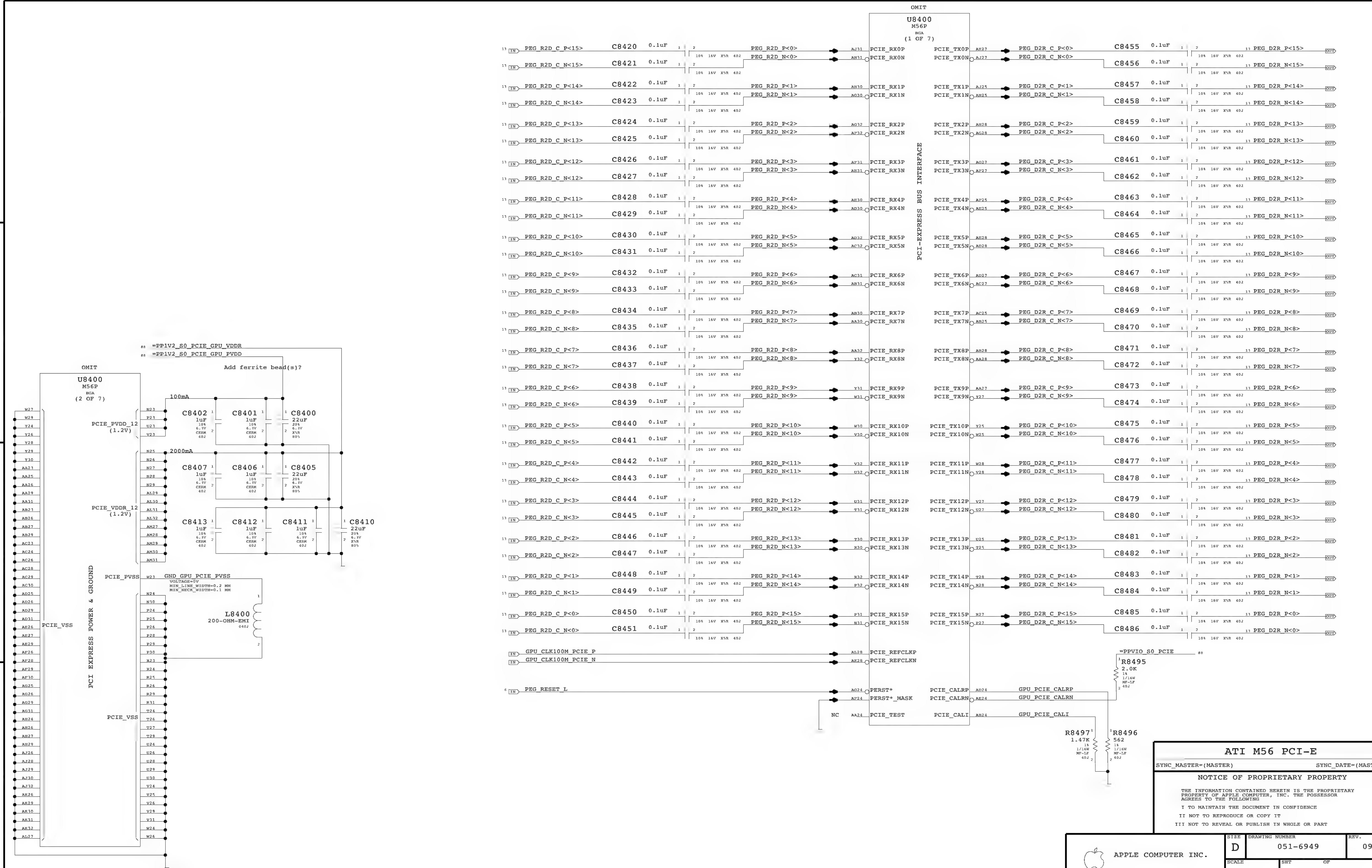

# 5V & 3.3V Fets

SYNC_MASTER=FINO-PC SYNC_DATE=04/12/2005



APPLE COMPUTER INC.

| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 83 OF 111 | |




ATI M56 PCI-E

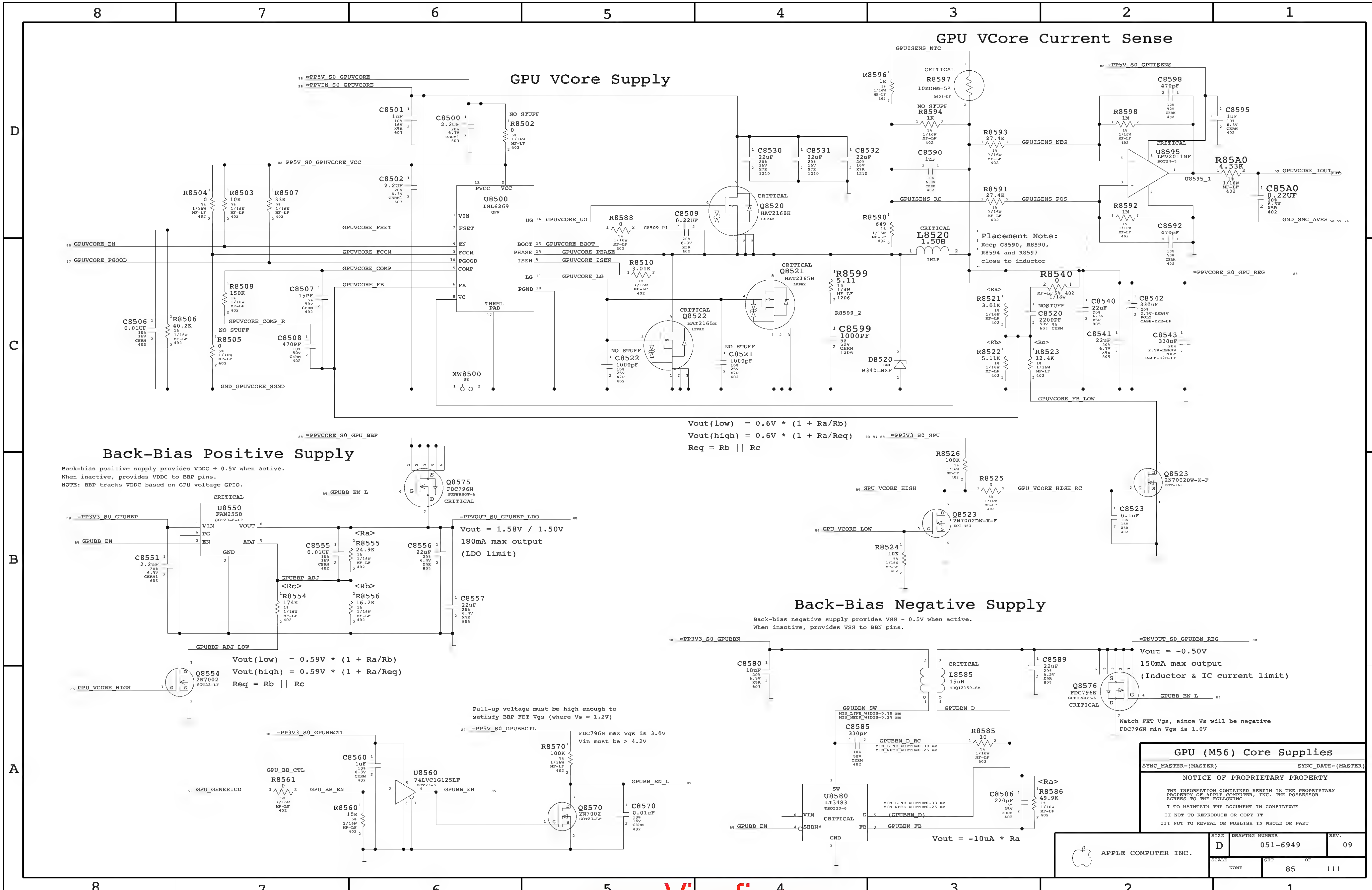
SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY



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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 84 | OF 111 |

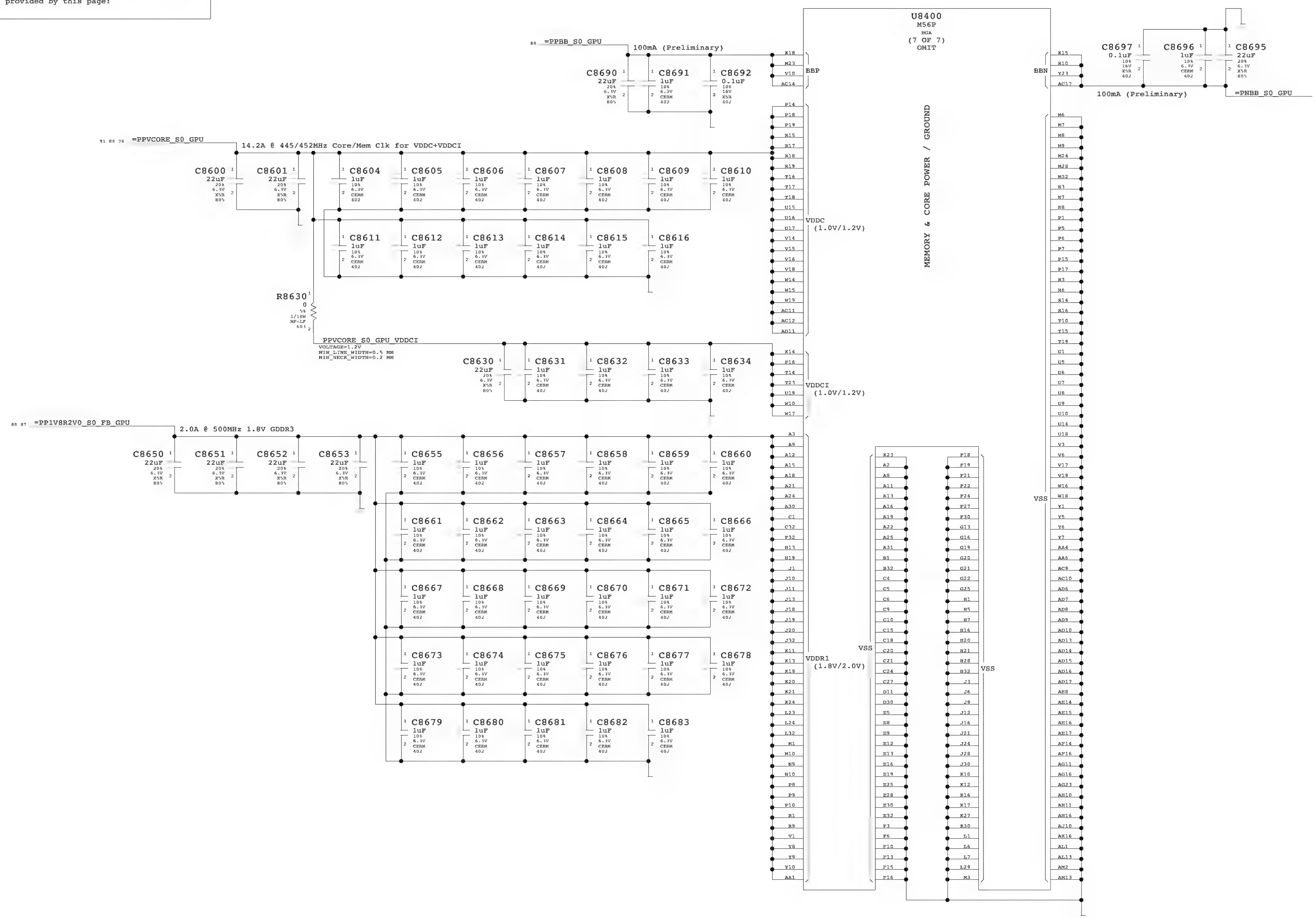

GPU VCore Current Sense
GPU VCore Supply
Placement Note:
Keep C8590, R8590,
R8594 and R8597
close to inductor
Vout(low) = 0.6V * (1 + Ra/Rb)
Vout(high) = 0.6V * (1 + Ra/Req)
Req = Rb || Rc
Back-Bias Positive Supply
Back-bias positive supply provides VDDC + 0.5V when active.
When inactive, provides VDDC to BBP pins.
NOTE: BBP tracks VDDC based on GPU voltage GPIO.
Vout = 1.58V / 1.50V
180mA max output
(LDO limit)
Vout(low) = 0.59V * (1 + Ra/Rb)
Vout(high) = 0.59V * (1 + Ra/Req)
Req = Rb || Rc
Pull-up voltage must be high enough to
satisfy BBP FET Vgs (where Vs = 1.2V)
FDC796N max Vgs is 3.0V
Vin must be > 4.2V
Back-Bias Negative Supply
Back-bias negative supply provides VSS - 0.5V when active.
When inactive, provides VSS to BBN pins.
Vout = -0.50V
150mA max output
(Inductor & IC current limit)
Watch FET Vgs, since Vs will be negative
FDC796N min Vgs is 1.0V
Vout = -10uA * Ra
GPU (M56) Core Supplies
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SIZE D
DRAWING NUMBER 051-6949
REV. 09
SCALE NONE
SHT 85 OF 111

## Page Notes

Power aliases required by this page:
- =PP1V5_GPU_VDD15
- =PP1VR1V3_GPU_VCORE

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)




# ATI M56 Core Power

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)



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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |

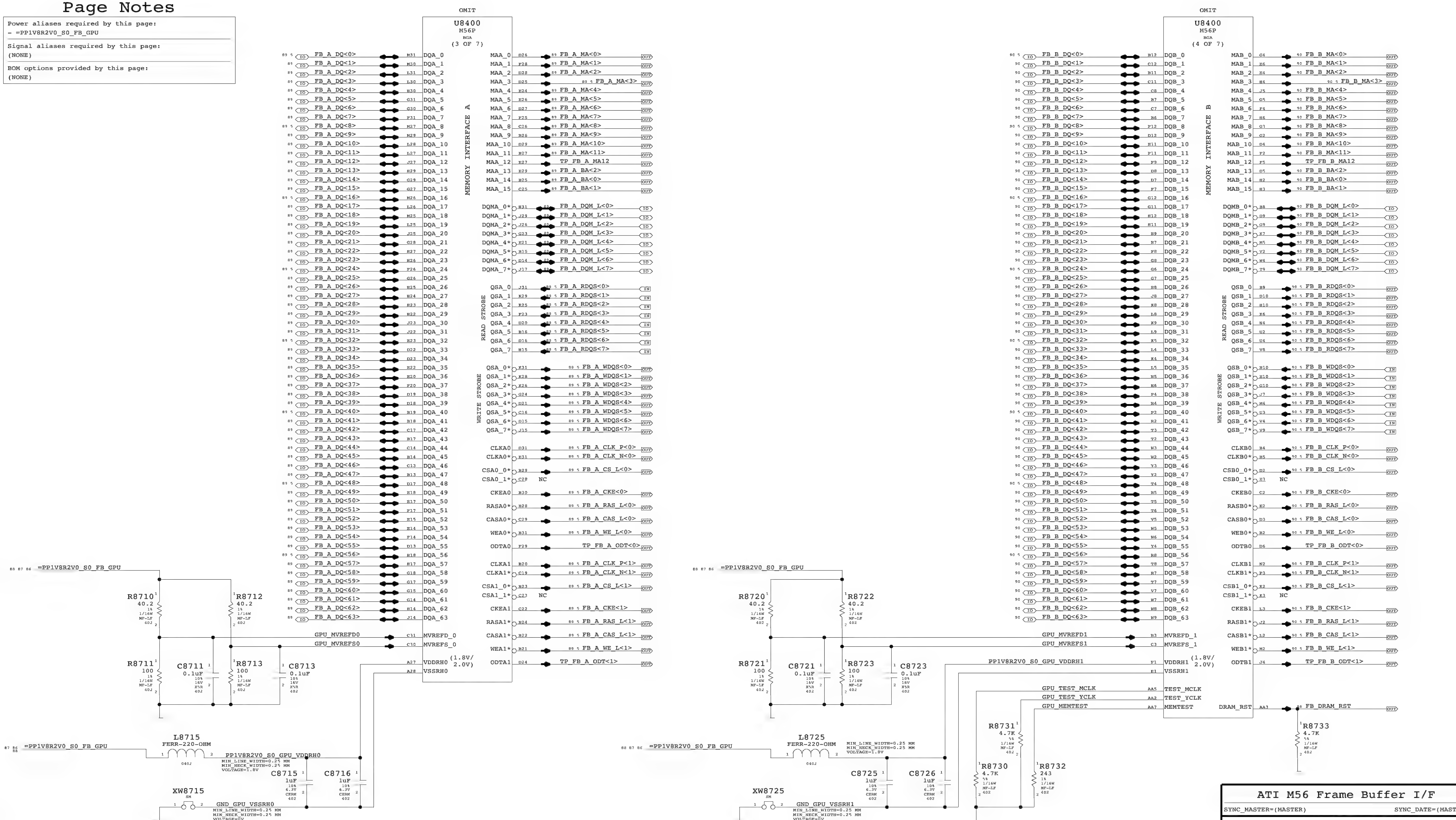
| SCALE | SHT | OF |
|---|---|---|
| NONE | 86 | 111 |

## Page Notes

Power aliases required by this page:
- =PP1V8R2V0_S0_FB_GPU

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)




# ATI M56 Frame Buffer I/F

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

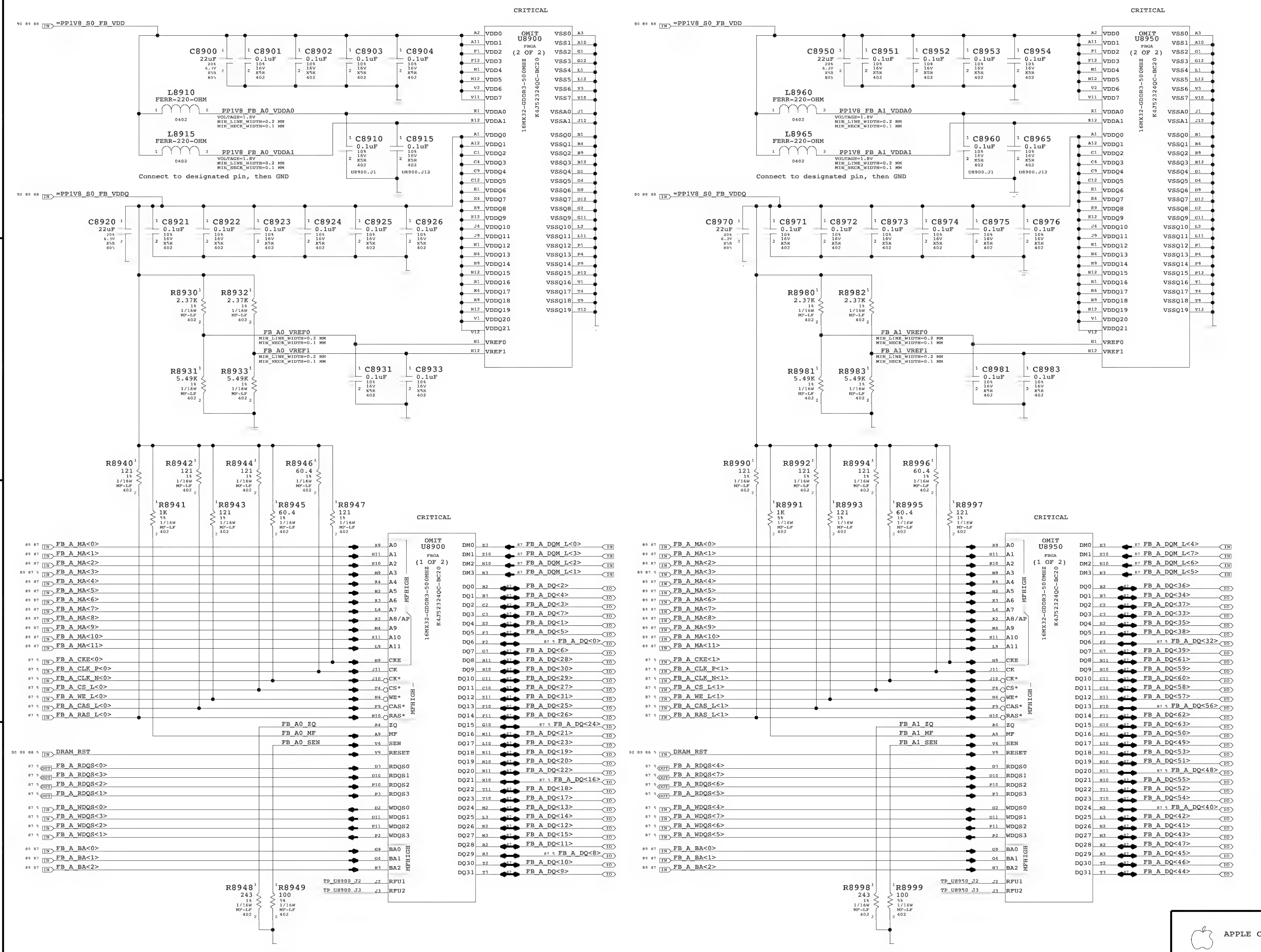


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| SIZE | DRAWING NUMBER | REV. |
| --- | --- | --- |
| D | 051-6949 | 09 |
| SCALE NONE | SHT 87 | OF 111 |


"S0" GPU RAILS
ONLY ON IN RUN
M56 GPIOS
PP1V0R1V2_S0_GPU
=PPVCORE_S0_GPU_REG
=PPVCORE_S0_GPU
=PPVCORE_S0_GPU_BBP
PP5V_S0_GPUVCORE_VCC
R8800
=PP5V_S0_GPUVCORE
GPUVCORE_VCC
PP1V2_GPU_IO_S0
=PP1V2_S0_PCIE_GPU_VDDR
=PP1V2_S0_PCIE_GPU_PVDD
=PPVIO_S0_PCIE
=PP1V2_S0_REG
=PP1V2_S0_GPU_VDDPLL
PPBB_S0_GPU
=PPVOUT_S0_GPUBBP_LDO
=PPBB_S0_GPU
PNBB_S0_GPU
=PNVOUT_S0_GPUBBN_REG
=PNBB_S0_GPU
PP3V3_S0
=PP3V3_S0_GPUBBP
=PP3V3_S0_GPUBBCTL
=PP3V3_DDC_DVI
=PP3V3_S0_LCD
=PP3V3_S0_GPU_VDDR3
=PP3V3_S0_GPU
=PP3V3_S0_GPUBBN
=PP3V3_S0_GPU_CLOCKS
=PP3V3_DDC_LCD
=PP3V3_GPU
PP2V5_S0
=PP2V5_S0_GPU_PVDD
=PP2V5_S0_GPU_VDD25
=PP2V5_S0_GPU_VDDC_CT
=PP2V5_S0_GPU
PP1V8R2V0_S0_FB_GPU
=PP1V8_S0_FB_VDD
=PP1V8R3V3_S0_GPU_VDDR4
=PP1V8R3V3_S0_GPU_VDDR5
=PP1V8_S0_FB_VDDQ
PP1V8_S0
=PP1V8R2V0_S0_FB_GPU
PP12V_S5
=PPVIN_S0_GPUVCORE
PP12V_S0
=PP12V_GPU
PP5V_S0
=PP5V_S0_GPUBBCTL
=PP5V_S0_DVI_DDC
=PP5V_S0_GPUISENS
GPUVCORE_EN
R8801
PM_SLP_S3_L
FB_DRAM_RST
DRAM_RST
=PP3V3_S0_GPU_VDDR3
GPU_GPIO_0
R8813
GPIO 0 = TRANSMITTER POWER SAVINGS ENABLE
INTERNAL PULL DOWN, ATI RECOMMENDS HIGH
GPU_GPIO_1
R8802
GPIO 1 = TRANSMITTER DE-EMPHASIS ENABLE
INTERNAL PULL DOWN, ATI RECOMMENDS HIGH
GPU_GPIO_2
R8803
GPU_GPIO_3
R8804
GPU_GPIO_4
R8805
GPIO 4 = DEBUG SIGNALS OUT
GPU_GPIO_5
R8806
GPU_GPIO_6
R8807
TP_GPU_GPIO_7
GPU_GPIO_7
GPU_GPIO_8
R8808
NC_GPU_GPIO_10
GPU_GPIO_10
GPU_GPIO_9
R8809
GPU_GPIO_13
R8812
GPU_GPIO_12
ATI_FB_256M
R8810
GPU_GPIO_11
R8811
GPIO 9,13,12,11 = ROM ID CFG
INTERNAL PULL DOWN
0010 = 256 M APERATURE SIZE
GPU_GPIO_24
R8830
GPU_GPIO_27
ATI_FB_HYNIX
R8831
GPU_GPIO_28
R8832
GPU_GPIO_29
TMDS_PANEL
R8833
GPU_VCORE_LOW
GPU_GPIO_15
R8850
GPIO 15 = SWITCH CORE VOLTAGE HIGH TO LOW
EXTERNAL PULL DOWN RECOMMENDED
TP_GPU_GPIO_14
GPU_GPIO_14
TP_GPU_GPIO_17
GPU_GPIO_17
TP_GPU_VGA_R
GPU_VGA_R
TP_GPU_VGA_G
GPU_VGA_G
TP_GPU_VGA_B
GPU_VGA_B
TP_GPU_VGA_HSYNC
GPU_VGA_HSYNC
TP_GPU_VGA_VSYNC
GPU_VGA_VSYNC
TP_GPU_TV_Y
GPU_TV_Y
TP_GPU_TV_COMP
GPU_TV_COMP
TP_GPU_TV_C
GPU_TV_C
TP_GPU_DDC_B_CLK
GPU_DDC_B_CLK
TP_GPU_DDC_B_DATA
GPU_DDC_B_DATA

GPU MISC




# Page Notes

Power aliases required by this page:
- =PP1V8_S0_FB_VDD
- =PP1V8_S0_FB_VDDQ

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

## GDDR3 Frame Buffer A

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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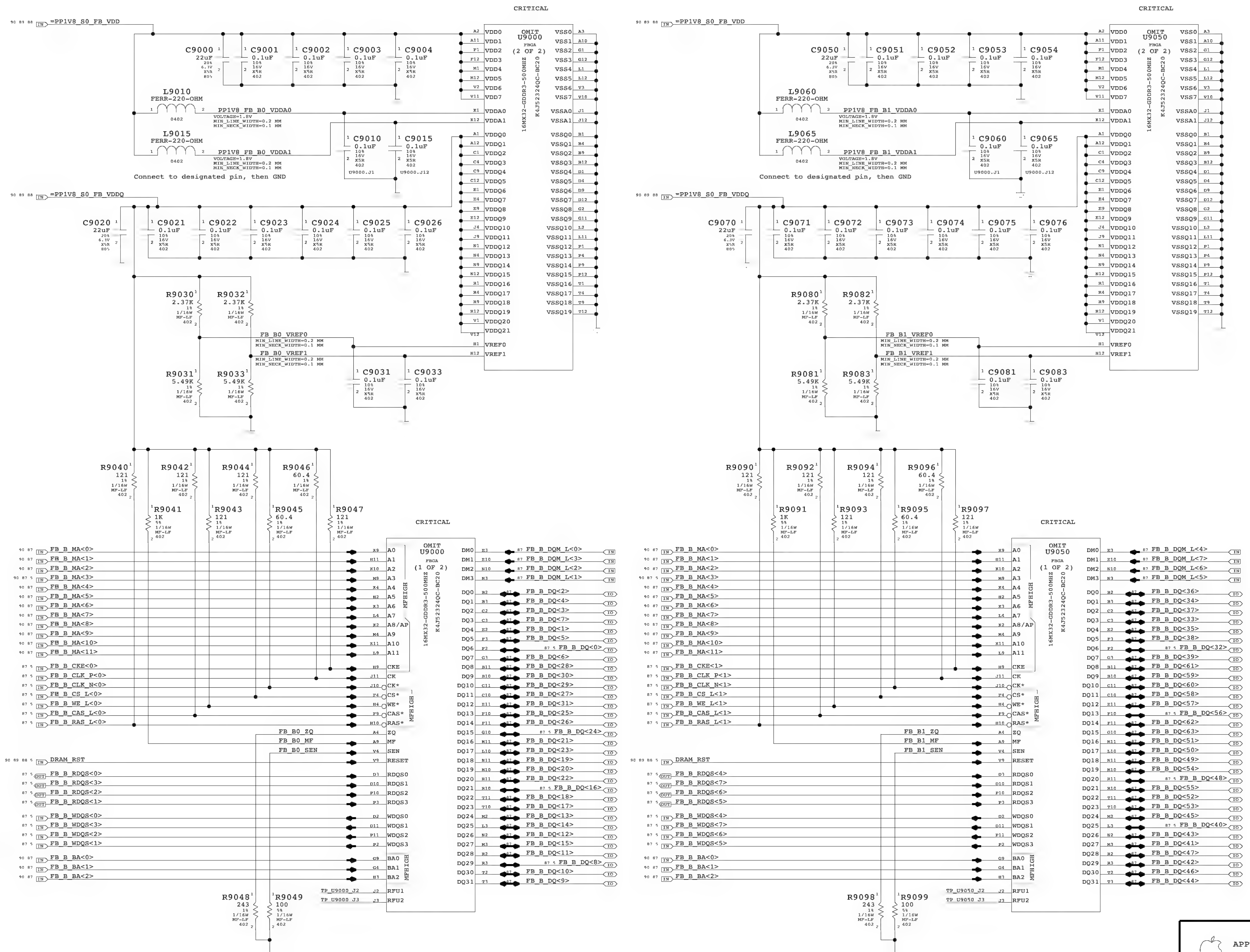
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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 89 | OF 111 |




# Page Notes

Power aliases required by this page:
- =PP1V8_S0_FB_VDD
- =PP1V8_S0_FB_VDDQ

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

## GDDR3 Frame Buffer B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)



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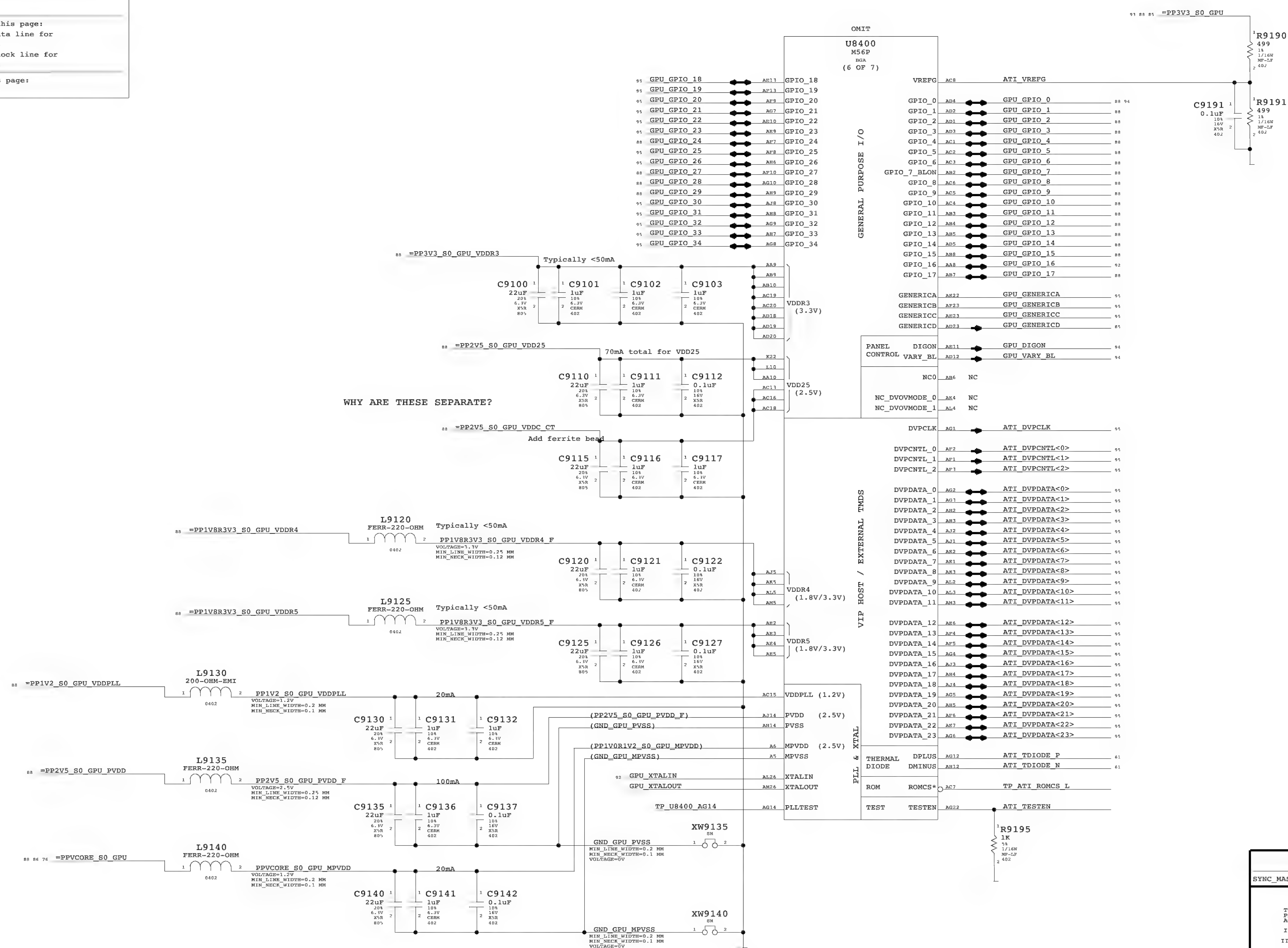
| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 90 | OF 111 |

## Page Notes

Power aliases required by this page:
- =PP3V3_GPU_GPIOS
- =PP2V5_PVDD
- =PP1V8_GPU_LVDS_PLL

Signal aliases required by this page:
- =I2C_GPU_TMDS_SDA - I2C data line for external TMDS transmitters
- =I2C_GPU_TMDS_SCL - I2C clock line for external TMDS transmitters

BOM options provided by this page:
(NONE)




| ATI M56 GPIO/DVO/Misc | |
|---|---|
| SYNC_MASTER=(MASTER) | SYNC_DATE=(MASTER) |

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|---|---|---|
| D | 051-6949 | 09 |
| SCALE NONE | SHT 91 | OF 111 |

## Page Notes

Power aliases required by this page:

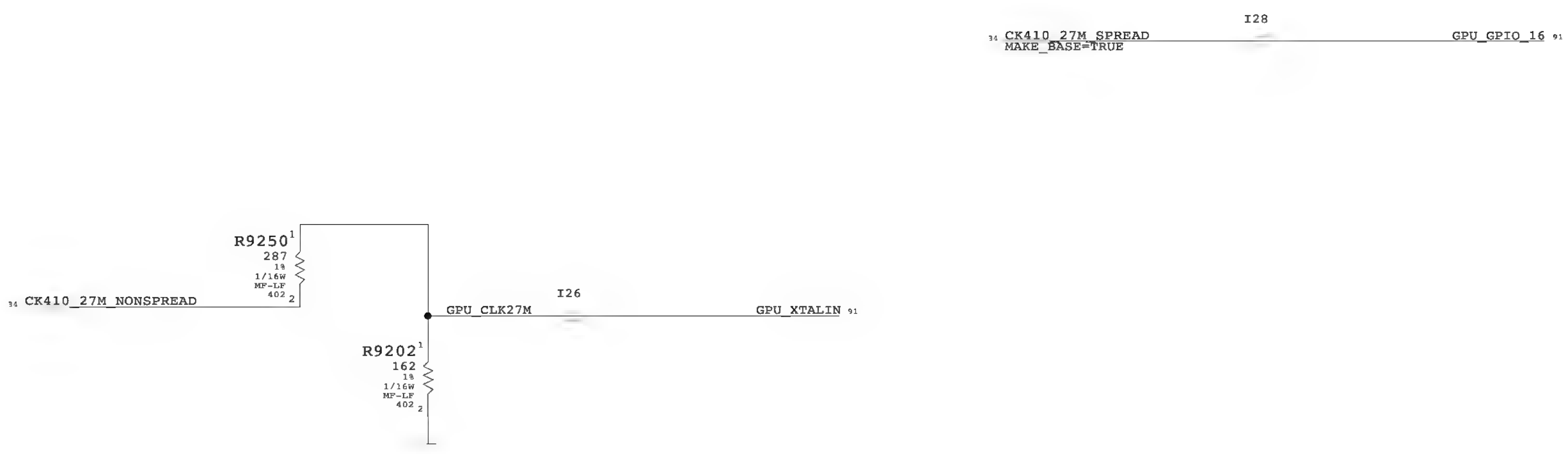
- =PP3V3_GPU_CLOCKS
- =PPVIN_GPU_LVDDR_LDO
- =PP2V5_GPU_LVDDR_LDO
- =PP3V3_GPU_PWRSEQ
- =PP2V5_GPU_PWRSEQ
- =PP1V8_GPU_PWRSEQ
- =PP1V5_GPU_PWRSEQ

Signal aliases required by this page:

(NONE)

BOM options provided by this page:

- GPU_SS
- GPU_LVDDR_2V8




# GPU CLOCKS

SYNC_MASTER=BOZEMAN SYNC_DATE=05/21/2005



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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |

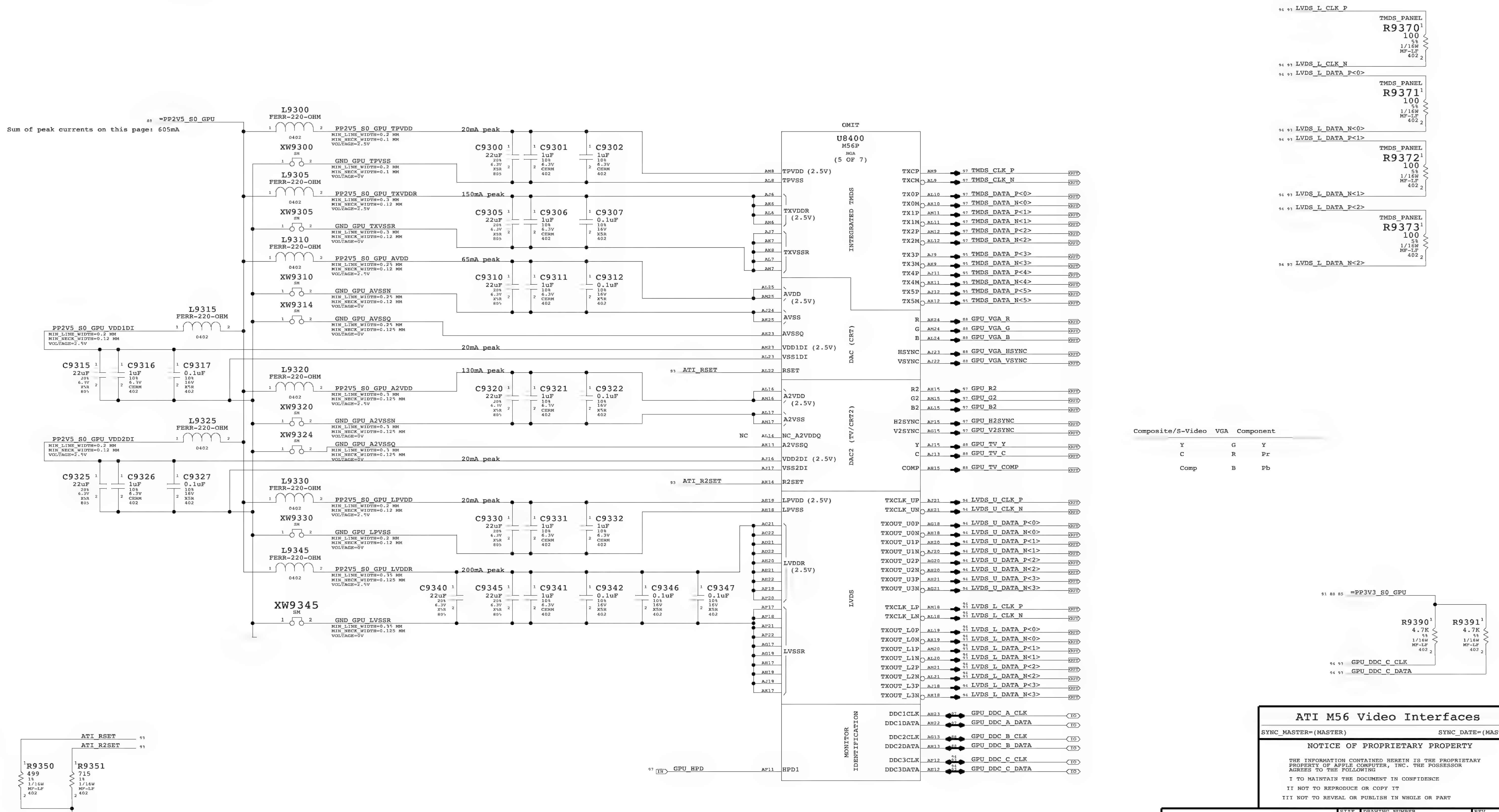
| SCALE | SHT | OF |
|---|---|---|
| NONE | 92 | 111 |

## Page Notes

Power aliases required by this page:
- =PP2V5_S0_GPU
- =PP1V8R2V5_S0_GPU_LVDDR

Signal aliases required by this page:
(NONE)

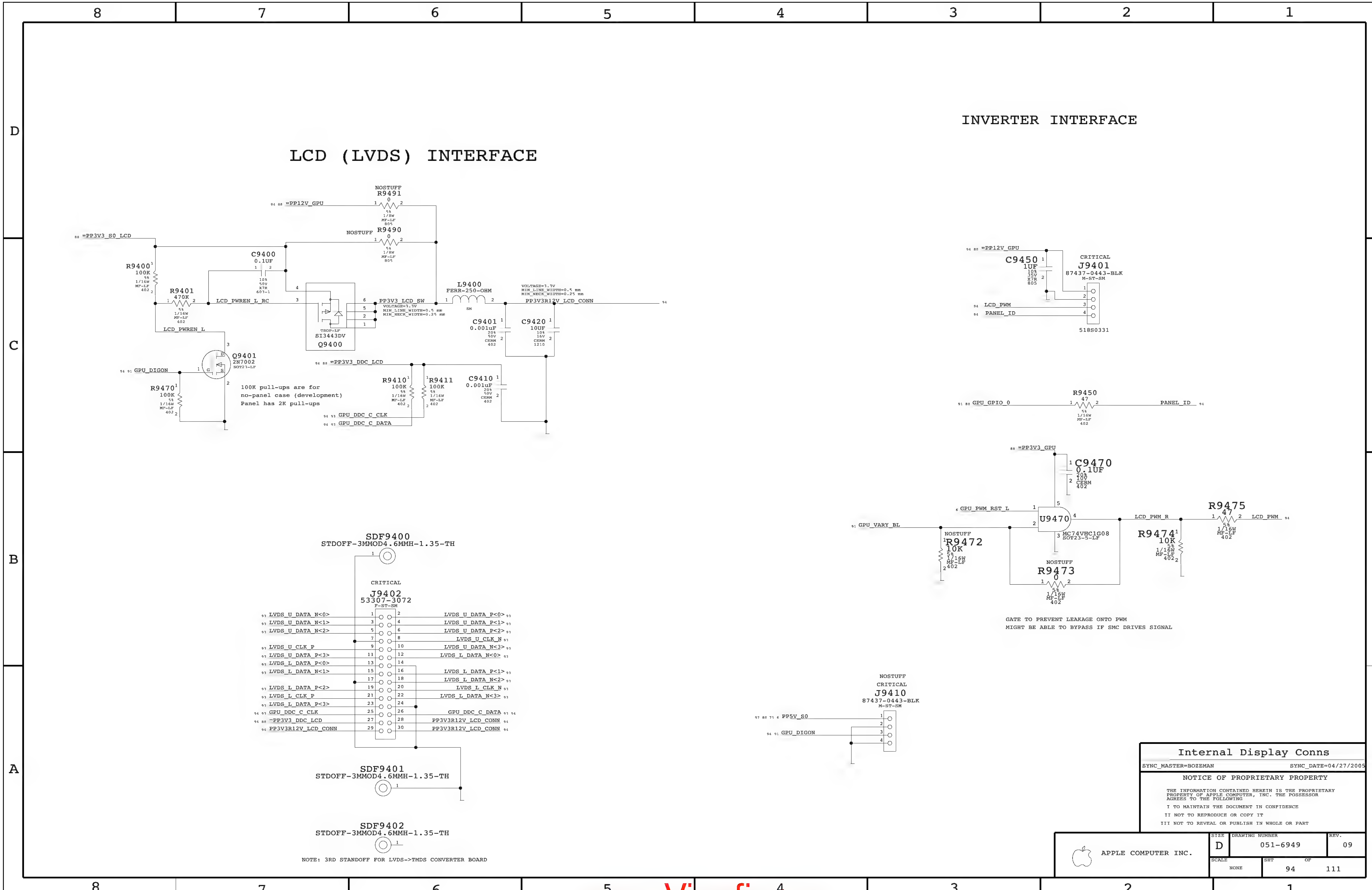
BOM options provided by this page:
(NONE)






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| SIZE | DRAWING NUMBER | REV. |
|---|---|---|
| D | 051-6949 | 09 |
| SCALE: NONE | SHT 93 | OF 111 |


LCD (LVDS) INTERFACE
INVERTER INTERFACE
=PP12V_GPU
NOSTUFF
R9491
=PP3V3_S0_LCD
R9490
C9400
0.1UF
R9400
100K
R9401
470K
LCD_PWREN_L_RC
LCD_PWREN_L
SI3443DV
Q9400
PP3V3_LCD_SW
L9400
FERR-250-OHM
PP3V3R12V_LCD_CONN
C9401
0.001uF
C9420
10UF
Q9401
2N7002
GPU_DIGON
R9470
100K
=PP3V3_DDC_LCD
R9410
R9411
C9410
100K pull-ups are for
no-panel case (development)
Panel has 2K pull-ups
GPU_DDC_C_CLK
GPU_DDC_C_DATA
C9450
1UF
CRITICAL
J9401
87437-0443-BLK
LCD_PWM
PANEL_ID
518S0331
R9450
47
GPU_GPIO_0
=PP3V3_GPU
C9470
0.1UF
GPU_PWM_RST_L
GPU_VARY_BL
U9470
MC74VHC1G08
LCD_PWM_R
R9475
47
R9474
10K
R9472
10K
R9473
0
GATE TO PREVENT LEAKAGE ONTO PWM
MIGHT BE ABLE TO BYPASS IF SMC DRIVES SIGNAL
SDF9400
STDOFF-3MMOD4.6MMH-1.35-TH
J9402
53307-3072
LVDS_U_DATA_N<0>
LVDS_U_DATA_N<1>
LVDS_U_DATA_N<2>
LVDS_U_CLK_P
LVDS_U_DATA_P<3>
LVDS_L_DATA_P<0>
LVDS_L_DATA_N<1>
LVDS_L_DATA_P<2>
LVDS_L_CLK_P
LVDS_L_DATA_P<3>
=PP3V3_DDC_LCD
LVDS_U_DATA_P<0>
LVDS_U_DATA_P<1>
LVDS_U_DATA_P<2>
LVDS_U_CLK_N
LVDS_U_DATA_N<3>
LVDS_L_DATA_N<0>
LVDS_L_DATA_P<1>
LVDS_L_DATA_N<2>
LVDS_L_CLK_N
LVDS_L_DATA_N<3>
SDF9401
SDF9402
NOTE: 3RD STANDOFF FOR LVDS->TMDS CONVERTER BOARD
J9410
PP5V_S0
Internal Display Conns
SYNC_MASTER=BOZEMAN
SYNC_DATE=04/27/2005
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051-6949
09
94
111

| Test point | Property | Net | Ref |
|---|---|---|---|
| TP_TMDS_DATA_P<3> | MAKE_BASE=TRUE | TMDS_DATA_P<3> | 93 |
| TP_TMDS_DATA_N<3> | MAKE_BASE=TRUE | TMDS_DATA_N<3> | 93 |
| TP_TMDS_DATA_P<4> | MAKE_BASE=TRUE | TMDS_DATA_P<4> | 93 |
| TP_TMDS_DATA_N<4> | MAKE_BASE=TRUE | TMDS_DATA_N<4> | 93 |
| TP_TMDS_DATA_P<5> | MAKE_BASE=TRUE | TMDS_DATA_P<5> | 93 |
| TP_TMDS_DATA_N<5> | MAKE_BASE=TRUE | TMDS_DATA_N<5> | 93 |
| TP_ATI_DVPDATA<23> | MAKE_BASE=TRUE | ATI_DVPDATA<23> | 91 |
| TP_ATI_DVPDATA<22> | MAKE_BASE=TRUE | ATI_DVPDATA<22> | 91 |
| TP_ATI_DVPDATA<21> | MAKE_BASE=TRUE | ATI_DVPDATA<21> | 91 |
| TP_ATI_DVPDATA<20> | MAKE_BASE=TRUE | ATI_DVPDATA<20> | 91 |
| TP_ATI_DVPDATA<19> | MAKE_BASE=TRUE | ATI_DVPDATA<19> | 91 |
| TP_ATI_DVPDATA<18> | MAKE_BASE=TRUE | ATI_DVPDATA<18> | 91 |
| TP_ATI_DVPDATA<17> | MAKE_BASE=TRUE | ATI_DVPDATA<17> | 91 |
| TP_ATI_DVPDATA<16> | MAKE_BASE=TRUE | ATI_DVPDATA<16> | 91 |
| TP_ATI_DVPDATA<15> | MAKE_BASE=TRUE | ATI_DVPDATA<15> | 91 |
| TP_ATI_DVPDATA<14> | MAKE_BASE=TRUE | ATI_DVPDATA<14> | 91 |
| TP_ATI_DVPDATA<13> | MAKE_BASE=TRUE | ATI_DVPDATA<13> | 91 |
| TP_ATI_DVPDATA<12> | MAKE_BASE=TRUE | ATI_DVPDATA<12> | 91 |
| TP_ATI_DVPDATA<11> | MAKE_BASE=TRUE | ATI_DVPDATA<11> | 91 |
| TP_ATI_DVPDATA<10> | MAKE_BASE=TRUE | ATI_DVPDATA<10> | 91 |
| TP_ATI_DVPDATA<9> | MAKE_BASE=TRUE | ATI_DVPDATA<9> | 91 |
| TP_ATI_DVPDATA<8> | MAKE_BASE=TRUE | ATI_DVPDATA<8> | 91 |
| TP_ATI_DVPDATA<7> | MAKE_BASE=TRUE | ATI_DVPDATA<7> | 91 |
| TP_ATI_DVPDATA<6> | MAKE_BASE=TRUE | ATI_DVPDATA<6> | 91 |
| TP_ATI_DVPDATA<5> | MAKE_BASE=TRUE | ATI_DVPDATA<5> | 91 |
| TP_ATI_DVPDATA<4> | MAKE_BASE=TRUE | ATI_DVPDATA<4> | 91 |
| TP_ATI_DVPDATA<3> | MAKE_BASE=TRUE | ATI_DVPDATA<3> | 91 |
| TP_ATI_DVPDATA<2> | MAKE_BASE=TRUE | ATI_DVPDATA<2> | 91 |
| TP_ATI_DVPDATA<1> | MAKE_BASE=TRUE | ATI_DVPDATA<1> | 91 |
| TP_ATI_DVPDATA<0> | MAKE_BASE=TRUE | ATI_DVPDATA<0> | 91 |
| TP_ATI_DVPCLK | MAKE_BASE=TRUE | ATI_DVPCLK | 91 |
| TP_ATI_DVPCNTL<0> | MAKE_BASE=TRUE | ATI_DVPCNTL<0> | 91 |
| TP_ATI_DVPCNTL<1> | MAKE_BASE=TRUE | ATI_DVPCNTL<1> | 91 |
| TP_ATI_DVPCNTL<2> | MAKE_BASE=TRUE | ATI_DVPCNTL<2> | 91 |

| Test point | Property | Net | Ref |
|---|---|---|---|
| TP_GPU_GPIO<34> | MAKE_BASE=TRUE | GPU_GPIO_34 | 91 |
| TP_GPU_GPIO<33> | MAKE_BASE=TRUE | GPU_GPIO_33 | 91 |
| TP_GPU_GPIO<32> | MAKE_BASE=TRUE | GPU_GPIO_32 | 91 |
| TP_GPU_GPIO<31> | MAKE_BASE=TRUE | GPU_GPIO_31 | 91 |
| TP_GPU_GPIO<30> | MAKE_BASE=TRUE | GPU_GPIO_30 | 91 |
| TP_GPU_GPIO<26> | MAKE_BASE=TRUE | GPU_GPIO_26 | 91 |
| TP_GPU_GPIO<25> | MAKE_BASE=TRUE | GPU_GPIO_25 | 91 |
| TP_GPU_GPIO<23> | MAKE_BASE=TRUE | GPU_GPIO_23 | 91 |
| TP_GPU_GPIO<22> | MAKE_BASE=TRUE | GPU_GPIO_22 | 91 |
| TP_GPU_GPIO<21> | MAKE_BASE=TRUE | GPU_GPIO_21 | 91 |
| TP_GPU_GPIO<20> | MAKE_BASE=TRUE | GPU_GPIO_20 | 91 |
| TP_GPU_GPIO<19> | MAKE_BASE=TRUE | GPU_GPIO_19 | 91 |
| TP_GPU_GPIO<18> | MAKE_BASE=TRUE | GPU_GPIO_18 | 91 |
| TP_GPU_GENERICA | MAKE_BASE=TRUE | GPU_GENERICA | 91 |
| TP_GPU_GENERICB | MAKE_BASE=TRUE | GPU_GENERICB | 91 |
| TP_GPU_GENERICC | MAKE_BASE=TRUE | GPU_GENERICC | 91 |

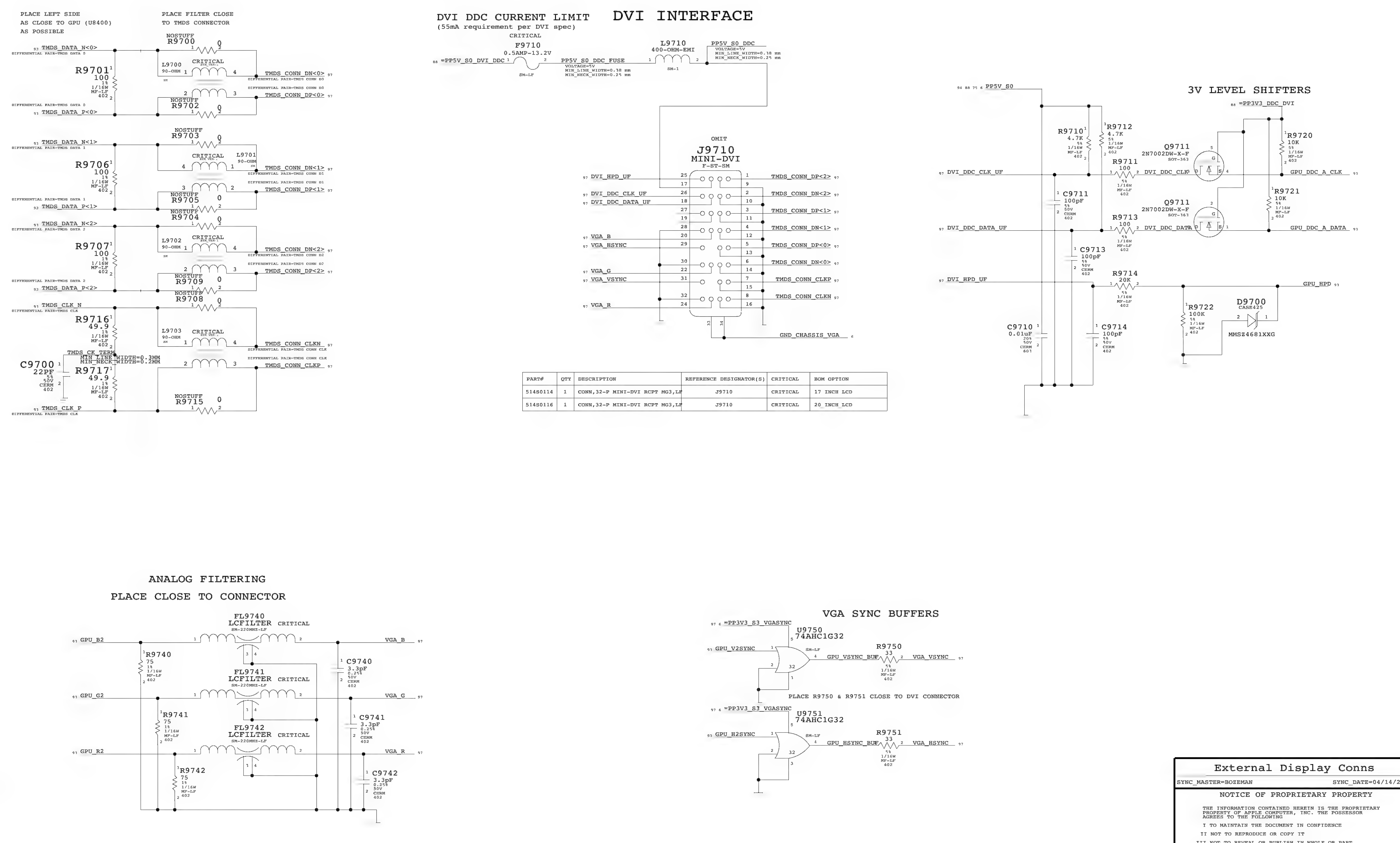
# M56 TPS



APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 95 OF 111




| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|---|---|---|---|---|---|
| 514S0114 | 1 | CONN,32-P MINI-DVI RCPT MG3,LF | J9710 | CRITICAL | 17 INCH LCD |
| 514S0116 | 1 | CONN,32-P MINI-DVI RCPT MG3,LF | J9710 | CRITICAL | 20 INCH LCD |

# External Display Conns

SYNC_MASTER=BOZEMAN SYNC_DATE=04/14/2005



APPLE COMPUTER INC.

SIZE D | DRAWING NUMBER 051-6949 | REV. 09

SCALE NONE | SHT 97 OF 111

Title: Basenet Report
Design: m38
Date: Nov 15 17:44:33 2005

Base nets and synonyms for m38 lib.M38(@m38 lib.m38(sch_1))

| Base Signal | Synonyms | Location([Zone][dir]) |
|---|---|---|
| 0V7 REF | 0V7 REF - @m38 lib.M38 | 79A3 |
| 1V0 REF | 1V0 REF - @m38 lib.M38 | 79A5 80A5 81A5 81B3 |
| 1V2REG ITH | 1V2REG ITH - @m38 lib.M38 | 77B5 |
| 1V2REG ITH RC | 1V2REG ITH RC - @m38 lib.M38 | 77B6 |
| 1V2REG MODE | 1V2REG MODE - @m38 lib.M38 | 77B5 |
| 1V2REG PGOOD | 1V2REG PGOOD - @m38 lib.M38 | 77B4 |
| 1V2REG RT | 1V2REG RT - @m38 lib.M38 | 77B5 |
| 1V2REG RUNSS | 1V2REG RUNSS - @m38 lib.M38 | 77B5 |
| 1V2REG SGND | 1V2REG SGND - @m38 lib.M38 | 77A7 |
| 1V2REG SW | 1V2REG SW - @m38 lib.M38 | 77B4 |
| 1V2REG VFB | 1V2REG VFB - @m38 lib.M38 | 77B5 |
| 1V2REG VFB DIV | 1V2REG VFB DIV - @m38 lib.M38 | 77A5 |
| 1V3 REF | 1V3 REF - @m38 lib.M38 | 80B3 |
| 1V05REG NB BOOT | 1V05REG NB BOOT - @m38 lib.M38 | 81C6 |
| 1V05REG NB BOOT R | 1V05REG NB BOOT R - @m38 lib.M38 | 81C5 |
| 1V05REG NB COMP | 1V05REG NB COMP - @m38 lib.M38 | 81C6 |
| 1V05REG NB COMP R | 1V05REG NB COMP R - @m38 lib.M38 | 81C5 |
| 1V05REG_NB_FB | 1V05REG_NB_FB - @m38_lib.M38 | 81C5 |
| 1V05REG_NB_FB_R | 1V05REG_NB_FB_R - @m38_lib.M38 | 81C3 |
| 1V05REG NB FS DIS | 1V05REG NB FS DIS - @m38 lib.M38 | 81C7 |
| 1V05REG_NB_GND | 1V05REG_NB_GND - @m38_lib.M38 | 81B7 |
| 1V05REG NB LDO DR | 1V05REG NB LDO DR - @m38 lib.M38 | 81C7 |
| 1V05REG NB LDO FB | 1V05REG NB LDO FB - @m38 lib.M38 | 81C7 |
| 1V05REG NB LGATE | 1V05REG NB LGATE - @m38 lib.M38 | 81C6 |
| 1V05REG NB PVCC5 | 1V05REG NB PVCC5 - @m38 lib.M38 | 81D7 |
| 1V05REG NB SNUB | 1V05REG NB SNUB - @m38 lib.M38 | 81C4 |
| 1V05REG NB SWITCHNODE | 1V05REG NB SWITCHNODE - @m38 lib.M38 | 81C5 |
| 1V05REG NB UGATE | 1V05REG NB UGATE - @m38 lib.M38 | 81D6 |
| 1V05REG NB VCC5 | 1V05REG NB VCC5 - @m38 lib.M38 | 81D7 |
| 1V5REG PCIE BOOT | 1V5REG PCIE BOOT - @m38 lib.M38 | 80C6 |
| 1V5REG PCIE BOOT R | 1V5REG PCIE BOOT R - @m38 lib.M38 | 80C5 |
| 1V5REG PCIE COMP | 1V5REG PCIE COMP - @m38 lib.M38 | 80C6 |
| 1V5REG PCIE COMP R | 1V5REG PCIE COMP R - @m38 lib.M38 | 80C5 |
| 1V5REG PCIE FB | 1V5REG PCIE FB - @m38 lib.M38 | 80C5 |
| 1V5REG PCIE FB R | 1V5REG PCIE FB R - @m38 lib.M38 | 80C3 |
| 1V5REG PCIE FS DIS | 1V5REG PCIE FS DIS - @m38 lib.M38 | 80C7 |
| 1V5REG PCIE GND | 1V5REG PCIE GND - @m38 lib.M38 | 80B7 |
| 1V5REG PCIE LDO DR | 1V5REG_PCIE_LDO DR - @m38_lib.M38 | 80C7 |
| 1V5REG PCIE LDO FB | 1V5REG_PCIE_LDO FB - @m38_lib.M38 | 80C7 |
| 1V5REG_PCIE_LGATE | 1V5REG_PCIE_LGATE - @m38_lib.M38 | 80C6 |
| 1V5REG_PCIE_PVCC5 | 1V5REG_PCIE_PVCC5 - @m38_lib.M38 | 80D7 |
| 1V5REG_PCIE_SNUB | 1V5REG_PCIE_SNUB - @m38_lib.M38 | 80C4 |
| 1V5REG_PCIE_SWITCHNODE | 1V5REG_PCIE_SWITCHNODE - @m38_lib.M38 | 80C5 |
| 1V5REG PCIE UGATE | 1V5REG_PCIE_UGATE - @m38_lib.M38 | 80D6 |
| 1V5REG PCIE VCC5 | 1V5REG PCIE VCC5 - @m38 lib.M38 | 80D7 |
| 1V6 REF | 1V6 REF - @m38 lib.M38 | 79B3 |
| 1V8REG DDR BOOT | 1V8REG DDR BOOT - @m38 lib.M38 | 79D6 |
| 1V8REG DDR BOOT R | 1V8REG DDR BOOT R - @m38 lib.M38 | 79D5 |
| 1V8REG DDR COMP | 1V8REG DDR COMP - @m38 lib.M38 | 79C6 |
| 1V8REG DDR COMP R | 1V8REG DDR COMP R - @m38 lib.M38 | 79C5 |
| 1V8REG DDR FB | 1V8REG DDR FB - @m38 lib.M38 | 79C5 |
| 1V8REG DDR FB R | 1V8REG DDR FB R - @m38 lib.M38 | 79C3 |
| 1V8REG DDR FS DIS | 1V8REG DDR FS DIS - @m38 lib.M38 | 79C7 |
| 1V8REG DDR GND | 1V8REG_DDR GND - @m38 lib.M38 | 79C7 |
| 1V8REG DDR LDO DR | 1V8REG DDR LDO DR - @m38 lib.M38 | 79C7 |
| 1V8REG DDR LDO FB | 1V8REG DDR LDO FB - @m38 lib.M38 | 79C7 |
| 1V8REG DDR LGATE | 1V8REG DDR LGATE - @m38 lib.M38 | 79C6 |
| 1V8REG DDR PVCC5 | 1V8REG DDR PVCC5 - @m38 lib.M38 | 79D7 |
| 1V8REG DDR SNUB | 1V8REG DDR SNUB - @m38 lib.M38 | 79C4 |
| 1V8REG DDR SWITCHNODE | 1V8REG DDR SWITCHNODE - @m38_lib.M38 | 79C5 |
| 1V8REG DDR UGATE | 1V8REG_DDR_UGATE - @m38_lib.M38 | 79D6 |
| 1V8REG_DDR_VCC5 | 1V8REG_DDR_VCC5 - @m38_lib.M38 | 79D7 |
| 1V8REG_GPU_BOOT | 1V8REG_GPU_BOOT - @m38_lib.M38 | 78C6 |
| 1V8REG_GPU_BOOT_R | 1V8REG_GPU_BOOT_R - @m38_lib.M38 | 78C5 |
| 1V8REG GPU COMP | 1V8REG GPU COMP - @m38 lib.M38 | 78B6 |
| 1V8REG GPU COMP_R | 1V8REG_GPU_COMP R - @m38 lib.M38 | 78B5 |
| 1V8REG GPU FB | 1V8REG GPU FB - @m38 lib.M38 | 78B5 |
| 1V8REG GPU FB R | 1V8REG GPU FB R - @m38 lib.M38 | 78B3 |
| 1V8REG GPU FS DIS | 1V8REG GPU FS DIS - @m38 lib.M38 | 78B7 |
| 1V8REG GPU GND | 1V8REG GPU GND - @m38 lib.M38 | 78B7 |
| 1V8REG GPU LDO DR | 1V8REG GPU LDO DR - @m38 lib.M38 | 78C7 |
| 1V8REG GPU LDO FB | 1V8REG GPU LDO FB - @m38 lib.M38 | 78B7 |
| 1V8REG GPU LGATE | 1V8REG GPU LGATE - @m38 lib.M38 | 78B6 |
| 1V8REG GPU PVCC5 | 1V8REG GPU PVCC5 - @m38 lib.M38 | 78C7 |
| 1V8REG GPU SNUB | 1V8REG GPU SNUB - @m38 lib.M38 | 78B4 |
| 1V8REG GPU SWITCHNODE | 1V8REG GPU SWITCHNODE - @m38 lib.M38 | 78B5 |
| 1V8REG GPU UGATE | 1V8REG GPU UGATE - @m38 lib.M38 | 78C6 |
| 1V8REG GPU VCC5 | 1V8REG GPU VCC5 - @m38 lib.M38 | 78C7 |
| 2V5REG ITH | 2V5REG ITH - @m38 lib.M38 | 77C4 |
| 2V5REG ITH RC | 2V5REG ITH RC - @m38 lib.M38 | 77C4 |
| 2V5REG MODE | 2V5REG MODE - @m38 lib.M38 | 77C5 |
| 2V5REG RT | 2V5REG_RT - @m38_lib.M38 | 77C5 |
| 2V5REG SGND | 2V5REG_SGND - @m38 lib.M38 | 77C4 |
| 2V5REG_SW | 2V5REG_SW - @m38_lib.M38 | 77C4 |
| 2V5REG_VFB | 2V5REG_VFB - @m38_lib.M38 | 77C4 |
| 5V_REG_IN | 5V_REG_IN - @m38_lib.M38 | 68A4 |
| =I2C_HD_TEMP_SCL | =I2C_HD_TEMP_SCL - @m38_lib.M38 | 59C1 66B6 |
| | =I2C ODD TEMP SCL - @m38 lib.M38 | 59C1 66B4 |
| | =SMB THRM CLK - @m38 lib.M38 | 10C3 59C1 |
| | SMB B S0 CLK - @m38 lib.M38 | 58B5 59C2 59D1 |
| | SMB GPU NB THRM CLK - @m38 lib.M38 | 59B1 61C3 |
| | SMB B S0 CLK - @m38 lib.M38 | 58B5 59C2 59D1 |
| | =SMB THRM CLK - @m38 lib.M38 | 10C3 59C1 |
| | =I2C ODD TEMP SCL - @m38 lib.M38 | 59C1 66B4 |
| =I2C HD TEMP SDA | =I2C HD_TEMP SDA - @m38 lib.M38 | 59C1 66B6 |
| | =I2C ODD_TEMP SDA - @m38 lib.M38 | 59C1 66B4 |
| | =SMB_THRM_DATA - @m38 lib.M38 | 10C3 59C1 |
| | SMB_B_S0 DATA - @m38 lib.M38 | 58B5 59C2 59D1 |
| | SMB GPU NB THRM DATA - @m38 lib.M38 | 59B1 61C3 |
| | SMB B S0 DATA - @m38 lib.M38 | 58B5 59C2 59D1 |
| | =SMB THRM DATA - @m38 lib.M38 | 10C3 59C1 |
| | =I2C ODD TEMP SDA - @m38 lib.M38 | 59C1 66B4 |
| =I2C MEM SCL | =I2C MEM SCL - @m38 lib.M38 | 27D6 28A6 29A6 |
| | =SMB AIRPORT CLK - @m38 lib.M38 | 27D6 53B4 |
| | SMB_CK410_CLK - @m38 lib.M38 | 27D6 33B6 |
| | SMB_CLK - @m38_lib.M38 | 23D5 27B7 27D7 |
| | SMB_CK410_CLK - @m38_lib.M38 | 27D6 33B6 |
| | =SMB_AIRPORT_CLK - @m38_lib.M38 | 27D6 53B4 |
| =I2C_MEM_SDA | =I2C_MEM_SDA - @m38 lib.M38 | 27D6 28A6 29A6 |
| | =SMB_AIRPORT_DATA - @m38_lib.M38 | 27C6 53B4 |
| | SMB CK410 DATA - @m38 lib.M38 | 27D6 33B6 |
| | SMB DATA - @m38 lib.M38 | 23D5 27B7 27D7 |
| | SMB CK410 DATA - @m38 lib.M38 | 27D6 33B6 |
| | =SMB AIRPORT DATA - @m38 lib.M38 | 27C6 53B4 |
| =PNBB_S0_GPU | =PNBB_S0_GPU - @m38_lib.M38 | 86D2 88C6 |
| | =PNVOUT_S0_GPUBBN_REG - @m38_lib.M38 | 85B1 88C6 |
| | PNBB_S0_GPU - @m38_lib.M38 | 88C8 |
| | =PNVOUT_S0_GPUBBN_REG - @m38_lib.M38 | 85B1 88C6 |
| =PP0V9 S0 MEMVTT LDO | =PP0V9 S0 MEMVTT LDO - @m38 lib.M38 | 6D4 31B3 |
| | =PP0V9 S0 MEM TERM - @m38 lib.M38 | 6D4 30D4 |
| | PP0V9 S0 - @m38 lib.M38 | 6D6 79A3 |
| | =PP0V9 S0 MEM TERM - @m38 lib.M38 | 6D4 30D4 |
| =PP1V2 S0 GPU VDDPLL | =PP1V2 S0 GPU VDDPLL - @m38 lib.M38 | 88C6 91B8 |
| | =PPVIO S0 PCIE - @m38 lib.M38 | 84A2 88C6 |
| | =PP1V2 S0 REG - @m38 lib.M38 | 77A1 88C6 |
| | =PP1V2 S0 PCIE GPU PVDD - @m38 lib.M38 | 84C8 88D6 |
| | =PP1V2 S0 PCIE GPU VDDR - @m38 lib.M38 | 84C8 88D6 |
| | PP1V2 GPU IO S0 - @m38 lib.M38 | 88D8 |
| | =PPVIO S0 PCIE - @m38 lib.M38 | 84A2 88C6 |
| | =PP1V2 S0 REG - @m38 lib.M38 | 77A1 88C6 |
| | =PP1V2 S0 PCIE GPU VDDR - @m38 lib.M38 | 84C8 88D6 |
| | =PP1V2_S0_PCIE_GPU_PVDD - @m38_lib.M38 | 84C8 88D6 |
| =PP1V2_S3_ENET | =PP1V2_S3_ENET - @m38_lib.M38 | 41A8 41D6 42B5 |
| | PP1V2_S3_ENET - @m38_lib.M38 | 42B5 |
| =PP1V2_S3_LAN | =PP1V2_S3_LAN - @m38_lib.M38 | 6D3 42B8 |
| | PP1V2_S3 - @m38_lib.M38 | 6D4 77B2 |
| =PP1V8_S0_FB_VDD | =PP1V8_S0_FB_VDD - @m38_lib.M38 | 88B6 89D5 89D8 90D5 90D8 |
| | =PP1V8 S0 FB VDDQ - @m38 lib.M38 | 88B6 89D5 89D8 90D5 90D8 |
| | =PP1V8R2V0 S0 FB GPU - @m38 lib.M38 | 86B8 87A5 87A8 87B5 87B8 88B6 |
| | PP1V8 S0 - @m38 lib.M38 | 78B2 88B6 |
| | =PP1V8R3V3 S0 GPU VDDR5 - @m38 lib.M38 | 88B6 91B7 |
| | =PP1V8R3V3 S0 GPU_VDDR4 - @m38 lib.M38 | 88B6 91B7 |
| | PP1V8_S0 - @m38_lib.M38 | 78B2 88B6 |
| | PP1V8R2V0_S0_FB_GPU - @m38 lib.M38 | 88B8 |
| | =PP1V8 S0 FB VDDQ - @m38 lib.M38 | 88B6 89D5 89D8 90D5 90D8 |
| | =PP1V8R3V3 S0 GPU VDDR5 - @m38 lib.M38 | 88B6 91B7 |
| | =PP1V8R3V3 S0 GPU VDDR4 - @m38 lib.M38 | 88B6 91B7 |
| | =PP1V8R2V0_S0_FB_GPU - @m38 lib.M38 | 86B8 87A5 87A8 87B5 87B8 88B6 |
| =PP1V8_S3_MEM_NB | =PP1V8_S3_MEM_NB - @m38_lib.M38 | 6D3 6D3 14C2 16B6 19B8 19D7 |
| | =PP1V8_S0_MEMVTT - @m38_lib.M38 | 6D3 31C7 |
| | PP1V8_S3 - @m38_lib.M38 | 5D2 6D4 79C2 |
| | =PP1V8_S3_MEM - @m38_lib.M38 | 5C4 6D3 28B2 28C8 28D3 28D6 29B2 29D3 29D6 |
| | PP1V8 S3 - @m38 lib.M38 | 5D2 6D4 79C2 |
| | =PP1V8 S3 MEM - @m38 lib.M38 | 5C4 6D3 28B2 28C8 28D3 28D6 29B2 29D3 29D6 |
| | =PP1V8 S0 MEMVTT - @m38 lib.M38 | 6D3 31C7 |
| =PP2V5 S0 NB VCCA 3GBG | =PP2V5 S0 NB VCCA 3GBG - @m38 lib.M38 | 6B4 17D6 19A8 19C7 |
| | =PP2V5 S0 GPU - @m38 lib.M38 | 88B6 93C8 |
| | PP2V5 S0 - @m38_lib.M38 | 6B6 77C3 88C8 |
| | =PP2V5 S0 GPU VDD25 - @m38 lib.M38 | 88B6 91C6 |
| | =PP2V5_S0_GPU_VDDC_CT - @m38 lib.M38 | 88B6 91C6 |
| | =PP2V5 S0 GPU PVDD - @m38 lib.M38 | 88B6 91A8 |
| | PP2V5 S0 - @m38 lib.M38 | 6B6 77C3 88C8 |
| | =PP2V5 S0 GPU VDDC CT - @m38 lib.M38 | 88B6 91C6 |
| | =PP2V5 S0 GPU VDD25 - @m38 lib.M38 | 88B6 91C6 |
| | =PP2V5_S0_GPU_PVDD - @m38_lib.M38 | 88B6 91A8 |
| | =PP2V5_S0_GPU - @m38_lib.M38 | 88B6 93C8 |
| =PP2V5_S3_ENET | =PP2V5_S3_ENET - @m38_lib.M38 | 41D5 41D6 42C4 |
| | PP2V5_S3_ENET - @m38_lib.M38 | 42C4 43D8 |
| =PP3V3_S0_NB | =PP3V3_S0_NB - @m38_lib.M38 | 6A4 14C7 14D6 19C7 20A4 20B4 20B4 |
| | =PP3V3_DDC_DVI - @m38_lib.M38 | 88C6 97D2 |
| | =PP3V3 DDC LCD - @m38 lib.M38 | 88C6 94A7 94C7 |
| | =PP3V3 GPU - @m38 lib.M38 | 88C6 94B3 |
| | PP3V3 S0 - @m38 lib.M38 | 6A7 6B6 6D8 10D2 26B7 26C6 41D8 59D3 61C3 76D6 88C8 |
| | =PP3V3 S0 GPUBBCTL - @m38 lib.M38 | 85A7 88C6 |
| | =PP3V3 S0 GPUBBN - @m38 lib.M38 | 85B5 88C6 |
| | =PP3V3 S0 GPU - @m38_lib.M38 | 85B3 88C6 91D2 93A1 |
| | =PP3V3 S0 GPU VDDR3 - @m38 lib.M38 | 88C6 88D3 91C6 |
| | =PP3V3 S0 LCD - @m38 lib.M38 | 88C6 94C8 |
| | =PP3V3 S0 GPUBBP - @m38 lib.M38 | 85B8 88C6 |
| | =PP3V3 S0 2V5REG - @m38 lib.M38 | 6A4 77D6 |
| | =PP3V3 S0 AIRPORT - @m38 lib.M38 | 6A4 53C2 |
| | =PP3V3 S0 PCI - @m38 lib.M38 | 6A4 44D5 |
| | =PP3V3 S0 AUDIO - @m38 lib.M38 | 6A4 68A6 68D7 72A6 72C8 72D5 73B8 73C2 74B5 74C5 74D5 |
| | =PP3V3_S0_IMVP - @m38_lib.M38 | 6A4 75D8 |
| | =PP3V3_S0_CK410 - @m38_lib.M38 | 6A4 33C8 33D3 33D8 34D4 |
| | =PPSPD_S0_MEM - @m38_lib.M38 | 6A4 28A7 29A3 29A7 |
| | =PP3V3_S0_TPM - @m38_lib.M38 | 6A4 67C7 67D4 |
| | =PP3V3_S0_SB_3V3_1V5_VCCHDA - @m38 lib.M38 | 6A4 24C3 25C4 |
| | =PP3V3 S0 ODD TSENS - @m38 lib.M38 | 6A4 66B4 |
| | =PP3V3 S0 HD TSENS - @m38 lib.M38 | 6B4 66B5 |
| | =PP3V3 S0 FAN - @m38 lib.M38 | 6B4 59A8 59B7 65B7 65C7 66C7 |
| | =PP3V3 S0 PATA - @m38 lib.M38 | 6B4 38D3 |
| | =PP3V3 S0 SB PM - @m38 lib.M38 | 6B4 26D4 |
| | =PP3V3 S0 SB PCI - @m38 lib.M38 | 6B4 26D1 |
| | =PP3V3 S0 SB VCC3_3_IDE - @m38 lib.M38 | 6B4 24C3 25B3 |
| | =PP3V3 S0 SB VCC3 3 PCI - @m38 lib.M38 | 6B4 24B3 25A3 |
| | =PP3V3 S0 SB VCC3 3 - @m38 lib.M38 | 6B4 24B5 24B5 25B8 25C6 |
| | =PP3V3 S0 NB VCC HV - @m38 lib.M38 | 6B4 17C6 19A8 19C7 |
| | =PP3V3 S0 SB GPIO - @m38 lib.M38 | 6B4 21C3 21D3 23B3 23D5 |
| | =PP3V3 S0 SB VCCLAN3 3 - @m38 lib.M38 | 6A4 24D3 25D3 |
| | =PP3V3_S0_SB - @m38_lib.M38 | 6B4 22B5 25D8 |
| | PP3V3_S0 - @m38_lib.M38 | 6A7 6B6 6D8 10D2 26B7 26C6 41D8 59D3 61C3 76D6 88C8 |
| | =PPSPD_S0_MEM - @m38_lib.M38 | 6A4 28A7 29A3 29A7 |
| | =PP3V3 S0 TPM - @m38 lib.M38 | 6A4 67C7 67D4 |
| | =PP3V3 S0 SB VCCLAN3 3 - @m38 lib.M38 | 6A4 24D3 25D3 |
| | =PP3V3 S0 SB VCC3 3 PCI - @m38 lib.M38 | 6B4 24B3 25A3 |
| | =PP3V3 S0 SB_VCC3_3_IDE - @m38_lib.M38 | 6B4 24C3 25B3 |
| | =PP3V3 S0 SB VCC3 3 - @m38_lib.M38 | 6B4 24B5 24B5 25B8 25C6 |
| | =PP3V3 S0 SB PM - @m38_lib.M38 | 6B4 26D4 |
| | =PP3V3_S0_SB_PCI - @m38_lib.M38 | 6B4 26D1 |
| | =PP3V3 S0 SB GPIO - @m38_lib.M38 | 6B4 21C3 21D3 23B3 23D5 |
| | =PP3V3 S0 SB 3V3 1V5 VCCHDA - @m38 lib.M38 | 6A4 24C3 25C4 |
| | =PP3V3 S0 SB - @m38 lib.M38 | 6B4 22B5 25D8 |
| | =PP3V3 S0 PCI - @m38 lib.M38 | 6A4 44D5 |
| | =PP3V3 S0 PATA - @m38 lib.M38 | 6B4 38D3 |
| | =PP3V3 S0 ODD TSENS - @m38 lib.M38 | 6A4 66B4 |
| | =PP3V3 S0 NB VCC HV - @m38 lib.M38 | 6B4 17C6 19A8 19C7 |
| | =PP3V3 S0 NB TVDAC - @m38 lib.M38 | 6B4 19C7 |
| | =PP3V3 S0 NB PM - @m38_lib.M38 | 6B4 |
| | =PP3V3 S0 LCD - @m38_lib.M38 | 88C6 94C8 |
| | =PP3V3 S0 IMVP - @m38_lib.M38 | 6A4 75D8 |
| | =PP3V3 S0 HD TSENS - @m38 lib.M38 | 6B4 66B5 |
| | =PP3V3 S0 GPU CLOCKS - @m38 lib.M38 | 88C6 |
| | =PP3V3 S0 GPUBBP - @m38 lib.M38 | 85B8 88C6 |
| | =PP3V3 S0 GPUBBN - @m38 lib.M38 | 85B5 88C6 |
| | =PP3V3 S0 GPUBBCTL - @m38_lib.M38 | 85A7 88C6 |
| | =PP3V3 S0 GPU - @m38_lib.M38 | 85B3 88C6 91D2 93A1 |
| | =PP3V3_S0_FAN - @m38_lib.M38 | 6B4 59A8 59B7 65B7 65C7 66C7 |
| | =PP3V3_S0_CK410 - @m38_lib.M38 | 6A4 33C8 33D3 33D8 34D4 |
| | =PP3V3_S0_AUDIO - @m38_lib.M38 | 6A4 68A6 68D7 72A6 72C8 72D5 73B8 73C2 74B5 74C5 74D5 |
| | =PP3V3 S0 AIRPORT - @m38 lib.M38 | 6A4 53C2 |
| | =PP3V3 S0 2V5REG - @m38 lib.M38 | 6A4 77D6 |
| | =PP3V3 GPU - @m38 lib.M38 | 88C6 94B3 |
| | =PP3V3 DDC LCD - @m38 lib.M38 | 88C6 94A7 94C7 |
| | =PP3V3 DDC DVI - @m38 lib.M38 | 88C6 97D2 |
| =PP3V3 S3 1V2REG | =PP3V3 S3 1V2REG - @m38 lib.M38 | 6D3 77B7 |
| | =PP3V3 S3 USB - @m38_lib.M38 | 6C3 49C5 |
| | =PP3V3 S3 VGASYNC - @m38_lib.M38 | 6C3 97A4 97B4 |
| | PP3V3 S3 - @m38 lib.M38 | 6A7 6D4 53C4 59C8 59D3 83B3 |
| | =PP3V3 S3 BT - @m38 lib.M38 | 6D3 47B3 |
| | =PP3V3 S3 TPM - @m38 lib.M38 | 6D3 67C2 |
| | =PP3V3 S3 ENET - @m38 lib.M38 | 6D3 41A5 41B3 41C3 41D6 41D8 42D3 42D8 43D2 |
| | PP3V3 S3 - @m38 lib.M38 | 6A7 6D4 53C4 59C8 59D3 83B3 |
| | =PP3V3 S3 VGASYNC - @m38_lib.M38 | 6C3 97A4 97B4 |
| | =PP3V3_S3_USB - @m38_lib.M38 | 6C3 49C5 |
| | =PP3V3_S3_TPM - @m38_lib.M38 | 6D3 67C2 |
| | =PP3V3_S3_ENET - @m38_lib.M38 | 6D3 41A5 41B3 41C3 41D6 41D8 42D3 42D8 43D2 |
| | =PP3V3_S3_BT - @m38_lib.M38 | 6D3 47B3 |
| =PP3V3_S5_DEBUG | =PP3V3 S5 DEBUG - @m38_lib.M38 | 6D1 60D6 |
| | =PP3V3 S5 ROM - @m38 lib.M38 | 6D1 63D4 |
| | PP3V3 S5 - @m38 lib.M38 | 5D2 6C8 6D2 6D8 6D8 26C1 26C4 26C4 59A8 65B7 65D7 66D7 76D2 76D4 77B8 77C7 77C8 77C8 77D8 79A3 79A5 79B3 80A5 80B3 81A5 81A5 83C3 |
| | =PP3V3 S5 SMC - @m38_lib.M38 | 6D1 58C2 58D3 58D4 59A4 59A5 59C4 59D3 59D8 |
| | =PP3V3 S5 FW - @m38_lib.M38 | 6D1 44A6 44B2 44B7 44D5 44D7 45D6 46A8 |
| | =PP3V3 S5 SB IO - @m38 lib.M38 | 6D1 22C6 27C6 |
| | =PP3V3 S5 SB 3V3 1V5 VCCSUSHDA - @m38 lib.M38 | 6D1 24C3 |
| | =PP3V3 S5 SB VCCSUS3 3 USB - @m38 lib.M38 | 6D1 24B3 25D1 |
| | =PP3V3 S5 SB_VCCSUS3_3 - @m38_lib.M38 | 6D1 24A5 24B3 25B6 25D1 |
| | =PP3V3_S5_SB_PM - @m38_lib.M38 | 6D1 11B5 23D1 |
| | =PP3V3_S5_SB_USB - @m38_lib.M38 | 6D1 22D8 |
| | =PP3V3_S5_SB - @m38_lib.M38 | 6D1 23A7 23B3 23B7 23D4 23D8 25C8 26D8 |
| | PP3V3 S5 - @m38_lib.M38 | 5D2 6C8 6D2 6D8 6D8 26C1 26C4 26C4 59A8 65B7 65D7 66D7 76D2 76D4 77B8 77C7 77C8 77C8 77D8 79A3 79A5 79B3 80A5 80B3 81A5 81A5 83C3 |
| | =PP3V3 S5 SMC - @m38 lib.M38 | 6D1 58C2 58D3 58D4 59A4 59A5 59C4 59D3 59D8 |
| | =PP3V3 S5 SB VCCSUS3_3_USB - @m38 lib.M38 | 6D1 24B3 25D1 |
| | =PP3V3 S5 SB VCCSUS3 3 - @m38 lib.M38 | 6D1 24A5 24B3 25B6 25D1 |
| | =PP3V3 S5 SB USB - @m38 lib.M38 | 6D1 22D8 |
| | =PP3V3 S5 SB PM - @m38 lib.M38 | 6D1 11B5 23D1 |
| | =PP3V3 S5 SB IO - @m38 lib.M38 | 6D1 22C6 27C6 |
| | =PP3V3 S5 SB 3V3 1V5 VCCSUSHDA - @m38 lib.M38 | 6D1 24C3 |
| | =PP3V3 S5 SB - @m38_lib.M38 | 6D1 23A7 23B3 23B7 23D4 23D8 25C8 26D8 |
| | =PP3V3_S5_ROM - @m38_lib.M38 | 6D1 63D4 |
| | =PP3V3_S5_FW - @m38_lib.M38 | 6D1 44A6 44B2 44B7 44D5 44D7 45D6 46A8 |
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| FB B0 VREF0 | FB B0 VREF0 - @m38 lib.M38 | 90C7 |
| FB B0 VREF1 | FB B0 VREF1 - @m38 lib.M38 | 90C7 |
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| FSB_ADSTB_L<1> | FSB_ADSTB_L<1> - @m38_lib.M38 | 5D7 5D8 7C7 12C4 |
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| FSB_A_L<26> | FSB_A_L<26> - @m38_lib.M38 | 7C7 12C4 |
| FSB_A_L<27> | FSB_A_L<27> - @m38_lib.M38 | 5D7 5D8 7C7 12C4 |
| FSB_A_L<28> | FSB_A_L<28> - @m38_lib.M38 | 7C7 12C4 |
| FSB_A_L<29> | FSB_A_L<29> - @m38_lib.M38 | 7C7 12C4 |
| FSB_A_L<30> | FSB_A_L<30> - @m38_lib.M38 | 7C7 12C4 |
| FSB_A_L<31> | FSB_A_L<31> - @m38_lib.M38 | 7C7 12C4 |
| FSB_BNR_L | FSB_BNR_L - @m38_lib.M38 | 5C7 7D6 12C4 |
| FSB_BPRI_L | FSB_BPRI_L - @m38_lib.M38 | 7D6 12C4 |
| FSB_BREQ0_L | FSB_BREQ0_L - @m38_lib.M38 | 5C7 7D6 12C4 |
| FSB_CLK_CPU_N | FSB_CLK_CPU_N - @m38_lib.M38 | 5C8 7C6 34B4 34C2 |
| FSB_CLK_CPU_P | FSB_CLK_CPU_P - @m38_lib.M38 | 5C8 7C6 34B4 34C2 |
| FSB_CLK_NB_N | FSB_CLK_NB_N - @m38_lib.M38 | 5C7 12A6 34B4 34C2 |
| FSB_CLK_NB_P | FSB_CLK_NB_P - @m38_lib.M38 | 5C7 12A6 34B4 34C2 |
| FSB_CPURST_L | FSB_CPURST_L - @m38_lib.M38 | 5C8 7D6 11B5 12C4 |
| FSB_DBSY_L | FSB_DBSY_L - @m38_lib.M38 | 5C7 7D6 12B4 |
| FSB_DEFER_L | FSB_DEFER_L - @m38_lib.M38 | 7D6 12B4 |
| FSB_DINV_L<0> | FSB_DINV_L<0> - @m38_lib.M38 | 5D7 5D8 7C4 12B4 |
| FSB_DINV_L<1> | FSB_DINV_L<1> - @m38_lib.M38 | 5D7 5D8 7B4 12B4 |
| FSB_DINV_L<2> | FSB_DINV_L<2> - @m38_lib.M38 | 5D7 5D8 7C2 12B4 |
| FSB_DINV_L<3> | FSB_DINV_L<3> - @m38_lib.M38 | 5D7 5D8 7B2 12B4 |
| FSB_DPWR_L | FSB_DPWR_L - @m38_lib.M38 | 5C7 7B3 12B4 |
| FSB_DRDY_L | FSB_DRDY_L - @m38_lib.M38 | 7D6 12B4 |
| FSB_DSTBN_L<0> | FSB_DSTBN_L<0> - @m38_lib.M38 | 5D7 5D8 7C4 12B4 |
| FSB_DSTBN_L<1> | FSB_DSTBN_L<1> - @m38_lib.M38 | 5D7 5D8 7B4 12B4 |
| FSB_DSTBN_L<2> | FSB_DSTBN_L<2> - @m38_lib.M38 | 5D7 5D8 7C2 12B4 |
| FSB_DSTBN_L<3> | FSB_DSTBN_L<3> - @m38_lib.M38 | 5D7 5D8 7B2 12B4 |
| FSB_DSTBP_L<0> | FSB_DSTBP_L<0> - @m38_lib.M38 | 5D7 5D8 7C4 12B4 |
| FSB_DSTBP_L<1> | FSB_DSTBP_L<1> - @m38_lib.M38 | 5D7 5D8 7B4 12B4 |
| FSB_DSTBP_L<2> | FSB_DSTBP_L<2> - @m38_lib.M38 | 5D7 5D8 7C2 12B4 |
| FSB_DSTBP_L<3> | FSB_DSTBP_L<3> - @m38_lib.M38 | 5D7 5D8 7B2 12B4 |
| FSB_D_L<0> | FSB_D_L<0> - @m38_lib.M38 | 5D7 5D8 7C4 12D6 |
| FSB_D_L<1> | FSB_D_L<1> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<2> | FSB_D_L<2> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<3> | FSB_D_L<3> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<4> | FSB_D_L<4> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<5> | FSB_D_L<5> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<6> | FSB_D_L<6> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<7> | FSB_D_L<7> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<8> | FSB_D_L<8> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<9> | FSB_D_L<9> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<10> | FSB_D_L<10> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<11> | FSB_D_L<11> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<12> | FSB_D_L<12> - @m38_lib.M38 | 7C4 12D6 |
| FSB_D_L<13> | FSB_D_L<13> - @m38_lib.M38 | 7C4 12C6 |
| FSB_D_L<14> | FSB_D_L<14> - @m38_lib.M38 | 7C4 12C6 |
| FSB_D_L<15> | FSB_D_L<15> - @m38_lib.M38 | 7C4 12C6 |
| FSB_D_L<16> | FSB_D_L<16> - @m38_lib.M38 | 5D7 5D8 7C4 12C6 |
| FSB_D_L<17> | FSB_D_L<17> - @m38_lib.M38 | 7C4 12C6 |
| FSB_D_L<18> | FSB_D_L<18> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<19> | FSB_D_L<19> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<20> | FSB_D_L<20> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<21> | FSB_D_L<21> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<22> | FSB_D_L<22> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<23> | FSB_D_L<23> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<24> | FSB_D_L<24> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<25> | FSB_D_L<25> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<26> | FSB_D_L<26> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<27> | FSB_D_L<27> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<28> | FSB_D_L<28> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<29> | FSB_D_L<29> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<30> | FSB_D_L<30> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<31> | FSB_D_L<31> - @m38_lib.M38 | 7B4 12C6 |
| FSB_D_L<32> | FSB_D_L<32> - @m38_lib.M38 | 7C2 12C6 |
| FSB_D_L<33> | FSB_D_L<33> - @m38_lib.M38 | 7C2 12C6 |
| FSB_D_L<34> | FSB_D_L<34> - @m38_lib.M38 | 7C2 12C6 |
| FSB_D_L<35> | FSB_D_L<35> - @m38_lib.M38 | 7C2 12C6 |
| FSB_D_L<36> | FSB_D_L<36> - @m38_lib.M38 | 7C2 12C6 |
| FSB_D_L<37> | FSB_D_L<37> - @m38_lib.M38 | 7C2 12C6 |
| FSB_D_L<38> | FSB_D_L<38> - @m38_lib.M38 | 7C2 12C6 |
| FSB_D_L<39> | FSB_D_L<39> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<40> | FSB_D_L<40> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<41> | FSB_D_L<41> - @m38_lib.M38 | 5D7 5D8 7C2 12B6 |
| FSB_D_L<42> | FSB_D_L<42> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<43> | FSB_D_L<43> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<44> | FSB_D_L<44> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<45> | FSB_D_L<45> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<46> | FSB_D_L<46> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<47> | FSB_D_L<47> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<48> | FSB_D_L<48> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<49> | FSB_D_L<49> - @m38_lib.M38 | 7C2 12B6 |
| FSB_D_L<50> | FSB_D_L<50> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<51> | FSB_D_L<51> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<52> | FSB_D_L<52> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<53> | FSB_D_L<53> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<54> | FSB_D_L<54> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<55> | FSB_D_L<55> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<56> | FSB_D_L<56> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<57> | FSB_D_L<57> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<58> | FSB_D_L<58> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<59> | FSB_D_L<59> - @m38_lib.M38 | 5D7 5D8 7B2 12B6 |
| FSB_D_L<60> | FSB_D_L<60> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<61> | FSB_D_L<61> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<62> | FSB_D_L<62> - @m38_lib.M38 | 7B2 12B6 |
| FSB_D_L<63> | FSB_D_L<63> - @m38_lib.M38 | 7B2 12B6 |
| FSB_HITM_L | FSB_HITM_L - @m38_lib.M38 | 5C7 7D6 12B4 |
| FSB_HIT_L | FSB_HIT_L - @m38_lib.M38 | 5C7 7D6 12B4 |
| FSB_IERR_L | FSB_IERR_L - @m38_lib.M38 | 7D6 |
| FSB_LOCK_L | FSB_LOCK_L - @m38_lib.M38 | 5D7 5D8 7D6 12B4 |
| FSB_REQ_L<0> | FSB_REQ_L<0> - @m38_lib.M38 | 5C7 7D7 12B4 |
| FSB_REQ_L<1> | FSB_REQ_L<1> - @m38_lib.M38 | 5C7 7D7 12B4 |
| FSB_REQ_L<2> | FSB_REQ_L<2> - @m38_lib.M38 | 5C7 7D7 12A4 |
| FSB_REQ_L<3> | FSB_REQ_L<3> - @m38_lib.M38 | 5C7 7D7 12A4 |
| FSB_REQ_L<4> | FSB_REQ_L<4> - @m38_lib.M38 | 5C7 7D7 12A4 |
| FSB_RS_L<0> | FSB_RS_L<0> - @m38_lib.M38 | 7D6 12A4 |
| FSB_RS_L<1> | FSB_RS_L<1> - @m38_lib.M38 | 7D6 12A4 |
| FSB_RS_L<2> | FSB_RS_L<2> - @m38_lib.M38 | 7D6 12A4 |
| FSB_SLPCPU_L | FSB_SLPCPU_L - @m38_lib.M38 | 7A3 12A4 |
| FSB_TRDY_L | FSB_TRDY_L - @m38_lib.M38 | 7D6 12A4 |
| FWH_INIT_L | FWH_INIT_L - @m38_lib.M38 | 21C4 59C5 60C3 |
| | SMC_CPU_INIT_3_3_L - @m38_lib.M38 | 58D5 59C6 |
| FWH_MFG_MODE | FWH_MFG_MODE - @m38_lib.M38 | 23A6 23C5 |
| FW_A_TPA_N | FW_A_TPA_N - @m38_lib.M38 | 44C3 46C8 |
| | FW_PORT0_TPA_N - @m38_lib.M38 | 46C5 46C6 |
| FW_A_TPA_P | FW_A_TPA_P - @m38_lib.M38 | 44C3 46C8 |
| | FW_PORT0_TPA_P - @m38_lib.M38 | 46C5 46C6 |
| FW_A_TPBIAS | FW_A_TPBIAS - @m38_lib.M38 | 44C4 46C8 |
| FW_A_TPB_N | FW_A_TPB_N - @m38_lib.M38 | 44C3 46C8 |
| | FW_PORT0_TPB_N - @m38_lib.M38 | 46C5 46C6 |
| FW_A_TPB_P | FW_A_TPB_P - @m38_lib.M38 | 44C3 46C8 |
| | FW_PORT0_TPB_P - @m38_lib.M38 | 46C5 46C6 |
| FW_B_TPA_N | FW_B_TPA_N - @m38_lib.M38 | 44C3 46C8 |
| | FW_PORT1_TPA_N - @m38_lib.M38 | 46B5 46C6 |
| FW_B_TPA_P | FW_B_TPA_P - @m38_lib.M38 | 44C3 46C8 |
| | FW_PORT1_TPA_P - @m38_lib.M38 | 46B5 46C6 |
| FW_B_TPBIAS | FW_B_TPBIAS - @m38_lib.M38 | 44C4 46C8 |
| FW_B_TPB_N | FW_B_TPB_N - @m38_lib.M38 | 44C3 46C8 |
| | FW_PORT1_TPB_N - @m38_lib.M38 | 46B5 46C6 |
| FW_B_TPB_P | FW_B_TPB_P - @m38_lib.M38 | 44C3 46C8 |
| | FW_PORT1_TPB_P - @m38_lib.M38 | 46B5 46C6 |
| FW_CARDBUS_L | FW_CARDBUS_L - @m38_lib.M38 | 44B5 |
| FW_CONTENDER | FW_CONTENDER - @m38_lib.M38 | 44B3 |
| FW_CPS | FW_CPS - @m38_lib.M38 | 44C3 |
| FW_C_TPA_N | FW_C_TPA_N - @m38_lib.M38 | 44C3 46B8 |
| | TP_FW_C_TPA_N - @m38_lib.M38 | 46B7 |
| FW_C_TPA_P | FW_C_TPA_P - @m38_lib.M38 | 44C3 46B8 |
| | TP_FW_C_TPA_P - @m38_lib.M38 | 46B7 |
| FW_C_TPBIAS | FW_C_TPBIAS - @m38_lib.M38 | 44C4 46B8 |
| | TP_FW_C_TPBIAS - @m38_lib.M38 | 46B7 |
| FW_C_TPB_N | FW_C_TPB_N - @m38_lib.M38 | 44C3 46A8 |
| | TP_FW_C_TPB_N - @m38_lib.M38 | 46A7 |
| FW_C_TPB_P | FW_C_TPB_P - @m38_lib.M38 | 44C3 46A8 |
| | TP_FW_C_TPB_P - @m38_lib.M38 | 46A7 |
| FW_PC0 | FW_PC0 - @m38_lib.M38 | 44B3 |
| FW_PC1 | FW_PC1 - @m38_lib.M38 | 44B3 |
| FW_PC2 | FW_PC2 - @m38_lib.M38 | 44B3 |
| FW_PORT0_TPA_FL_N | FW_PORT0_TPA_FL_N - @m38_lib.M38 | 46C2 |
| FW_PORT0_TPA_FL_P | FW_PORT0_TPA_FL_P - @m38_lib.M38 | 46C2 |
| FW_PORT0_TPB_FL_N | FW_PORT0_TPB_FL_N - @m38_lib.M38 | 46C2 |
| FW_PORT0_TPB_FL_P | FW_PORT0_TPB_FL_P - @m38_lib.M38 | 46C2 |
| FW_PORT1_TPA_FL_N | FW_PORT1_TPA_FL_N - @m38_lib.M38 | 46B2 |
| FW_PORT1_TPA_FL_P | FW_PORT1_TPA_FL_P - @m38_lib.M38 | 46B2 |
| FW_PORT1_TPB_FL_N | FW_PORT1_TPB_FL_N - @m38_lib.M38 | 46B2 |
| FW_PORT1_TPB_FL_P | FW_PORT1_TPB_FL_P - @m38_lib.M38 | 46B2 |
| FW_R0 | FW_R0 - @m38_lib.M38 | 44C4 |
| FW_R1 | FW_R1 - @m38_lib.M38 | 44C4 |
| FW_RESET_L | FW_RESET_L - @m38_lib.M38 | 44C2 44C3 |
| FW_ROM_CLK | FW_ROM_CLK - @m38_lib.M38 | 44B4 |
| FW_SE | FW_SE - @m38_lib.M38 | 44B3 |
| FW_SM | FW_SM - @m38_lib.M38 | 44B3 |
| FW_TEST | FW_TEST - @m38_lib.M38 | 44B3 |
| FW_TPA_C<0> | FW_TPA_C<0> - @m38_lib.M38 | 46B8 |
| FW_TPA_C<1> | FW_TPA_C<1> - @m38_lib.M38 | 46B7 |
| FW_VP | FW_VP - @m38_lib.M38 | 46D4 |
| FW_VP_R | FW_VP_R - @m38_lib.M38 | 46D5 |
| FW_XTAL_X0 | FW_XTAL_X0 - @m38_lib.M38 | 44D2 44D3 |
| FW_XTAL_XI | FW_XTAL_XI - @m38_lib.M38 | 44D2 44D3 |
| FW_XTAL_XR | FW_XTAL_XR - @m38_lib.M38 | 44D2 |
| GATE_3V3_S3 | GATE_3V3_S3 - @m38_lib.M38 | 83B4 |
| GATE_5V_S3 | GATE_5V_S3 - @m38_lib.M38 | 83C4 |
| GND_AUDIO | GND_AUDIO - @m38_lib.M38 | 6C2 74D2 |
| GND_AUDIO_CODEC | GND_AUDIO_CODEC - @m38_lib.M38 | 68A6 68B7 68D2 72B8 72C8 73A8 73C2 73D4 74A3 74A5 74A6 74A7 74B5 74B7 74C1 74C2 74C8 74D1 |
| GND_AUDIO_MIC_CONN | GND_AUDIO_MIC_CONN - @m38_lib.M38 | 47C2 73C4 |
| GND_AUDIO_SPKRAMP | GND_AUDIO_SPKRAMP - @m38_lib.M38 | 6C2 72B1 74D2 |
| GND_AUDIO_SPKRAMP_PLANE | GND_AUDIO_SPKRAMP_PLANE - @m38_lib.M38 | 72A6 72B2 72B6 72D2 72D8 74C2 |
| GND_BNDI | GND_BNDI - @m38_lib.M38 | 47B2 47D2 |
| GND_CHASSIS_AUDIO_EXTERNAL | GND_CHASSIS_AUDIO_EXTERNAL - @m38_lib.M38 | 6B2 73A8 73C7 73D8 74C1 74D2 |
| | GND_CHASSIS_IO_LEFT - @m38_lib.M38 | 6B2 |
| | GND_CHASSIS_USB - @m38_lib.M38 | 6B2 47A4 47B4 47C4 |
| | GND_CHASSIS_IO_LEFT - @m38_lib.M38 | 6B2 |
| GND_CHASSIS_AUDIO_INTERNAL | GND_CHASSIS_AUDIO_INTERNAL - @m38_lib.M38 | 6A2 73C6 |
| | GND_CHASSIS_BNDI - @m38_lib.M38 | 6A2 47C2 47C2 47D2 |
| GND_CHASSIS_FIREWIRE | GND_CHASSIS_FIREWIRE - @m38_lib.M38 | 6B2 46A1 46C1 |
| | GND_CHASSIS_RJ45 - @m38_lib.M38 | 6B2 43C6 |
| | GND_CHASSIS_VGA - @m38_lib.M38 | 6B2 97C4 |
| | GND_CHASSIS_RJ45 - @m38_lib.M38 | 6B2 43C6 |
| | GND_CHASSIS_IO_RIGHT - @m38_lib.M38 | 6B1 |
| GND_CPU_ISENSE_OPAMP | GND_CPU_ISENSE_OPAMP - @m38_lib.M38 | 76D3 |
| GND_GPUVCORE_SGND | GND_GPUVCORE_SGND - @m38_lib.M38 | 85C7 |
| GND_GPU_A2VSSN | GND_GPU_A2VSSN - @m38_lib.M38 | 93B7 |
| GND_GPU_A2VSSQ | GND_GPU_A2VSSQ - @m38_lib.M38 | 93B7 |
| GND_GPU_AVSSN | GND_GPU_AVSSN - @m38_lib.M38 | 93C7 |
| GND_GPU_AVSSQ | GND_GPU_AVSSQ - @m38_lib.M38 | 93C7 |
| GND_GPU_LPVSS | GND_GPU_LPVSS - @m38_lib.M38 | 93B7 |
| GND_GPU_LVSSR | GND_GPU_LVSSR - @m38_lib.M38 | 93A7 |
| GND_GPU_MPVSS | GND_GPU_MPVSS - @m38_lib.M38 | 91A5 |
| GND_GPU_PCIE_PVSS | GND_GPU_PCIE_PVSS - @m38_lib.M38 | 84B8 |
| GND_GPU_PVSS | GND_GPU_PVSS - @m38_lib.M38 | 91A5 |
| GND_GPU_TPVSS | GND_GPU_TPVSS - @m38_lib.M38 | 93C7 |
| GND_GPU_TXVSSR | GND_GPU_TXVSSR - @m38_lib.M38 | 93C7 |
| GND_GPU_VSSRH0 | GND_GPU_VSSRH0 - @m38_lib.M38 | 87A7 |
| GND_GPU_VSSRH1 | GND_GPU_VSSRH1 - @m38_lib.M38 | 87A4 |
| GND_IMVP6_SGND | GND_IMVP6_SGND - @m38_lib.M38 | 75A4 75B6 75B8 75D7 |
| GND_NB_VSSA_LVDS | GND_NB_VSSA_LVDS - @m38_lib.M38 | 17C6 19C2 |
| | TP_GND_NB_VSSA_LVDS - @m38_lib.M38 | 19C1 |
| GND_NEXT_TO_SMC | GND_NEXT_TO_SMC - @m38_lib.M38 | 59B2 76C2 |
| GND_SMC_AVSS | GND_SMC_AVSS - @m38_lib.M38 | 58B2 58C4 59A3 59B3 76B2 76B6 76C2 76C5 85C1 |
| GND_USB_PORT0 | GND_USB_PORT0 - @m38_lib.M38 | 47D5 |
| GND_USB_PORT1 | GND_USB_PORT1 - @m38_lib.M38 | 47C5 |
| GND_USB_PORT2 | GND_USB_PORT2 - @m38_lib.M38 | 47A5 |
| GPUBBN_D | GPUBBN_D - @m38_lib.M38 | 85A3 |
| GPUBBN_D_RC | GPUBBN_D_RC - @m38_lib.M38 | 85A3 |
| GPUBBN_FB | GPUBBN_FB - @m38_lib.M38 | 85A3 |
| GPUBBN_SW | GPUBBN_SW - @m38_lib.M38 | 85A4 |
| GPUBBP_ADJ | GPUBBP_ADJ - @m38_lib.M38 | 85B7 |
| GPUBBP_ADJ_LOW | GPUBBP_ADJ_LOW - @m38_lib.M38 | 85A7 |
| GPUBB_EN | GPUBB_EN - @m38_lib.M38 | 85A4 85A6 85B8 |
| GPUBB_EN_L | GPUBB_EN_L - @m38_lib.M38 | 85A1 85A5 85B7 |
| GPUISENS_NEG | GPUISENS_NEG - @m38_lib.M38 | 85D3 |
| GPUISENS_NTC | GPUISENS_NTC - @m38_lib.M38 | 85D3 |
| GPUISENS_POS | GPUISENS_POS - @m38_lib.M38 | 85D3 |
| GPUISENS_RC | GPUISENS_RC - @m38_lib.M38 | 85D3 |
| GPUVCORE_BOOT | GPUVCORE_BOOT - @m38_lib.M38 | 85C5 |
| GPUVCORE_COMP | GPUVCORE_COMP - @m38_lib.M38 | 85C7 |
| GPUVCORE_COMP_R | GPUVCORE_COMP_R - @m38_lib.M38 | 85C7 |
| GPUVCORE_EN | GPUVCORE_EN - @m38_lib.M38 | 85C8 88A8 |
| GPUVCORE_FB | GPUVCORE_FB - @m38_lib.M38 | 85C7 |
| GPUVCORE_FB_LOW | GPUVCORE_FB_LOW - @m38_lib.M38 | 85C3 |
| GPUVCORE_FCCM | GPUVCORE_FCCM - @m38_lib.M38 | 85C7 |
| GPUVCORE_FSET | GPUVCORE_FSET - @m38_lib.M38 | 85C7 |
| GPUVCORE_IOUT | GPUVCORE_IOUT - @m38_lib.M38 | 59C5 85D1 |
| | SMC_GPU_ISENSE - @m38_lib.M38 | 58D5 59C6 |
| GPUVCORE_ISEN | GPUVCORE_ISEN - @m38_lib.M38 | 85C5 |
| GPUVCORE_ISENSE_CAL | GPUVCORE_ISENSE_CAL - @m38_lib.M38 | 76A4 |
| GPUVCORE_LG | GPUVCORE_LG - @m38_lib.M38 | 85C5 |
| GPUVCORE_PGOOD | GPUVCORE_PGOOD - @m38_lib.M38 | 77A4 85C8 |
| GPUVCORE_PHASE | GPUVCORE_PHASE - @m38_lib.M38 | 85C5 |
| GPUVCORE_UG | GPUVCORE_UG - @m38_lib.M38 | 85C5 |
| GPU_B2 | GPU_B2 - @m38_lib.M38 | 93B3 97B8 |
| GPU_BB_EN | GPU_BB_EN - @m38_lib.M38 | 85A7 |
| GPU_CLK27M | GPU_CLK27M - @m38_lib.M38 | 92C6 |
| | GPU_XTALIN - @m38_lib.M38 | 91A5 92C5 |
| GPU_CLK100M_PCIE_N | GPU_CLK100M_PCIE_N - @m38_lib.M38 | 5C6 34A4 34B2 84A5 |
| GPU_CLK100M_PCIE_P | GPU_CLK100M_PCIE_P - @m38_lib.M38 | 5C6 34A4 34B2 84A5 |
| GPU_DDC_A_CLK | GPU_DDC_A_CLK - @m38_lib.M38 | 93A3 97D1 |
| GPU_DDC_A_DATA | GPU_DDC_A_DATA - @m38_lib.M38 | 93A3 97C1 |
| GPU_DDC_B_CLK | GPU_DDC_B_CLK - @m38_lib.M38 | 88B1 93A3 |
| | TP_GPU_DDC_B_CLK - @m38_lib.M38 | 88B3 |
| GPU_DDC_B_DATA | GPU_DDC_B_DATA - @m38_lib.M38 | 88B1 93A3 |
| | TP_GPU_DDC_B_DATA - @m38_lib.M38 | 88B3 |
| GPU_DDC_C_CLK | GPU_DDC_C_CLK - @m38_lib.M38 | 93A1 93A3 94A7 94C7 |
| GPU_DDC_C_DATA | GPU_DDC_C_DATA - @m38_lib.M38 | 93A1 93A3 94A6 94C7 |
| GPU_DIGON | GPU_DIGON - @m38_lib.M38 | 91C3 94A4 94C8 |
| GPU_EXT_TDIODE_N | GPU_EXT_TDIODE_N - @m38_lib.M38 | 61B7 |
| GPU_EXT_TDIODE_P | GPU_EXT_TDIODE_P - @m38_lib.M38 | 61C7 |
| GPU_G2 | GPU_G2 - @m38_lib.M38 | 93B3 97A8 |
| GPU_GENERICA | GPU_GENERICA - @m38_lib.M38 | 91C3 95B4 |
| | TP_GPU_GENERICA - @m38_lib.M38 | 95B6 |
| GPU_GENERICB | GPU_GENERICB - @m38_lib.M38 | 91C3 95B4 |
| | TP_GPU_GENERICB - @m38_lib.M38 | 95B6 |
| GPU_GENERICC | GPU_GENERICC - @m38_lib.M38 | 91C3 95B4 |
| | TP_GPU_GENERICC - @m38_lib.M38 | 95B6 |
| GPU_GENERICD | GPU_GENERICD - @m38_lib.M38 | 85A7 91C3 |
| GPU_GPIO_0 | GPU_GPIO_0 - @m38_lib.M38 | 88D5 91D3 94C3 |
| GPU_GPIO_1 | GPU_GPIO_1 - @m38_lib.M38 | 88D5 91D3 |
| GPU_GPIO_2 | GPU_GPIO_2 - @m38_lib.M38 | 88D5 91D3 |
| GPU_GPIO_3 | GPU_GPIO_3 - @m38_lib.M38 | 88D5 91D3 |
| GPU_GPIO_4 | GPU_GPIO_4 - @m38_lib.M38 | 88C5 91D3 |
| GPU_GPIO_5 | GPU_GPIO_5 - @m38_lib.M38 | 88C5 91D3 |
| GPU_GPIO_6 | GPU_GPIO_6 - @m38_lib.M38 | 88C5 91D3 |
| GPU_GPIO_7 | GPU_GPIO_7 - @m38_lib.M38 | 88C3 91D3 |
| | TP_GPU_GPIO_7 - @m38_lib.M38 | 88C5 |
| GPU_GPIO_8 | GPU_GPIO_8 - @m38_lib.M38 | 88C5 91D3 |
| GPU_GPIO_9 | GPU_GPIO_9 - @m38_lib.M38 | 88C5 91C3 |
| GPU_GPIO_10 | GPU_GPIO_10 - @m38_lib.M38 | 88C3 91C3 |
| | NC_GPU_GPIO_10 - @m38_lib.M38 | 88C5 |
| GPU_GPIO_11 | GPU_GPIO_11 - @m38_lib.M38 | 88B5 91C3 |
| GPU_GPIO_12 | GPU_GPIO_12 - @m38_lib.M38 | 88C5 91C3 |
| GPU_GPIO_13 | GPU_GPIO_13 - @m38_lib.M38 | 88C5 91C3 |
| GPU_GPIO_14 | GPU_GPIO_14 - @m38_lib.M38 | 88C1 91C3 |
| | TP_GPU_GPIO_14 - @m38_lib.M38 | 88C3 |
| GPU_GPIO_15 | GPU_GPIO_15 - @m38_lib.M38 | 88B4 91C3 |
| | GPU_VCORE_LOW - @m38_lib.M38 | 85B4 88B5 |
| GPU_GPIO_17 | GPU_GPIO_17 - @m38_lib.M38 | 88C1 91C3 |
| | TP_GPU_GPIO_17 - @m38_lib.M38 | 88C3 |
| GPU_GPIO_18 | GPU_GPIO_18 - @m38_lib.M38 | 91D5 95B4 |
| GPU_GPIO_19 | GPU_GPIO_19 - @m38_lib.M38 | 91D5 95B4 |
| GPU_GPIO_20 | GPU_GPIO_20 - @m38_lib.M38 | 91D5 95B4 |
| GPU_GPIO_21 | GPU_GPIO_21 - @m38_lib.M38 | 91D5 95B4 |
| GPU_GPIO_22 | GPU_GPIO_22 - @m38_lib.M38 | 91D5 95B4 |
| GPU_GPIO_23 | GPU_GPIO_23 - @m38_lib.M38 | 91D5 95B4 |
| GPU_GPIO_24 | GPU_GPIO_24 - @m38_lib.M38 | 88B5 91D5 |
| GPU_GPIO_25 | GPU_GPIO_25 - @m38_lib.M38 | 91D5 95C4 |
| GPU_GPIO_26 | GPU_GPIO_26 - @m38_lib.M38 | 91D5 95C4 |
| GPU_GPIO_27 | GPU_GPIO_27 - @m38_lib.M38 | 88B5 91D5 |
| GPU_GPIO_28 | GPU_GPIO_28 - @m38_lib.M38 | 88B5 91D5 |
| GPU_GPIO_29 | GPU_GPIO_29 - @m38_lib.M38 | 88B5 91C5 |
| GPU_GPIO_30 | GPU_GPIO_30 - @m38_lib.M38 | 91C5 95C4 |
| GPU_GPIO_31 | GPU_GPIO_31 - @m38_lib.M38 | 91C5 95C4 |
| GPU_GPIO_32 | GPU_GPIO_32 - @m38_lib.M38 | 91C5 95C4 |
| GPU_GPIO_33 | GPU_GPIO_33 - @m38_lib.M38 | 91C5 95C4 |
| GPU_GPIO_34 | GPU_GPIO_34 - @m38_lib.M38 | 91C5 95C4 |
| GPU_H2SYNC | GPU_H2SYNC - @m38_lib.M38 | 93B3 97A4 |
| GPU_HPD | GPU_HPD - @m38_lib.M38 | 93A5 97C1 |
| GPU_HSYNC_BUF | GPU_HSYNC_BUF - @m38_lib.M38 | 97A4 |
| GPU_MEMTEST | GPU_MEMTEST - @m38_lib.M38 | 87A3 |
| GPU_MVREFD0 | GPU_MVREFD0 - @m38_lib.M38 | 87B7 |
| GPU_MVREFD1 | GPU_MVREFD1 - @m38_lib.M38 | 87B3 |
| GPU_MVREFS0 | GPU_MVREFS0 - @m38_lib.M38 | 87B7 |
| GPU_MVREFS1 | GPU_MVREFS1 - @m38_lib.M38 | 87B3 |
| GPU_PCIE_CALI | GPU_PCIE_CALI - @m38_lib.M38 | 84A3 |
| GPU_PCIE_CALRN | GPU_PCIE_CALRN - @m38_lib.M38 | 84A3 |
| GPU_PCIE_CALRP | GPU_PCIE_CALRP - @m38_lib.M38 | 84A3 |
| GPU_PWM_RST_L | GPU_PWM_RST_L - @m38_lib.M38 | 6C7 94B3 |
| GPU_R2 | GPU_R2 - @m38_lib.M38 | 93B3 97A8 |
| GPU_TEST_MCLK | GPU_TEST_MCLK - @m38_lib.M38 | 87A3 |
| GPU_TEST_YCLK | GPU_TEST_YCLK - @m38_lib.M38 | 87A3 |
| GPU_TV_C | GPU_TV_C - @m38_lib.M38 | 88B1 93B3 |
| | TP_GPU_TV_C - @m38_lib.M38 | 88B3 |
| GPU_TV_COMP | GPU_TV_COMP - @m38_lib.M38 | 88B1 93B3 |
| | TP_GPU_TV_COMP - @m38_lib.M38 | 88B3 |
| GPU_TV_Y | GPU_TV_Y - @m38_lib.M38 | 88B1 93B3 |
| | TP_GPU_TV_Y - @m38_lib.M38 | 88B3 |
| GPU_V2SYNC | GPU_V2SYNC - @m38_lib.M38 | 93B3 97B4 |
| GPU_VARY_BL | GPU_VARY_BL - @m38_lib.M38 | 91C3 94B4 |
| GPU_VCORE_HIGH | GPU_VCORE_HIGH - @m38_lib.M38 | 85A8 85B4 |
| GPU_VCORE_HIGH_RC | GPU_VCORE_HIGH_RC - @m38_lib.M38 | 85B3 |
| GPU_VGA_B | GPU_VGA_B - @m38_lib.M38 | 88C1 93C3 |
| | TP_GPU_VGA_B - @m38_lib.M38 | 88C3 |
| GPU_VGA_G | GPU_VGA_G - @m38_lib.M38 | 88C1 93C3 |
| | TP_GPU_VGA_G - @m38_lib.M38 | 88C3 |
| GPU_VGA_HSYNC | GPU_VGA_HSYNC - @m38_lib.M38 | 88B1 93B3 |
| | TP_GPU_VGA_HSYNC - @m38_lib.M38 | 88B3 |
| GPU_VGA_R | GPU_VGA_R - @m38_lib.M38 | 88C1 93C3 |
| | TP_GPU_VGA_R - @m38_lib.M38 | 88C3 |
| GPU_VGA_VSYNC | GPU_VGA_VSYNC - @m38_lib.M38 | 88B1 93B3 |
| | TP_GPU_VGA_VSYNC - @m38_lib.M38 | 88B3 |
| GPU_VSYNC_BUF | GPU_VSYNC_BUF - @m38_lib.M38 | 97A4 |
| GPU_XTALOUT | GPU_XTALOUT - @m38_lib.M38 | 91A5 |
| I2C_ALS_SCL | I2C_ALS_SCL - @m38_lib.M38 | 59B1 59C8 |
| | SMB_A_S3_CLK - @m38_lib.M38 | 58B5 59B2 59D1 |
| I2C_ALS_SDA | I2C_ALS_SDA - @m38_lib.M38 | 59B1 59C8 |
| | SMB_A_S3_DATA - @m38_lib.M38 | 58B5 59B2 59D1 |
| IDE_CSEL_PD | IDE_CSEL_PD - @m38_lib.M38 | 38C3 |
| IDE_DASP_L | IDE_DASP_L - @m38_lib.M38 | 38C3 |
| IDE_DASP_L_DS | IDE_DASP_L_DS - @m38_lib.M38 | 38B3 |
| IDE_IOCS16_PU | IDE_IOCS16_PU - @m38_lib.M38 | 38C1 |
| IDE_IRQ14 | IDE_IRQ14 - @m38_lib.M38 | 21B6 38C4 |
| IDE_PDA<0> | IDE_PDA<0> - @m38_lib.M38 | 21B5 38C3 |
| IDE_PDA<1> | IDE_PDA<1> - @m38_lib.M38 | 21B5 38C3 |
| IDE_PDA<2> | IDE_PDA<2> - @m38_lib.M38 | 21B5 38C1 |
| IDE_PDCS1_L | IDE_PDCS1_L - @m38_lib.M38 | 21B5 38C3 |
| IDE_PDCS3_L | IDE_PDCS3_L - @m38_lib.M38 | 21B5 38C1 |
| IDE_PDD<0> | IDE_PDD<0> - @m38_lib.M38 | 21C5 38C3 |
| IDE_PDD<1> | IDE_PDD<1> - @m38_lib.M38 | 21B5 38C3 |
| IDE_PDD<2> | IDE_PDD<2> - @m38_lib.M38 | 21B5 38D3 |
| IDE_PDD<3> | IDE_PDD<3> - @m38_lib.M38 | 21B5 38D3 |
| IDE_PDD<4> | IDE_PDD<4> - @m38_lib.M38 | 21B5 38D3 |
| IDE_PDD<5> | IDE_PDD<5> - @m38_lib.M38 | 21B5 38D3 |
| IDE_PDD<6> | IDE_PDD<6> - @m38_lib.M38 | 21B5 38D3 |
| IDE_PDD<7> | IDE_PDD<7> - @m38_lib.M38 | 21B5 38D3 |
| IDE_PDD<8> | IDE_PDD<8> - @m38_lib.M38 | 21B5 38D1 |
| IDE_PDD<9> | IDE_PDD<9> - @m38_lib.M38 | 5C8 21B5 38D1 |
| IDE_PDD<10> | IDE_PDD<10> - @m38_lib.M38 | 21B5 38D1 |
| IDE_PDD<11> | IDE_PDD<11> - @m38_lib.M38 | 21B5 38D1 |
| IDE_PDD<12> | IDE_PDD<12> - @m38_lib.M38 | 21B5 38D1 |
| IDE_PDD<13> | IDE_PDD<13> - @m38_lib.M38 | 21B5 38D1 |
| IDE_PDD<14> | IDE_PDD<14> - @m38_lib.M38 | 21B5 38D1 |
| IDE_PDD<15> | IDE_PDD<15> - @m38_lib.M38 | 21B5 38C1 |
| IDE_PDDACK_L | IDE_PDDACK_L - @m38_lib.M38 | 21B6 38C1 |
| IDE_PDDREQ | IDE_PDDREQ - @m38_lib.M38 | 21B6 38C4 |
| IDE_PDIORDY | IDE_PDIORDY - @m38_lib.M38 | 5C8 21B6 38C4 |
| IDE_PDIOR_L | IDE_PDIOR_L - @m38_lib.M38 | 5C8 21B6 38C1 |
| IDE_PDIOW_L | IDE_PDIOW_L - @m38_lib.M38 | 21B6 38C3 |
| IDE_RESET_L | IDE_RESET_L - @m38_lib.M38 | 23C3 38D3 38D6 |
| IMVP6_BOOT1 | IMVP6_BOOT1 - @m38_lib.M38 | 75A8 75C5 |
| IMVP6_BOOT2 | IMVP6_BOOT2 - @m38_lib.M38 | 75A6 75C5 |
| IMVP6_COMP | IMVP6_COMP - @m38_lib.M38 | 75A4 75B6 |
| IMVP6_COMP_RC | IMVP6_COMP_RC - @m38_lib.M38 | 75B7 |
| IMVP6_DFB | IMVP6_DFB - @m38_lib.M38 | 75A4 75B5 |
| IMVP6_DROOP | IMVP6_DROOP - @m38_lib.M38 | 75A4 75B4 76D5 |
| IMVP6_DROOP_R | IMVP6_DROOP_R - @m38_lib.M38 | 76D4 |

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|---|---|---|
| IMVP6_FB | IMVP6_FB - @m38_lib.M38 | 75A4 75B6 |
| IMVP6_FB2 | IMVP6_FB2 - @m38_lib.M38 | 75A4 75B6 |
| IMVP6_FET_RC1 | IMVP6_FET_RC1 - @m38_lib.M38 | 75A8 75C2 |
| IMVP6_FET_RC2 | IMVP6_FET_RC2 - @m38_lib.M38 | 75A6 75B2 |
| IMVP6_ISEN1 | IMVP6_ISEN1 - @m38_lib.M38 | 75A8 75C5 |
| IMVP6_ISEN2 | IMVP6_ISEN2 - @m38_lib.M38 | 75A6 75C5 |
| IMVP6_LGATE1 | IMVP6_LGATE1 - @m38_lib.M38 | 75A8 75C5 |
| IMVP6_LGATE2 | IMVP6_LGATE2 - @m38_lib.M38 | 75A6 75C5 |
| IMVP6_NTC | IMVP6_NTC - @m38_lib.M38 | 75C7 |
| IMVP6_NTC_R | IMVP6_NTC_R - @m38_lib.M38 | 75C7 |
| IMVP6_OCSET | IMVP6_OCSET - @m38_lib.M38 | 75A4 75B5 |
| IMVP6_PHASE1 | IMVP6_PHASE1 - @m38_lib.M38 | 75A8 75C5 |
| IMVP6_PHASE2 | IMVP6_PHASE2 - @m38_lib.M38 | 75A6 75C5 |
| IMVP6_RBIAS | IMVP6_RBIAS - @m38_lib.M38 | 75A4 75B6 |
| IMVP6_RTN | IMVP6_RTN - @m38_lib.M38 | 75A4 75B5 |
| IMVP6_SOFT | IMVP6_SOFT - @m38_lib.M38 | 75A4 75C6 |
| IMVP6_UGATE1 | IMVP6_UGATE1 - @m38_lib.M38 | 75A8 75C5 |
| IMVP6_UGATE2 | IMVP6_UGATE2 - @m38_lib.M38 | 75A6 75C5 |
| IMVP6_VDIFF | IMVP6_VDIFF - @m38_lib.M38 | 75A4 75B6 |
| IMVP6_VDIFF_RC | IMVP6_VDIFF_RC - @m38_lib.M38 | 75B7 |
| IMVP6_VO | IMVP6_VO - @m38_lib.M38 | 75A4 75B4 76D5 |
| IMVP6_VO_R | IMVP6_VO_R - @m38_lib.M38 | 75B4 |
| IMVP6_VO_R_OA | IMVP6_VO_R_OA - @m38_lib.M38 | 76D4 |
| IMVP6_VR_TT | IMVP6_VR_TT - @m38_lib.M38 | 75C7 |
| IMVP6_VSEN | IMVP6_VSEN - @m38_lib.M38 | 75A4 75B5 |
| IMVP6_VSUM | IMVP6_VSUM - @m38_lib.M38 | 75A4 75C5 |
| IMVP6_VSUM_R1 | IMVP6_VSUM_R1 - @m38_lib.M38 | 75A8 75C2 |
| IMVP6_VSUM_R2 | IMVP6_VSUM_R2 - @m38_lib.M38 | 75A6 75B2 |
| IMVP6_VW | IMVP6_VW - @m38_lib.M38 | 75A4 75B6 |
| IMVP_DPRSLPVR | IMVP_DPRSLPVR - @m38_lib.M38 | 75C6 |
| IMVP_PGD_IN | IMVP_PGD_IN - @m38_lib.M38 | 75C6 77C7 |
| IMVP_VID<0> | IMVP_VID<0> - @m38_lib.M38 | 75C6 |
| IMVP_VID<1> | IMVP_VID<1> - @m38_lib.M38 | 75C7 |
| IMVP_VID<2> | IMVP_VID<2> - @m38_lib.M38 | 75C7 |
| IMVP_VID<3> | IMVP_VID<3> - @m38_lib.M38 | 75C7 |
| IMVP_VID<4> | IMVP_VID<4> - @m38_lib.M38 | 75C7 |
| IMVP_VID<5> | IMVP_VID<5> - @m38_lib.M38 | 75C7 |
| IMVP_VID<6> | IMVP_VID<6> - @m38_lib.M38 | 75C7 |
| IMVP_VR_ON | IMVP_VR_ON - @m38_lib.M38 | 58D7 75C6 |
| INT_PIRQA_L | INT_PIRQA_L - @m38_lib.M38 | 22A7 26D2 |
| INT_PIRQB_L | INT_PIRQB_L - @m38_lib.M38 | 22A7 26D2 |
| INT_PIRQC_L | INT_PIRQC_L - @m38_lib.M38 | 22A7 26D2 |
| INT_PIRQD_L | INT_PIRQD_L - @m38_lib.M38 | 22A7 26D2 44B5 |
| INT_SERIRQ | INT_SERIRQ - @m38_lib.M38 | 23C8 58C7 60C3 67C6 |
| ISENSE_CAL_EN | ISENSE_CAL_EN - @m38_lib.M38 | 58B7 76A8 |
| ISENSE_CAL_EN_L | ISENSE_CAL_EN_L - @m38_lib.M38 | 76B7 |
| ISENSE_CAL_EN_LS12V | ISENSE_CAL_EN_LS12V - @m38_lib.M38 | 76B6 |
| ISENSE_CAL_EN_L_R | ISENSE_CAL_EN_L_R - @m38_lib.M38 | 76A7 |
| ITPRESET_L | ITPRESET_L - @m38_lib.M38 | 11B3 |
| ITP_TDO | ITP_TDO - @m38_lib.M38 | 11B3 |
| ITS_ALIVE | ITS_ALIVE - @m38_lib.M38 | 6A7 |
| ITS_PLUGGED_IN | ITS_PLUGGED_IN - @m38_lib.M38 | 6A8 |
| ITS_RUNNING | ITS_RUNNING - @m38_lib.M38 | 6A6 |
| JDREF | JDREF - @m38_lib.M38 | 68C4 |
| KBC_MDE | KBC_MDE - @m38_lib.M38 | 58C2 |
| LCD_PWM | LCD_PWM - @m38_lib.M38 | 94B1 94C3 |
| LCD_PWM_R | LCD_PWM_R - @m38_lib.M38 | 94B2 |
| LCD_PWREN_L | LCD_PWREN_L - @m38_lib.M38 | 94C8 |
| LCD_PWREN_L_RC | LCD_PWREN_L_RC - @m38_lib.M38 | 94C7 |
| LED4300_1 | LED4300_1 - @m38_lib.M38 | 43D2 |
| LED4301_1 | LED4301_1 - @m38_lib.M38 | 43D1 |
| LED4302_1 | LED4302_1 - @m38_lib.M38 | 43D1 |
| LED4303_1 | LED4303_1 - @m38_lib.M38 | 43D1 |
| LED_PP1V05_S0_N | LED_PP1V05_S0_N - @m38_lib.M38 | 81A4 |
| LED_PP1V05_S0_P | LED_PP1V05_S0_P - @m38_lib.M38 | 81A4 |
| LED_PP1V5_S0_N | LED_PP1V5_S0_N - @m38_lib.M38 | 80A3 |
| LED_PP1V5_S0_P | LED_PP1V5_S0_P - @m38_lib.M38 | 80A4 |
| LED_PP1V8_S3_N | LED_PP1V8_S3_N - @m38_lib.M38 | 79A4 |
| LED_PP1V8_S3_P | LED_PP1V8_S3_P - @m38_lib.M38 | 79A4 |
| LPC_AD<0> | LPC_AD<0> - @m38_lib.M38 | 21D4 58D7 60C6 67C6 |
| LPC_AD<1> | LPC_AD<1> - @m38_lib.M38 | 21D4 58D7 60C6 67C6 |
| LPC_AD<2> | LPC_AD<2> - @m38_lib.M38 | 21D4 58C7 60C3 67C6 |
| LPC_AD<3> | LPC_AD<3> - @m38_lib.M38 | 21D4 58C7 60C3 67C6 |
| LPC_FRAME_L | LPC_FRAME_L - @m38_lib.M38 | 21C5 58C7 60C6 67C6 |
| LVDS_A_CLK_N | LVDS_A_CLK_N - @m38_lib.M38 | 13D5 19D2 |
| | TP_LVDS_A_CLK_N - @m38_lib.M38 | 19D1 |
| LVDS_A_CLK_P | LVDS_A_CLK_P - @m38_lib.M38 | 13C5 19D2 |
| | TP_LVDS_A_CLK_P - @m38_lib.M38 | 19D1 |
| LVDS_A_DATA_N<0> | LVDS_A_DATA_N<0> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_A_DATA_N<1> | LVDS_A_DATA_N<1> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_A_DATA_N<2> | LVDS_A_DATA_N<2> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_A_DATA_P<0> | LVDS_A_DATA_P<0> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_A_DATA_P<1> | LVDS_A_DATA_P<1> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_A_DATA_P<2> | LVDS_A_DATA_P<2> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_BKLTCTL | LVDS_BKLTCTL - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_BKLTCTL - @m38_lib.M38 | 19C1 |
| LVDS_BKLTEN | LVDS_BKLTEN - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_BKLTEN - @m38_lib.M38 | 19C1 |
| LVDS_B_CLK_N | LVDS_B_CLK_N - @m38_lib.M38 | 13C5 19D2 |
| | TP_LVDS_B_CLK_N - @m38_lib.M38 | 19D1 |
| LVDS_B_CLK_P | LVDS_B_CLK_P - @m38_lib.M38 | 13C5 19D2 |
| | TP_LVDS_B_CLK_P - @m38_lib.M38 | 19D1 |
| LVDS_B_DATA_N<0> | LVDS_B_DATA_N<0> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_B_DATA_N<1> | LVDS_B_DATA_N<1> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_B_DATA_N<2> | LVDS_B_DATA_N<2> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_B_DATA_P<0> | LVDS_B_DATA_P<0> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_B_DATA_P<1> | LVDS_B_DATA_P<1> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_B_DATA_P<2> | LVDS_B_DATA_P<2> - @m38_lib.M38 | 13C5 19D2 |
| LVDS_CLKCTLA | LVDS_CLKCTLA - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_CLKCTLA - @m38_lib.M38 | 19C1 |
| LVDS_CLKCTLB | LVDS_CLKCTLB - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_CLKCTLB - @m38_lib.M38 | 19C1 |
| LVDS_DDC_CLK | LVDS_DDC_CLK - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_DDC_CLK - @m38_lib.M38 | 19C1 |
| LVDS_DDC_DATA | LVDS_DDC_DATA - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_DDC_DATA - @m38_lib.M38 | 19C1 |
| LVDS_IBG | LVDS_IBG - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_IBG - @m38_lib.M38 | 19C1 |
| LVDS_L_CLK_N | LVDS_L_CLK_N - @m38_lib.M38 | 93A3 93D2 94A6 |
| LVDS_L_CLK_P | LVDS_L_CLK_P - @m38_lib.M38 | 93A3 93D2 94A7 |
| LVDS_L_DATA_N<0> | LVDS_L_DATA_N<0> - @m38_lib.M38 | 93A3 93C2 94A6 |
| LVDS_L_DATA_N<1> | LVDS_L_DATA_N<1> - @m38_lib.M38 | 93A3 93C2 94A7 |
| LVDS_L_DATA_N<2> | LVDS_L_DATA_N<2> - @m38_lib.M38 | 93A3 93C2 94A6 |
| LVDS_L_DATA_N<3> | LVDS_L_DATA_N<3> - @m38_lib.M38 | 93A3 94A6 |
| LVDS_L_DATA_P<0> | LVDS_L_DATA_P<0> - @m38_lib.M38 | 93A3 93D2 94A7 |
| LVDS_L_DATA_P<1> | LVDS_L_DATA_P<1> - @m38_lib.M38 | 93A3 93C2 94A6 |
| LVDS_L_DATA_P<2> | LVDS_L_DATA_P<2> - @m38_lib.M38 | 93A3 93C2 94A7 |
| LVDS_L_DATA_P<3> | LVDS_L_DATA_P<3> - @m38_lib.M38 | 93A3 94A7 |
| LVDS_U_CLK_N | LVDS_U_CLK_N - @m38_lib.M38 | 93B3 94B6 |
| LVDS_U_CLK_P | LVDS_U_CLK_P - @m38_lib.M38 | 93B3 94A7 |
| LVDS_U_DATA_N<0> | LVDS_U_DATA_N<0> - @m38_lib.M38 | 93B3 94B7 |
| LVDS_U_DATA_N<1> | LVDS_U_DATA_N<1> - @m38_lib.M38 | 93B3 94B7 |
| LVDS_U_DATA_N<2> | LVDS_U_DATA_N<2> - @m38_lib.M38 | 93B3 94B7 |
| LVDS_U_DATA_N<3> | LVDS_U_DATA_N<3> - @m38_lib.M38 | 93B3 94A6 |
| LVDS_U_DATA_P<0> | LVDS_U_DATA_P<0> - @m38_lib.M38 | 93B3 94B6 |
| LVDS_U_DATA_P<1> | LVDS_U_DATA_P<1> - @m38_lib.M38 | 93B3 94B6 |
| LVDS_U_DATA_P<2> | LVDS_U_DATA_P<2> - @m38_lib.M38 | 93B3 94B6 |
| LVDS_U_DATA_P<3> | LVDS_U_DATA_P<3> - @m38_lib.M38 | 93B3 94A7 |
| LVDS_VDDEN | LVDS_VDDEN - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_VDDEN - @m38_lib.M38 | 19C1 |
| LVDS_VREFH | LVDS_VREFH - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_VREFH - @m38_lib.M38 | 19C1 |
| LVDS_VREFL | LVDS_VREFL - @m38_lib.M38 | 13D5 19C2 |
| | TP_LVDS_VREFL - @m38_lib.M38 | 19C1 |
| MEMVTT_VREF | MEMVTT_VREF - @m38_lib.M38 | 31B4 |
| MEM_A_A<0> | MEM_A_A<0> - @m38_lib.M38 | 15C5 28B3 |
| MEM_A_A<13..0> | MEM_A_A<13..0> - @m38_lib.M38 | 30C6 |
| MEM_A_A<1> | MEM_A_A<1> - @m38_lib.M38 | 15C5 28B6 |
| MEM_A_A<2> | MEM_A_A<2> - @m38_lib.M38 | 15C5 28B3 |
| MEM_A_A<3> | MEM_A_A<3> - @m38_lib.M38 | 15B5 28B6 |
| MEM_A_A<4> | MEM_A_A<4> - @m38_lib.M38 | 15B5 28B3 |
| MEM_A_A<5> | MEM_A_A<5> - @m38_lib.M38 | 15B5 28B6 |
| MEM_A_A<6> | MEM_A_A<6> - @m38_lib.M38 | 15B5 28C3 |
| MEM_A_A<7> | MEM_A_A<7> - @m38_lib.M38 | 15B5 28C3 |
| MEM_A_A<8> | MEM_A_A<8> - @m38_lib.M38 | 15B5 28C6 |
| MEM_A_A<9> | MEM_A_A<9> - @m38_lib.M38 | 15B5 28C6 |
| MEM_A_A<10> | MEM_A_A<10> - @m38_lib.M38 | 15B5 28B6 |
| MEM_A_A<11> | MEM_A_A<11> - @m38_lib.M38 | 15B5 28C3 |
| MEM_A_A<12> | MEM_A_A<12> - @m38_lib.M38 | 15B5 28C6 |
| MEM_A_A<13> | MEM_A_A<13> - @m38_lib.M38 | 15B5 28B3 |
| MEM_A_BS<0> | MEM_A_BS<0> - @m38_lib.M38 | 15D5 28B6 |
| MEM_A_BS<2..0> | MEM_A_BS<2..0> - @m38_lib.M38 | 30C6 |
| MEM_A_BS<1> | MEM_A_BS<1> - @m38_lib.M38 | 15D5 28B3 |
| MEM_A_BS<2> | MEM_A_BS<2> - @m38_lib.M38 | 15D5 28C6 |
| MEM_A_CAS_L | MEM_A_CAS_L - @m38_lib.M38 | 15D5 28B6 30B6 |
| MEM_A_DM<0> | MEM_A_DM<0> - @m38_lib.M38 | 15D5 28D3 |
| MEM_A_DM<1> | MEM_A_DM<1> - @m38_lib.M38 | 15D5 28D3 |
| MEM_A_DM<2> | MEM_A_DM<2> - @m38_lib.M38 | 15D5 28C3 |
| MEM_A_DM<3> | MEM_A_DM<3> - @m38_lib.M38 | 15C5 28C6 |
| MEM_A_DM<4> | MEM_A_DM<4> - @m38_lib.M38 | 15C5 28B3 |
| MEM_A_DM<5> | MEM_A_DM<5> - @m38_lib.M38 | 15C5 28A6 |
| MEM_A_DM<6> | MEM_A_DM<6> - @m38_lib.M38 | 15C5 28A3 |
| MEM_A_DM<7> | MEM_A_DM<7> - @m38_lib.M38 | 15C5 28A6 |
| MEM_A_DQ<0> | MEM_A_DQ<0> - @m38_lib.M38 | 15D7 28D6 |
| MEM_A_DQ<1> | MEM_A_DQ<1> - @m38_lib.M38 | 15D7 28D6 |
| MEM_A_DQ<2> | MEM_A_DQ<2> - @m38_lib.M38 | 15D7 28D6 |
| MEM_A_DQ<3> | MEM_A_DQ<3> - @m38_lib.M38 | 15D7 28D6 |
| MEM_A_DQ<4> | MEM_A_DQ<4> - @m38_lib.M38 | 15D7 28D3 |
| MEM_A_DQ<5> | MEM_A_DQ<5> - @m38_lib.M38 | 15D7 28D3 |
| MEM_A_DQ<6> | MEM_A_DQ<6> - @m38_lib.M38 | 15D7 28D3 |
| MEM_A_DQ<7> | MEM_A_DQ<7> - @m38_lib.M38 | 5B7 15D7 28D3 |
| MEM_A_DQ<8> | MEM_A_DQ<8> - @m38_lib.M38 | 15C7 28D6 |
| MEM_A_DQ<9> | MEM_A_DQ<9> - @m38_lib.M38 | 15C7 28D6 |
| MEM_A_DQ<10> | MEM_A_DQ<10> - @m38_lib.M38 | 15C7 28D6 |
| MEM_A_DQ<11> | MEM_A_DQ<11> - @m38_lib.M38 | 15C7 28D6 |
| MEM_A_DQ<12> | MEM_A_DQ<12> - @m38_lib.M38 | 15C7 28D3 |
| MEM_A_DQ<13> | MEM_A_DQ<13> - @m38_lib.M38 | 15C7 28D3 |
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| MEM_A_DQ<25> | MEM_A_DQ<25> - @m38_lib.M38 | 5B7 15C7 28C6 |
| MEM_A_DQ<26> | MEM_A_DQ<26> - @m38_lib.M38 | 15C7 28C6 |
| MEM_A_DQ<27> | MEM_A_DQ<27> - @m38_lib.M38 | 15C7 28C6 |
| MEM_A_DQ<28> | MEM_A_DQ<28> - @m38_lib.M38 | 15C7 28C3 |
| MEM_A_DQ<29> | MEM_A_DQ<29> - @m38_lib.M38 | 15C7 28C3 |
| MEM_A_DQ<30> | MEM_A_DQ<30> - @m38_lib.M38 | 15C7 28C3 |
| MEM_A_DQ<31> | MEM_A_DQ<31> - @m38_lib.M38 | 15C7 28C3 |
| MEM_A_DQ<32> | MEM_A_DQ<32> - @m38_lib.M38 | 15C7 28B6 |
| MEM_A_DQ<33> | MEM_A_DQ<33> - @m38_lib.M38 | 15C7 28B6 |
| MEM_A_DQ<34> | MEM_A_DQ<34> - @m38_lib.M38 | 15B7 28B6 |
| MEM_A_DQ<35> | MEM_A_DQ<35> - @m38_lib.M38 | 15B7 28B6 |
| MEM_A_DQ<36> | MEM_A_DQ<36> - @m38_lib.M38 | 15B7 28B3 |
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| MEM_A_DQ<39> | MEM_A_DQ<39> - @m38_lib.M38 | 5B7 15B7 28B3 |
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| MEM_A_DQ<47> | MEM_A_DQ<47> - @m38_lib.M38 | 5B7 15B7 28A3 |
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| MEM_A_DQ<54> | MEM_A_DQ<54> - @m38_lib.M38 | 5B7 15B7 28A3 |
| MEM_A_DQ<55> | MEM_A_DQ<55> - @m38_lib.M38 | 15B7 28A3 |
| MEM_A_DQ<56> | MEM_A_DQ<56> - @m38_lib.M38 | 15B7 28A6 |
| MEM_A_DQ<57> | MEM_A_DQ<57> - @m38_lib.M38 | 15B7 28A6 |
| MEM_A_DQ<58> | MEM_A_DQ<58> - @m38_lib.M38 | 15B7 28A6 |
| MEM_A_DQ<59> | MEM_A_DQ<59> - @m38_lib.M38 | 5B7 15B7 28A6 |
| MEM_A_DQ<60> | MEM_A_DQ<60> - @m38_lib.M38 | 15A7 28A3 |
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| MEM_A_DQ<62> | MEM_A_DQ<62> - @m38_lib.M38 | 15A7 28A3 |
| MEM_A_DQ<63> | MEM_A_DQ<63> - @m38_lib.M38 | 15A7 28A3 |
| MEM_A_DQS_N<0> | MEM_A_DQS_N<0> - @m38_lib.M38 | 5B7 15C5 28D6 |
| MEM_A_DQS_N<1> | MEM_A_DQS_N<1> - @m38_lib.M38 | 5B7 15C5 28D6 |
| MEM_A_DQS_N<2> | MEM_A_DQS_N<2> - @m38_lib.M38 | 5B7 15C5 28C6 |
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| MEM_A_DQS_N<4> | MEM_A_DQS_N<4> - @m38_lib.M38 | 5B7 15C5 28B6 |
| MEM_A_DQS_N<5> | MEM_A_DQS_N<5> - @m38_lib.M38 | 5B7 15C5 28B3 |
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| MEM_A_DQS_P<1> | MEM_A_DQS_P<1> - @m38_lib.M38 | 5B7 15C5 28D6 |
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| MEM_A_DQS_P<3> | MEM_A_DQS_P<3> - @m38_lib.M38 | 5B7 15C5 28C3 |
| MEM_A_DQS_P<4> | MEM_A_DQS_P<4> - @m38_lib.M38 | 5B7 15C5 28B6 |
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| MEM_A_DQS_P<7> | MEM_A_DQS_P<7> - @m38_lib.M38 | 5B7 15C5 28A3 |
| MEM_A_RAS_L | MEM_A_RAS_L - @m38_lib.M38 | 15B5 28B3 30B6 |
| MEM_A_WE_L | MEM_A_WE_L - @m38_lib.M38 | 15B5 28B6 30B6 |
| MEM_B_A<0> | MEM_B_A<0> - @m38_lib.M38 | 15C2 29B3 30B5 |
| MEM_B_A<1> | MEM_B_A<1> - @m38_lib.M38 | 15C2 29B6 30B5 |
| MEM_B_A<2> | MEM_B_A<2> - @m38_lib.M38 | 15C2 29B3 30B5 |
| MEM_B_A<3> | MEM_B_A<3> - @m38_lib.M38 | 15B2 29B6 30B5 |
| MEM_B_A<4> | MEM_B_A<4> - @m38_lib.M38 | 15B2 29B3 30B5 |
| MEM_B_A<5> | MEM_B_A<5> - @m38_lib.M38 | 15B2 29B6 30B5 |
| MEM_B_A<6> | MEM_B_A<6> - @m38_lib.M38 | 15B2 29C3 30B5 |
| MEM_B_A<7> | MEM_B_A<7> - @m38_lib.M38 | 15B2 29C3 30B5 |
| MEM_B_A<8> | MEM_B_A<8> - @m38_lib.M38 | 15B2 29C6 30B5 |
| MEM_B_A<9> | MEM_B_A<9> - @m38_lib.M38 | 15B2 29C6 30B5 |
| MEM_B_A<10> | MEM_B_A<10> - @m38_lib.M38 | 15B2 29B6 30B5 |
| MEM_B_A<11> | MEM_B_A<11> - @m38_lib.M38 | 15B2 29C3 30A5 |
| MEM_B_A<12> | MEM_B_A<12> - @m38_lib.M38 | 15B2 29C6 30A5 |
| MEM_B_A<13> | MEM_B_A<13> - @m38_lib.M38 | 15B2 29B3 30A5 |
| MEM_B_BS<0> | MEM_B_BS<0> - @m38_lib.M38 | 15D2 29B6 |
| MEM_B_BS<2..0> | MEM_B_BS<2..0> - @m38_lib.M38 | 30A6 |
| MEM_B_BS<1> | MEM_B_BS<1> - @m38_lib.M38 | 15D2 29B3 |
| MEM_B_BS<2> | MEM_B_BS<2> - @m38_lib.M38 | 15D2 29C6 |
| MEM_B_CAS_L | MEM_B_CAS_L - @m38_lib.M38 | 15D2 29B6 30A6 |
| MEM_B_DM<0> | MEM_B_DM<0> - @m38_lib.M38 | 15D2 29D3 |
| MEM_B_DM<1> | MEM_B_DM<1> - @m38_lib.M38 | 15D2 29D3 |
| MEM_B_DM<2> | MEM_B_DM<2> - @m38_lib.M38 | 15D2 29C3 |
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| MEM_B_DM<7> | MEM_B_DM<7> - @m38_lib.M38 | 15C2 29A6 |
| MEM_B_DQ<0> | MEM_B_DQ<0> - @m38_lib.M38 | 15D4 29D6 |
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| MEM_B_DQ<3> | MEM_B_DQ<3> - @m38_lib.M38 | 15D4 29D6 |
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| MEM_B_DQ<6> | MEM_B_DQ<6> - @m38_lib.M38 | 5A7 15D4 29D3 |
| MEM_B_DQ<7> | MEM_B_DQ<7> - @m38_lib.M38 | 15D4 29D3 |
| MEM_B_DQ<8> | MEM_B_DQ<8> - @m38_lib.M38 | 5A7 15C4 29D6 |
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| MEM_B_RAS_L | MEM_B_RAS_L - @m38_lib.M38 | 15B2 29B3 30A6 |
| MEM_B_SPD_SA1 | MEM_B_SPD_SA1 - @m38_lib.M38 | 29A4 |
| MEM_B_WE_L | MEM_B_WE_L - @m38_lib.M38 | 15B2 29B6 30A6 |
| MEM_CKE<0> | MEM_CKE<0> - @m38_lib.M38 | 14C4 28C6 |
| MEM_CKE<3..0> | MEM_CKE<3..0> - @m38_lib.M38 | 30D6 |
| MEM_CKE<1> | MEM_CKE<1> - @m38_lib.M38 | 14C4 28C3 |
| MEM_CKE<2> | MEM_CKE<2> - @m38_lib.M38 | 14C4 29C6 |
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| MEM_ODT<3> | MEM_ODT<3> - @m38_lib.M38 | 14C4 29B6 |
| MEM_RCOMP | MEM_RCOMP - @m38_lib.M38 | 14C4 |
| MEM_RCOMP_L | MEM_RCOMP_L - @m38_lib.M38 | 14C4 |
| MEM_VREF | MEM_VREF - @m38_lib.M38 | 5C4 28C7 28D6 29D6 |
| MEM_VREF_NB_0 | MEM_VREF_NB_0 - @m38_lib.M38 | 5B7 14C4 19B6 |
| MEM_VREF_NB_1 | MEM_VREF_NB_1 - @m38_lib.M38 | 5B7 14C4 19B7 |
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| NB_BSEL<1> | NB_BSEL<1> - @m38_lib.M38 | 14C6 34B8 |
| NB_BSEL<2> | NB_BSEL<2> - @m38_lib.M38 | 14C6 34A8 |
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| NB_CFG<4> | NB_CFG<4> - @m38_lib.M38 | 14C6 |
| NB_CFG<5> | NB_CFG<5> - @m38_lib.M38 | 14C6 20C7 |
| NB_CFG<6> | NB_CFG<6> - @m38_lib.M38 | 14C6 |
| NB_CFG<7> | NB_CFG<7> - @m38_lib.M38 | 14C6 20C7 |
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| NB_CFG<13> | NB_CFG<13> - @m38_lib.M38 | 14C6 |
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| NB_CFG<16> | NB_CFG<16> - @m38_lib.M38 | 14C6 20C5 |
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| NB_CFG<18> | NB_CFG<18> - @m38_lib.M38 | 14C6 20B5 |
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| NB_CLK100M_GCLKIN_N | NB_CLK100M_GCLKIN_N - @m38_lib.M38 | 5C7 14C4 34B4 34C2 |
| NB_CLK100M_GCLKIN_P | NB_CLK100M_GCLKIN_P - @m38_lib.M38 | 5C7 14C4 34B4 34C2 |
| NB_FSB_VREF | NB_FSB_VREF - @m38_lib.M38 | 5D4 12C4 |
| NB_FSB_XRCOMP | NB_FSB_XRCOMP - @m38_lib.M38 | 12A6 |
| NB_FSB_XSCOMP | NB_FSB_XSCOMP - @m38_lib.M38 | 12A6 |
| NB_FSB_XSWING | NB_FSB_XSWING - @m38_lib.M38 | 12A6 |
| NB_FSB_YRCOMP | NB_FSB_YRCOMP - @m38_lib.M38 | 12A6 |
| NB_FSB_YSCOMP | NB_FSB_YSCOMP - @m38_lib.M38 | 12A6 |
| NB_FSB_YSWING | NB_FSB_YSWING - @m38_lib.M38 | 12A6 |
| NB_RST_IN_L | NB_RST_IN_L - @m38_lib.M38 | 6B7 14B7 |
| NB_RST_IN_L_R | NB_RST_IN_L_R - @m38_lib.M38 | 5C7 14B6 |
| NB_SB_SYNC_L | NB_SB_SYNC_L - @m38_lib.M38 | 14B6 22A6 |
| NB_TV_DCONSEL0 | NB_TV_DCONSEL0 - @m38_lib.M38 | 14D6 |
| NB_TV_DCONSEL1 | NB_TV_DCONSEL1 - @m38_lib.M38 | 14C6 |
| NB_VCCSM_LF1 | NB_VCCSM_LF1 - @m38_lib.M38 | 16B4 |
| NB_VCCSM_LF2 | NB_VCCSM_LF2 - @m38_lib.M38 | 16B4 |
| NB_VCCSM_LF4 | NB_VCCSM_LF4 - @m38_lib.M38 | 16B8 |
| NB_VCCSM_LF5 | NB_VCCSM_LF5 - @m38_lib.M38 | 16B8 |
| NB_VTTLF_CAP1 | NB_VTTLF_CAP1 - @m38_lib.M38 | 17A4 |
| NB_VTTLF_CAP2 | NB_VTTLF_CAP2 - @m38_lib.M38 | 17A4 |
| NB_VTTLF_CAP3 | NB_VTTLF_CAP3 - @m38_lib.M38 | 17B4 |
| NC_AUD_VREF_PORT_A | NC_AUD_VREF_PORT_A - @m38_lib.M38 | 68C1 |
| NC_AUD_VREF_PORT_C | NC_AUD_VREF_PORT_C - @m38_lib.M38 | 68C1 |
| NC_AUD_VREF_PORT_D | NC_AUD_VREF_PORT_D - @m38_lib.M38 | 68C1 |
| NC_FW_NU1 | NC_FW_NU1 - @m38_lib.M38 | 44B3 |
| NC_FW_NU2 | NC_FW_NU2 - @m38_lib.M38 | 44B3 |
| NC_JE350_13 | NC_JE350_13 - @m38_lib.M38 | 47B1 |
| NC_SMC_P20 | NC_SMC_P20 - @m38_lib.M38 | 59D5 |
| | SMC_P20 - @m38_lib.M38 | 58D7 59D6 |
| NC_SMC_P21 | NC_SMC_P21 - @m38_lib.M38 | 59D5 |
| | SMC_P21 - @m38_lib.M38 | 58D7 59D6 |
| NC_SMC_P22 | NC_SMC_P22 - @m38_lib.M38 | 59D5 |
| | SMC_P22 - @m38_lib.M38 | 58D7 59D6 |
| NC_SMC_P23 | NC_SMC_P23 - @m38_lib.M38 | 59D5 |
| | SMC_P23 - @m38_lib.M38 | 58D7 59D6 |
| NC_SMC_P26 | NC_SMC_P26 - @m38_lib.M38 | 59D5 |
| | SMC_P26 - @m38_lib.M38 | 58D7 59D6 |
| NC_SMC_P27 | NC_SMC_P27 - @m38_lib.M38 | 59D5 |
| | SMC_P27 - @m38_lib.M38 | 58D7 59D6 |
| ODD_PWR_EN_L | ODD_PWR_EN_L - @m38_lib.M38 | 22A6 26C3 |
| | SB_GPIO5 - @m38_lib.M38 | 26C2 |
| P0V48_SMC_LSREF | P0V48_SMC_LSREF - @m38_lib.M38 | 59A6 59A8 59B7 |
| PANEL_ID | PANEL_ID - @m38_lib.M38 | 94C2 94C3 |
| PATA_PWR_EN_L | PATA_PWR_EN_L - @m38_lib.M38 | 23B3 23C3 |
| PCIE_A_D2R_C_N | PCIE_A_D2R_C_N - @m38_lib.M38 | 41D5 |
| PCIE_A_D2R_C_P | PCIE_A_D2R_C_P - @m38_lib.M38 | 41D5 |
| PCIE_A_D2R_N | PCIE_A_D2R_N - @m38_lib.M38 | 5B8 22D4 41D4 |
| PCIE_A_D2R_P | PCIE_A_D2R_P - @m38_lib.M38 | 5C8 22D4 41D4 |
| PCIE_A_R2D_C_N | PCIE_A_R2D_C_N - @m38_lib.M38 | 22D4 41C3 |
| PCIE_A_R2D_C_P | PCIE_A_R2D_C_P - @m38_lib.M38 | 22D4 41C3 |
| PCIE_A_R2D_N | PCIE_A_R2D_N - @m38_lib.M38 | 41C5 |
| PCIE_A_R2D_P | PCIE_A_R2D_P - @m38_lib.M38 | 41C5 |
| PCIE_B_D2R_N | PCIE_B_D2R_N - @m38_lib.M38 | 5B8 22D4 53C7 |
| PCIE_B_D2R_P | PCIE_B_D2R_P - @m38_lib.M38 | 5B8 22D4 53C7 |
| PCIE_B_R2D_C_N | PCIE_B_R2D_C_N - @m38_lib.M38 | 22D4 53B7 |
| PCIE_B_R2D_C_P | PCIE_B_R2D_C_P - @m38_lib.M38 | 22D4 53B7 |
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| | TP_PCIE_C_D2R_N - @m38_lib.M38 | 54D6 |
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| PCIE_C_R2D_C_N | PCIE_C_R2D_C_N - @m38_lib.M38 | 22D4 54D8 |
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| | TP_PCIE_C_R2D_C_P - @m38_lib.M38 | 54D6 |
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| | TP_PCIE_D_D2R_N - @m38_lib.M38 | 54C6 |
| PCIE_D_D2R_P | PCIE_D_D2R_P - @m38_lib.M38 | 22D4 54C8 |
| | TP_PCIE_D_D2R_P - @m38_lib.M38 | 54C6 |
| PCIE_D_R2D_C_N | PCIE_D_R2D_C_N - @m38_lib.M38 | 22D4 54D8 |
| | TP_PCIE_D_R2D_C_N - @m38_lib.M38 | 54D6 |
| PCIE_D_R2D_C_P | PCIE_D_R2D_C_P - @m38_lib.M38 | 22D4 54D8 |
| | TP_PCIE_D_R2D_C_P - @m38_lib.M38 | 54D6 |
| PCIE_E_D2R_N | PCIE_E_D2R_N - @m38_lib.M38 | 22C4 54C8 |
| | TP_PCIE_E_D2R_N - @m38_lib.M38 | 54C6 |
| PCIE_E_D2R_P | PCIE_E_D2R_P - @m38_lib.M38 | 22C4 54C8 |
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| PCIE_E_R2D_C_P | PCIE_E_R2D_C_P - @m38_lib.M38 | 22C4 54C8 |
| | TP_PCIE_E_R2D_C_P - @m38_lib.M38 | 54C6 |
| PCIE_F_D2R_N | PCIE_F_D2R_N - @m38_lib.M38 | 22C4 54B8 |
| | TP_PCIE_F_D2R_N - @m38_lib.M38 | 54B6 |
| PCIE_F_D2R_P | PCIE_F_D2R_P - @m38_lib.M38 | 22C4 54B8 |
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| PCIE_F_R2D_C_N | PCIE_F_R2D_C_N - @m38_lib.M38 | 22C4 54C8 |
| | TP_PCIE_F_R2D_C_N - @m38_lib.M38 | 54C6 |
| PCIE_F_R2D_C_P | PCIE_F_R2D_C_P - @m38_lib.M38 | 22C4 54B8 |
| | TP_PCIE_F_R2D_C_P - @m38_lib.M38 | 54B6 |
| PCIE_WAKE_L | PCIE_WAKE_L - @m38_lib.M38 | 23C8 41C4 53C7 |
| PCI_AD<0> | PCI_AD<0> - @m38_lib.M38 | 22B7 44D5 |

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| Signal | Alias | References |
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| PCI AD<2> | PCI AD<2> - @m38 lib.M38 | 22B7 44D5 |
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| PCI AD<29> | PCI AD<29> - @m38 lib.M38 | 22A7 44C5 |
| PCI AD<30> | PCI AD<30> - @m38 lib.M38 | 22A7 44C5 |
| PCI AD<31> | PCI AD<31> - @m38 lib.M38 | 22A7 44B5 |
| PCI CLK FW | PCI CLK FW - @m38 lib.M38 | 34C4 44B5 |
| PCI CLK PORT80 | PCI CLK PORT80 - @m38 lib.M38 | 34B4 60C3 |
| PCI CLK SB | PCI CLK SB - @m38 lib.M38 | 5C8 22A6 34B4 |
| PCI CLK SMC | PCI CLK SMC - @m38 lib.M38 | 34C4 58C7 |
| PCI CLK TPM | PCI CLK TPM - @m38 lib.M38 | 34C4 67C6 |
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| PCI C BE L<2> | PCI C BE L<2> - @m38 lib.M38 | 22B6 44B5 |
| PCI C BE L<3> | PCI C BE L<3> - @m38 lib.M38 | 22B6 44B5 |
| PCI DEVSEL L | PCI DEVSEL L - @m38 lib.M38 | 22A6 26D2 44B5 |
| PCI FRAME L | PCI FRAME L - @m38 lib.M38 | 22A7 26D2 44B5 |
| PCI GNT3 L | PCI GNT3 L - @m38 lib.M38 | 22B4 44B5 |
| PCI IDSEL | PCI IDSEL - @m38 lib.M38 | 44B5 |
| PCI IRDY L | PCI IRDY L - @m38 lib.M38 | 22A6 26D2 44B5 |
| PCI LOCK L | PCI LOCK L - @m38 lib.M38 | 22A6 26D2 |
| PCI PAR | PCI PAR - @m38 lib.M38 | 22A6 44B5 |
| PCI PERR L | PCI PERR L - @m38 lib.M38 | 22A6 26D2 44B5 |
| PCI PME FW L | PCI PME FW L - @m38 lib.M38 | 22B5 44B5 |
| PCI REQ0 L | PCI REQ0 L - @m38 lib.M38 | 22B6 26D2 |
| PCI REQ1 L | PCI REQ1 L - @m38 lib.M38 | 22B6 26D2 |
| PCI REQ2 L | PCI REQ2 L - @m38 lib.M38 | 22B6 26D2 |
| PCI REQ3 L | PCI REQ3 L - @m38 lib.M38 | 22B4 26D2 44B5 |
| PCI RST FW L | PCI RST FW L - @m38 lib.M38 | 44A7 44B5 |
| PCI RST L | PCI RST L - @m38 lib.M38 | 22A6 44A8 |
| PCI SERR L | PCI SERR L - @m38 lib.M38 | 22A6 26D2 44B5 |
| PCI STOP L | PCI STOP L - @m38 lib.M38 | 22A6 26D2 44B5 |
| PCI TRDY L | PCI TRDY L - @m38 lib.M38 | 22A6 26D2 44B5 |
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| PEG R2D P<14> | PEG R2D P<14> - @m38 lib.M38 | 84B4 |
| PEG R2D P<15> | PEG R2D P<15> - @m38 lib.M38 | 84B4 |
| PEG RESET L | PEG RESET L - @m38 lib.M38 | 6B7 84A5 |
| PLT RST L | PLT RST L - @m38 lib.M38 | 6C8 22A6 |
| PM BATLOW L | PM BATLOW L - @m38 lib.M38 | 23C1 58B7 |
| PM BMBUSY L | PM BMBUSY L - @m38 lib.M38 | 14B6 23C5 |
| PM CLKRUN L | PM CLKRUN L - @m38 lib.M38 | 5B8 23C8 44B5 58C5 60C6 67C6 |
| PM DPRSLPVR | PM DPRSLPVR - @m38 lib.M38 | 14B7 23C3 75C7 |
| PM EXTTS L<0> | PM EXTTS L<0> - @m38 lib.M38 | 14B7 58B7 59C5 |
| | DIMM OVERTEMP L - @m38 lib.M38 | 28C3 29C3 59C6 |
| PM LAN ENABLE | PM LAN ENABLE - @m38 lib.M38 | 23C3 58D7 |
| PM PWRBTN L | PM PWRBTN L - @m38 lib.M38 | 23C3 58D7 |
| PM PWROK | PM PWROK - @m38 lib.M38 | 77B7 |
| PM RI L | PM RI L - @m38 lib.M38 | 23D5 |
| PM RSMRST L | PM RSMRST L - @m38 lib.M38 | 23C1 58D7 |
| PM SB PWROK | PM SB PWROK - @m38 lib.M38 | 23C3 26D6 |
| PM SLP S3 | PM SLP S3 - @m38 lib.M38 | 77D7 78B8 80C8 81C8 |
| PM SLP S3 L | PM SLP S3 L - @m38 lib.M38 | 6C8 23C3 58C5 77D8 79A7 88A6 |
| | MEMVTT EN - @m38 lib.M38 | 31B5 79A6 |
| PM SLP S4 | PM SLP S4 - @m38 lib.M38 | 77A7 77C7 79C8 83C6 |
| PM SLP S4 L | PM SLP S4 L - @m38 lib.M38 | 23C3 58C5 77C8 |
| PM SLP S5 L | PM SLP S5 L - @m38 lib.M38 | 23C3 58C5 |
| PM STPCPU L | PM STPCPU L - @m38 lib.M38 | 23C8 33C4 |
| PM STPPCI L | PM STPPCI L - @m38 lib.M38 | 23C8 33C4 |
| PM SUS STAT L | PM SUS STAT L - @m38 lib.M38 | 23C5 58C5 60C3 67C6 |
| PM SYSRST L | PM SYSRST L - @m38 lib.M38 | 5B8 23C5 26C3 58B7 |
| PM THRMTRIP L | PM THRMTRIP L - @m38 lib.M38 | 7C6 14B6 21C2 59C7 |
| PM THRM L | PM THRM L - @m38 lib.M38 | 10D3 23C8 58B7 |
| POWER BUTTON L | POWER BUTTON L - @m38 lib.M38 | 5D1 59C8 |
| PP0V9 S0 PGOOD | PP0V9 S0 PGOOD - @m38 lib.M38 | 77B8 79A2 |
| PP1V2 S0 GPU VDDPLL | PP1V2 S0 GPU VDDPLL - @m38 lib.M38 | 91B6 |
| PP1V05 S0 PGOOD | PP1V05 S0 PGOOD - @m38 lib.M38 | 77C8 81B2 |
| PP1V5 S0 NB 3GPLL F | PP1V5 S0 NB 3GPLL F - @m38 lib.M38 | 19A5 |
| PP1V5 S0 NB VCC3G | PP1V5 S0 NB VCC3G - @m38 lib.M38 | 17D6 19A3 |
| PP1V5 S0 NB VCCA 3GPLL | PP1V5 S0 NB VCCA 3GPLL - @m38 lib.M38 | 17D6 19A3 |
| PP1V5 S0 NB VCCA DPLLA | PP1V5 S0 NB VCCA DPLLA - @m38 lib.M38 | 17C6 19C4 |
| | TP NB VCCA DPLLA - @m38 lib.M38 | 19C5 |
| PP1V5 S0 NB VCCA DPLLB | PP1V5 S0 NB VCCA DPLLB - @m38 lib.M38 | 17C6 19C4 |
| | TP NB VCCA DPLLB - @m38 lib.M38 | 19C5 |
| PP1V5 S0 NB VCCA HPLL | PP1V5 S0 NB VCCA HPLL - @m38 lib.M38 | 17C6 19C6 |
| PP1V5 S0 NB VCCA MPLL | PP1V5 S0 NB VCCA MPLL - @m38 lib.M38 | 17C6 19C6 |
| PP1V5 S0 NB VCCD QTVDAC | PP1V5 S0 NB VCCD QTVDAC - @m38 lib.M38 | 17B6 19A1 |
| | PP1V5 S0 NB VCCD TVDAC - @m38 lib.M38 | 17C6 19B1 |
| PP1V5 S0 PGOOD | PP1V5 S0 PGOOD - @m38 lib.M38 | 77C8 80B2 |
| PP1V5 S0 SB R | PP1V5 S0 SB R - @m38 lib.M38 | 25A7 |
| PP1V5 S0 SB VCC1 5 B | PP1V5 S0 SB VCC1 5 B - @m38 lib.M38 | 22C1 24D5 25B7 |
| PP1V5 S0 SB VCCDMIPLL | PP1V5 S0 SB VCCDMIPLL - @m38 lib.M38 | 24B5 25A6 |
| PP1V8R2V0 S0 GPU VDDRH0 | PP1V8R2V0 S0 GPU VDDRH0 - @m38 lib.M38 | 87A7 |
| PP1V8R2V0 S0 GPU VDDRH1 | PP1V8R2V0 S0 GPU VDDRH1 - @m38 lib.M38 | 87B3 |
| PP1V8R3V3 S0 GPU VDDR4 F | PP1V8R3V3 S0 GPU VDDR4 F - @m38 lib.M38 | 91B6 |
| PP1V8R3V3 S0 GPU VDDR5 F | PP1V8R3V3 S0 GPU VDDR5 F - @m38 lib.M38 | 91B6 |
| PP1V8 FB A0 VDDA0 | PP1V8 FB A0 VDDA0 - @m38 lib.M38 | 89D7 |
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| PP1V8 S3 PGOOD | PP1V8 S3 PGOOD - @m38 lib.M38 | 79B2 |
| PP2V5 S0 PGOOD | PP2V5 S0 PGOOD - @m38 lib.M38 | 77B8 77C5 |
| PP2V5 ENET CTAP | PP2V5 ENET CTAP - @m38 lib.M38 | 43D7 |
| PP2V5 S0 GPU A2VDD | PP2V5 S0 GPU A2VDD - @m38 lib.M38 | 93B7 |
| PP2V5 S0 GPU AVDD | PP2V5 S0 GPU AVDD - @m38 lib.M38 | 93C7 |
| PP2V5 S0 GPU LPVDD | PP2V5 S0 GPU LPVDD - @m38 lib.M38 | 93B7 |
| PP2V5 S0 GPU LVDDR | PP2V5 S0 GPU LVDDR - @m38 lib.M38 | 93B7 |
| PP2V5 S0 GPU PVDD F | PP2V5 S0 GPU PVDD F - @m38 lib.M38 | 91A6 |
| PP2V5 S0 GPU TPVDD | PP2V5 S0 GPU TPVDD - @m38 lib.M38 | 93C7 |
| PP2V5 S0 GPU TXVDDR | PP2V5 S0 GPU TXVDDR - @m38 lib.M38 | 93C7 |
| PP2V5 S0 GPU VDD1DI | PP2V5 S0 GPU VDD1DI - @m38 lib.M38 | 93C8 |
| PP2V5 S0 GPU VDD2DI | PP2V5 S0 GPU VDD2DI - @m38 lib.M38 | 93B8 |
| PP3V3R12V LCD CONN | PP3V3R12V LCD CONN - @m38 lib.M38 | 94A6 94A6 94A7 94C5 |
| PP3V3 AUDIO SPDIF EMI | PP3V3 AUDIO SPDIF EMI - @m38 lib.M38 | 73B7 |
| PP3V3 AUDIO SPDIF JACK | PP3V3 AUDIO SPDIF JACK - @m38 lib.M38 | 73B5 |
| PP3V3 AVCC SMC | PP3V3 AVCC SMC - @m38 lib.M38 | 58D3 |
| PP3V3 AVREF SMC | PP3V3 AVREF SMC - @m38 lib.M38 | 58D2 59A3 |
| PP3V3 FW ESD | PP3V3 FW ESD - @m38 lib.M38 | 46A5 46A6 46B5 46C5 46D5 |
| PP3V3 FW ESD F | PP3V3 FW ESD F - @m38 lib.M38 | 46A7 |
| PP3V3 INTERCON | PP3V3 INTERCON - @m38 lib.M38 | 72C7 |
| PP3V3 LCD SW | PP3V3 LCD SW - @m38 lib.M38 | 94C6 |
| PP3V3 S0 CK410 VDD48 | PP3V3 S0 CK410 VDD48 - @m38 lib.M38 | 33D5 |
| PP3V3 S0 CK410 VDDA | PP3V3 S0 CK410 VDDA - @m38 lib.M38 | 33C6 |
| PP3V3 S0 CK410 VDD CPU SRC | PP3V3 S0 CK410 VDD CPU SRC - @m38 lib.M38 | 33D6 |
| PP3V3 S0 CK410 VDD PCI | PP3V3 S0 CK410 VDD PCI - @m38 lib.M38 | 33D5 |
| PP3V3 S0 CK410 VDD REF | PP3V3 S0 CK410 VDD REF - @m38 lib.M38 | 33C5 |
| PP3V3 S0 IMVP6 3V3 | PP3V3 S0 IMVP6 3V3 - @m38 lib.M38 | 75D6 |
| PP3V3 S5 FW VDDA | PP3V3 S5 FW VDDA - @m38 lib.M38 | 44D5 45C6 |
| PP3V3 S5 SB RTC | PP3V3 S5 SB RTC - @m38 lib.M38 | 5D2 21D6 24B3 25A3 26D7 |
| PP3V3 SO 2V5REG R | PP3V3 SO 2V5REG R - @m38 lib.M38 | 77D5 |
| PP3V3 TPM 3VSB | PP3V3 TPM 3VSB - @m38 lib.M38 | 59C5 67C4 |
| PP4V5 AUDIO ANALOG | PP4V5 AUDIO ANALOG - @m38 lib.M38 | 68A2 68D2 74D8 |
| PP5V BNDI LE340 | PP5V BNDI LE340 - @m38 lib.M38 | 47D3 |
| PP5V S0 DDC | PP5V S0 DDC - @m38 lib.M38 | 97D4 |
| PP5V S0 DDC FUSE | PP5V S0 DDC FUSE - @m38 lib.M38 | 97D5 |
| PP5V S0 GPUVCORE VCC | PP5V S0 GPUVCORE VCC - @m38 lib.M38 | 85D7 88D8 |
| PP5V S0 IMVP6 VDD | PP5V S0 IMVP6 VDD - @m38 lib.M38 | 75D6 |
| PP5V S0 SB V5REF | PP5V S0 SB V5REF - @m38 lib.M38 | 24D5 25D7 |
| PP5V S3 BNDI | PP5V S3 BNDI - @m38 lib.M38 | 47B2 47D1 |
| PP5V S5 SB V5REF SUS | PP5V S5 SB V5REF SUS - @m38 lib.M38 | 24D5 25C7 |
| PP5V USB2 | PP5V USB2 - @m38 lib.M38 | 47D7 |
| PP5V USB2 PORT0 F | PP5V USB2 PORT0 F - @m38 lib.M38 | 47D5 |
| PP5V USB2 PORT1 F | PP5V USB2 PORT1 F - @m38 lib.M38 | 47C5 |
| PP5V USB2 PORT2 F | PP5V USB2 PORT2 F - @m38 lib.M38 | 47B5 |
| PP12V AUD SPKRAMP PLANE | PP12V AUD SPKRAMP PLANE - @m38 lib.M38 | 72D5 |
| PP12V L7502 | PP12V L7502 - @m38 lib.M38 | 76D7 |
| PP12V S5 CPU REG | PP12V S5 CPU REG - @m38 lib.M38 | 75C3 75D4 75D7 76C8 76D6 |
| PPFW PORT0 VP | PPFW PORT0 VP - @m38 lib.M38 | 46D2 |
| PPFW PORT0 VP FL | PPFW PORT0 VP FL - @m38 lib.M38 | 46C2 |
| PPFW PORT1 VP | PPFW PORT1 VP - @m38 lib.M38 | 46B2 46D1 |
| PPFW PORT1 VP FL | PPFW PORT1 VP FL - @m38 lib.M38 | 46B2 |
| PPFW PORTS VP | PPFW PORTS VP - @m38 lib.M38 | 46D3 |
| PPVCORE S0 GPU MPVDD | PPVCORE S0 GPU MPVDD - @m38 lib.M38 | 91A6 |
| PPVCORE S0 GPU VDDCI | PPVCORE S0 GPU VDDCI - @m38 lib.M38 | 86C7 |
| PPVIN S5 IMVP6 VIN | PPVIN S5 IMVP6 VIN - @m38 lib.M38 | 75D6 |
| PPV 3V3 AUDIO CODEC | PPV 3V3 AUDIO CODEC - @m38 lib.M38 | 68D6 |
| Q4201 3 | Q4201 3 - @m38 lib.M38 | 42D6 |
| R7504 1 | R7504 1 - @m38 lib.M38 | 75A8 75C1 |
| R7507 1 | R7507 1 - @m38 lib.M38 | 75A6 75B1 |
| R8599 2 | R8599 2 - @m38 lib.M38 | 85C4 |
| RSMRST PWRGD | RSMRST PWRGD - @m38 lib.M38 | 58D7 76D1 |
| SATA A D2R N | SATA A D2R N - @m38 lib.M38 | 21B6 38A6 |
| | TP SATA A D2R N - @m38 lib.M38 | 38A5 |
| SATA A D2R P | SATA A D2R P - @m38 lib.M38 | 21B6 38A6 |
| | TP SATA A D2R P - @m38 lib.M38 | 38A5 |
| SATA A R2D C N | SATA A R2D C N - @m38 lib.M38 | 21B6 38A6 |
| | TP SATA A R2D N - @m38 lib.M38 | 38A5 |
| SATA A R2D C P | SATA A R2D C P - @m38 lib.M38 | 21B6 38A6 |
| | TP SATA A R2D P - @m38 lib.M38 | 38A5 |
| SATA C D2R C N | SATA C D2R C N - @m38 lib.M38 | 38B8 |
| SATA C D2R C P | SATA C D2R C P - @m38 lib.M38 | 38B8 |
| SATA C D2R N | SATA C D2R N - @m38 lib.M38 | 21B6 38B6 |
| SATA C D2R P | SATA C D2R P - @m38 lib.M38 | 21B6 38B6 |
| SATA C DET L | SATA C DET L - @m38 lib.M38 | 23D2 38B5 |
| SATA C PWR EN L | SATA C PWR EN L - @m38 lib.M38 | 23A3 23B3 |
| SATA C R2D C N | SATA C R2D C N - @m38 lib.M38 | 21B6 38B6 |
| SATA C R2D C P | SATA C R2D C P - @m38 lib.M38 | 21B6 38B6 |
| SATA C R2D N | SATA C R2D N - @m38 lib.M38 | 38B8 |
| SATA C R2D P | SATA C R2D P - @m38 lib.M38 | 38B8 |
| SATA RBIAS N | SATA RBIAS N - @m38 lib.M38 | 21B6 38C7 |
| | SATA RBIAS P - @m38 lib.M38 | 21B6 38C7 |
| | SATA RBIAS - @m38 lib.M38 | 38C7 |
| | SATA RBIAS P - @m38 lib.M38 | 21B6 38C7 |
| | SATA RBIAS - @m38 lib.M38 | 38C7 |
| SB A20GATE | SB A20GATE - @m38 lib.M38 | 21C4 |
| SB ACZ BITCLK | SB ACZ BITCLK - @m38 lib.M38 | 21C6 |
| SB ACZ RST L | SB ACZ RST L - @m38 lib.M38 | 21C6 |
| SB ACZ SDATAOUT | SB ACZ SDATAOUT - @m38 lib.M38 | 21C6 |
| SB ACZ SYNC | SB ACZ SYNC - @m38 lib.M38 | 21C6 |
| SB CLK14P3M TIMER | SB CLK14P3M TIMER - @m38 lib.M38 | 5B8 23D3 34D4 |
| SB CLK48M USBCTLR | SB CLK48M USBCTLR - @m38 lib.M38 | 5B8 23D3 34C4 |
| SB CLK100M DMI N | SB CLK100M DMI N - @m38 lib.M38 | 5B8 22C2 34A4 34C2 |
| SB CLK100M DMI P | SB CLK100M DMI P - @m38 lib.M38 | 5B8 22C2 34A4 34C2 |
| SB CLK100M SATA N | SB CLK100M SATA N - @m38 lib.M38 | 5C8 21B6 34B4 34C2 |
| SB CLK100M SATA OE L | SB CLK100M SATA OE L - @m38 lib.M38 | 23C3 33B4 |
| SB CLK100M SATA P | SB CLK100M SATA P - @m38 lib.M38 | 5C8 21B6 34B4 34C2 |
| SB CRT TVOUT MUX | SB CRT TVOUT MUX - @m38 lib.M38 | 22B5 |
| SB GPIO2 | SB GPIO2 - @m38 lib.M38 | 22A6 26C2 |
| SB GPIO3 | SB GPIO3 - @m38 lib.M38 | 22A6 26C2 |
| SB GPIO4 | SB GPIO4 - @m38 lib.M38 | 22A6 26C2 |
| SB GPIO19 | SB GPIO19 - @m38 lib.M38 | 23D3 |
| SB GPIO21 | SB GPIO21 - @m38 lib.M38 | 23D3 |
| SB GPIO26 | SB GPIO26 - @m38 lib.M38 | 23C7 |
| SB GPIO29 | SB GPIO29 - @m38 lib.M38 | 22C4 22D8 |
| SB GPIO30 | SB GPIO30 - @m38 lib.M38 | 22C4 22D8 |
| SB GPIO31 | SB GPIO31 - @m38 lib.M38 | 22C4 22D8 |
| SB GPIO37 | SB GPIO37 - @m38 lib.M38 | 23D3 |
| SB INTVRMEN | SB INTVRMEN - @m38 lib.M38 | 21D6 |
| SB RTC RST L | SB RTC RST L - @m38 lib.M38 | 21D6 26C7 |
| SB RTC X1 | SB RTC X1 - @m38 lib.M38 | 21D6 26D7 |
| SB RTC X2 | SB RTC X2 - @m38 lib.M38 | 21D6 26D7 |
| SB SM INTRUDER L | SB SM INTRUDER L - @m38 lib.M38 | 21D6 26C7 |
| SB SPKR | SB SPKR - @m38 lib.M38 | 23C5 |
| SC RX L | SC RX L - @m38 lib.M38 | 58C5 59B6 59C4 |
| SC TX L | SC TX L - @m38 lib.M38 | 58C5 59B4 59B6 |
| SDVO CTRLCLK | SDVO CTRLCLK - @m38 lib.M38 | 14B6 19A2 |
| | TP SDVO CTRLCLK - @m38 lib.M38 | 19A1 |
| SDVO CTRLDATA | SDVO CTRLDATA - @m38 lib.M38 | 14B6 19A2 |
| | TP SDVO CTRLDATA - @m38 lib.M38 | 19A1 |
| SMB ALERT L | SMB ALERT L - @m38 lib.M38 | 23C5 |
| SMB BSA CLK | SMB BSA CLK - @m38 lib.M38 | 58B5 59C1 |
| SMB BSA DATA | SMB BSA DATA - @m38 lib.M38 | 58B5 59C1 |
| SMB BSB CLK | SMB BSB CLK - @m38 lib.M38 | 58C5 59D1 |
| SMB BSB DATA | SMB BSB DATA - @m38 lib.M38 | 58C7 59D1 |
| SMB LINK ALERT L | SMB LINK ALERT L - @m38 lib.M38 | 23D5 |
| SMC ADAPTER EN | SMC ADAPTER EN - @m38 lib.M38 | 58D5 59D5 |
| | TP SMC ADAPTER EN - @m38 lib.M38 | 59D3 |
| SMC ANALOG ID | SMC ANALOG ID - @m38 lib.M38 | 58A7 59C6 |
| | NC SMC ANALOG ID - @m38 lib.M38 | 59C5 |
| SMC BATT CHG EN | SMC BATT CHG EN - @m38 lib.M38 | 58D7 59D6 |
| | NC SMC BATT CHG EN - @m38 lib.M38 | 59D5 |
| SMC BATT ISENSE | SMC BATT ISENSE - @m38 lib.M38 | 58D5 59B6 |
| | UNUSED SMC SENSE - @m38 lib.M38 | 59B5 59B5 59B6 |
| | SMC FWIRE ISENSE - @m38 lib.M38 | 58D5 59B6 |
| | UNUSED SMC SENSE - @m38 lib.M38 | 59B5 59B5 59B6 |
| | SMC FWIRE ISENSE - @m38 lib.M38 | 58D5 59B6 |
| SMC BATT ISET | SMC BATT ISET - @m38 lib.M38 | 58B5 59D6 |
| | NC SMC BATT ISET - @m38 lib.M38 | 59D5 |
| SMC BATT TRICKLE EN L | SMC BATT TRICKLE EN L - @m38 lib.M38 | 58D7 59D6 |
| | NC SMC BATT TRICKLE EN L - @m38 lib.M38 | 59D5 |
| SMC BATT VSET | SMC BATT VSET - @m38 lib.M38 | 58B5 59D6 |
| | NC SMC BATT VSET - @m38 lib.M38 | 59D5 |
| SMC BC ACOK | SMC BC ACOK - @m38 lib.M38 | 58C5 59B4 |
| SMC BS ALRT L | SMC BS ALRT L - @m38 lib.M38 | 58C5 59B4 |
| SMC CASE OPEN | SMC CASE OPEN - @m38 lib.M38 | 58C5 59C1 |
| SMC CPU ISENSE | SMC CPU ISENSE - @m38 lib.M38 | 58D5 76D2 |
| SMC CPU RESET 3 3 L | SMC CPU RESET 3 3 L - @m38 lib.M38 | 58B5 59A7 |
| SMC CPU VSENSE | SMC CPU VSENSE - @m38 lib.M38 | 58D5 76B2 |
| SMC DCIN ISENSE | SMC DCIN ISENSE - @m38 lib.M38 | 58D5 76D5 |
| SMC EXCARD CP | SMC EXCARD CP - @m38 lib.M38 | 58B7 59C4 |
| SMC EXCARD PWR EN | SMC EXCARD PWR EN - @m38 lib.M38 | 58B7 59C5 |
| | TP SMC EXCARD PWR EN - @m38 lib.M38 | 59C3 |
| SMC EXCARD PWR OC L | SMC EXCARD PWR OC L - @m38 lib.M38 | 58B7 59C4 |
| SMC EXTAL | SMC EXTAL - @m38 lib.M38 | 58C3 59B8 |
| SMC EXTSMI L | SMC EXTSMI L - @m38 lib.M38 | 23B8 58B7 |
| SMC FAN 0 CTL | SMC FAN 0 CTL - @m38 lib.M38 | 58B7 65D8 |
| | FAN RPM0 - @m38 lib.M38 | 65D7 |
| SMC FAN 0 TACH | SMC FAN 0 TACH - @m38 lib.M38 | 58B7 65C8 |
| SMC FAN 1 CTL | SMC FAN 1 CTL - @m38 lib.M38 | 58B7 65B8 66D8 |
| SMC FAN 1 TACH | SMC FAN 1 TACH - @m38 lib.M38 | 58B7 65A8 66C8 |
| SMC FAN 2 CTL | SMC FAN 2 CTL - @m38 lib.M38 | 58B7 65B8 66D8 |
| SMC FAN 2 TACH | SMC FAN 2 TACH - @m38 lib.M38 | 58B7 65A8 66C8 |
| SMC FAN 3 CTL | SMC FAN 3 CTL - @m38 lib.M38 | 58B7 59C5 |
| | TP SMC FAN 3 CTL - @m38 lib.M38 | 59C3 |
| SMC FAN 3 TACH | SMC FAN 3 TACH - @m38 lib.M38 | 58B7 59C5 |
| | TP SMC FAN 3 TACH - @m38 lib.M38 | 59C3 |
| SMC FWE | SMC FWE - @m38 lib.M38 | 58B5 59B4 |
| SMC GPU VSENSE | SMC GPU VSENSE - @m38 lib.M38 | 58D5 59B3 |
| SMC LID | SMC LID - @m38 lib.M38 | 58B5 59C1 |
| SMC LRESET L | SMC LRESET L - @m38 lib.M38 | 6B7 58C7 |
| SMC MANUAL RST L | SMC MANUAL RST L - @m38 lib.M38 | 5D1 59D8 |
| SMC MD1 | SMC MD1 - @m38 lib.M38 | 58C1 60B5 |
| SMC MEM ISENSE | SMC MEM ISENSE - @m38 lib.M38 | 58A7 59B6 |
| | NC SMC MEM ISENSE - @m38 lib.M38 | 59B5 |
| SMC NB ISENSE | SMC NB ISENSE - @m38 lib.M38 | 58A7 59B6 |
| | NC SMC NB ISENSE - @m38 lib.M38 | 59B5 |
| SMC NMI | SMC NMI - @m38 lib.M38 | 58C1 60B4 |
| SMC ODD DETECT | SMC ODD DETECT - @m38 lib.M38 | 58B7 59C4 |
| SMC ONOFF L | SMC ONOFF L - @m38 lib.M38 | 58C5 59B7 59C4 |
| SMC PB7 | SMC PB7 - @m38 lib.M38 | 59C5 59C5 |
| | TP SMC PB7 - @m38 lib.M38 | 59C3 59C3 |
| SMC PBUS VSENSE | SMC PBUS VSENSE - @m38 lib.M38 | 58D5 76C6 |
| SMC PBUS VSENSE R | SMC PBUS VSENSE R - @m38 lib.M38 | 76C8 |
| SMC PF0 | SMC PF0 - @m38 lib.M38 | 58B5 59D5 |
| | TP SMC PF0 - @m38 lib.M38 | 59D3 |
| SMC PF1 | SMC PF1 - @m38 lib.M38 | 58B5 59D5 |
| | TP SMC PF1 - @m38 lib.M38 | 59D3 |
| SMC PM G2 EN | SMC PM G2 EN - @m38 lib.M38 | 58D5 59D5 |
| | TP PM G2 EN - @m38 lib.M38 | 59D3 |
| SMC PROCHOT | SMC PROCHOT - @m38 lib.M38 | 58B5 59C7 |
| SMC PROCHOT 3 3 L | SMC PROCHOT 3 3 L - @m38 lib.M38 | 58D5 59A7 |
| SMC RCIN L | SMC RCIN L - @m38 lib.M38 | 21C3 58C7 |
| SMC REF GATE1 | SMC REF GATE1 - @m38 lib.M38 | 59A5 |
| SMC REF GATE2 | SMC REF GATE2 - @m38 lib.M38 | 59A4 |
| SMC REF IN | SMC REF IN - @m38 lib.M38 | 59A4 |
| SMC RSTGATE L | SMC RSTGATE L - @m38 lib.M38 | 42C3 44A8 58D7 |
| SMC RST L | SMC RST L - @m38 lib.M38 | 58C3 59D7 60B4 |
| SMC RUNTIME SCI L | SMC RUNTIME SCI L - @m38 lib.M38 | 23C8 58B7 |
| SMC RX L | SMC RX L - @m38 lib.M38 | 5D1 58C7 59B4 59B5 60B4 |
| SMC SB NMI | SMC SB NMI - @m38 lib.M38 | 23C3 58D7 |
| SMC SMB 0 CLK | SMC SMB 0 CLK - @m38 lib.M38 | 58C7 59D1 |
| | TP SMC SMB 0 CLK - @m38 lib.M38 | 59D1 |
| SMC SMB 0 DATA | SMC SMB 0 DATA - @m38 lib.M38 | 58C5 59D1 |
| | TP SMC SMB 0 DATA - @m38 lib.M38 | 59D1 |
| SMC SYS ISET | SMC SYS ISET - @m38 lib.M38 | 58B5 59D6 |
| | NC SMC SYS ISET - @m38 lib.M38 | 59D5 |
| SMC SYS KBDLED | SMC SYS KBDLED - @m38 lib.M38 | 58C7 59D5 |
| | TP SMC SYS KBDLED - @m38 lib.M38 | 59D3 |
| SMC SYS LED | SMC SYS LED - @m38 lib.M38 | 58C7 59D7 |
| SMC SYS VSET | SMC SYS VSET - @m38 lib.M38 | 58B5 59D6 |
| | NC SMC SYS VSET - @m38 lib.M38 | 59D5 |
| SMC TCK | SMC TCK - @m38 lib.M38 | 5D1 58C5 59B4 60B4 |
| SMC TDI | SMC TDI - @m38 lib.M38 | 5D1 58B5 59B4 60B4 |
| SMC TDO | SMC TDO - @m38 lib.M38 | 5D1 58B5 59B4 60B5 |
| SMC THRMTRIP | SMC THRMTRIP - @m38 lib.M38 | 58B5 59C7 |
| SMC TMS | SMC TMS - @m38 lib.M38 | 5D1 58B5 59B4 60B5 |
| SMC TPM GPIO | SMC TPM GPIO - @m38 lib.M38 | 58D5 59B6 |
| SMC TPM PP | SMC TPM PP - @m38 lib.M38 | 58C7 59A6 |
| SMC TPM RESET L | SMC TPM RESET L - @m38 lib.M38 | 58B7 59C6 67B7 |
| SMC TRST L | SMC TRST L - @m38 lib.M38 | 5D1 58C1 60B5 |
| SMC TX L | SMC TX L - @m38 lib.M38 | 5D1 58C7 59B4 59B5 60B5 |
| SMC VCL | SMC VCL - @m38 lib.M38 | 58D3 |
| SMC WAKE SCI L | SMC WAKE SCI L - @m38 lib.M38 | 23C1 58D5 |
| SMC XDP TCK | SMC XDP TCK - @m38 lib.M38 | 58C7 59A2 |
| SMC XDP TCK 3 3 | SMC XDP TCK 3 3 - @m38 lib.M38 | 58B5 59A5 |
| SMC XDP TCK R | SMC XDP TCK R - @m38 lib.M38 | 59A1 |
| SMC XDP TDI L | SMC XDP TDI L - @m38 lib.M38 | 58C7 59B3 |
| SMC XDP TDO 3 3 L | SMC XDP TDO 3 3 L - @m38 lib.M38 | 58B7 59A5 |
| SMC XDP TMS L | SMC XDP TMS L - @m38 lib.M38 | 58C7 59B2 |
| SMC XDP TRST L | SMC XDP TRST L - @m38 lib.M38 | 58C7 59A3 |

| Signal | Net | Sheet/Zone |
|---|---|---|
| SMC XDP TRST L R | SMC XDP TRST L R - @m38 lib.M38 | 59A3 |
| SMC XTAL | SMC XTAL - @m38 lib.M38 | 58C3 59B8 |
| SMLINK<0> | SMLINK<0> - @m38 lib.M38 | 23D5 |
| SMLINK<1> | SMLINK<1> - @m38 lib.M38 | 23D5 |
| SMS INT L | SMS INT L - @m38 lib.M38 | 23C3 26C2 58B5 |
| SMS ONOFF L | SMS ONOFF L - @m38 lib.M38 | 58A5 59B4 |
| SMS X AXIS | SMS X AXIS - @m38 lib.M38 | 58B7 59B6 |
| | NC SMS X AXIS - @m38 lib.M38 | 59B5 |
| SMS Y AXIS | SMS Y AXIS - @m38 lib.M38 | 58B7 59B6 |
| | NC SMS Y AXIS - @m38 lib.M38 | 59B5 |
| SMS Z AXIS | SMS Z AXIS - @m38 lib.M38 | 58A7 59B6 |
| | NC SMS Z AXIS - @m38 lib.M38 | 59B5 |
| SPARE SRC3 N | SPARE SRC3 N - @m38 lib.M38 | 34D2 |
| SPARE SRC3 P | SPARE SRC3 P - @m38 lib.M38 | 34D2 |
| SPARE SRC7 N | SPARE SRC7 N - @m38 lib.M38 | 34D2 |
| SPARE SRC7 P | SPARE SRC7 P - @m38 lib.M38 | 34D2 |
| SPI ARB | SPI ARB - @m38 lib.M38 | 22C6 58D5 |
| SPI CE L | SPI CE L - @m38 lib.M38 | 22C6 58B5 63C7 |
| SPI HOLD L | SPI HOLD L - @m38 lib.M38 | 63C4 |
| SPI SCLK | SPI SCLK - @m38 lib.M38 | 22C6 58D5 63C7 |
| SPI SCLK R | SPI SCLK R - @m38 lib.M38 | 63C4 |
| SPI SI | SPI SI - @m38 lib.M38 | 22C6 58D5 63C1 |
| SPI SI R | SPI SI R - @m38 lib.M38 | 63C3 |
| SPI SO | SPI SO - @m38 lib.M38 | 22C6 58D5 63C1 |
| SPI SO R | SPI SO R - @m38 lib.M38 | 63C3 |
| SPI WP L | SPI WP L - @m38 lib.M38 | 63C4 |
| SPKRAMP MUTE | SPKRAMP MUTE - @m38 lib.M38 | 72B5 |
| SPKRAMP SS | SPKRAMP SS - @m38 lib.M38 | 72B4 |
| SUS CLK SB | SUS CLK SB - @m38 lib.M38 | 23C3 59B5 |
| | SMC SUS CLK - @m38 lib.M38 | 58C5 59B6 |
| SV SET UP | SV SET UP - @m38 lib.M38 | 23B6 23C3 60B3 |
| SW RST BTN L | SW RST BTN L - @m38 lib.M38 | 5D1 26C6 |
| SW RST DEBNC | SW RST DEBNC - @m38 lib.M38 | 26C4 |
| SYS LED DRV C | SYS LED DRV C - @m38 lib.M38 | 59D6 |
| SYS LED DRV K | SYS LED DRV K - @m38 lib.M38 | 59D6 |
| SYS ONEWIRE | SYS ONEWIRE - @m38 lib.M38 | 58B7 59B4 |
| SYS POWERFAIL L | SYS POWERFAIL L - @m38 lib.M38 | 6D8 76D2 |
| SYS PWRUP L | SYS PWRUP L - @m38 lib.M38 | 6C7 |
| THERM DX N | THERM DX N - @m38 lib.M38 | 10B5 10C5 |
| THERM DX P | THERM DX P - @m38 lib.M38 | 10B5 10C5 |
| THRM ALERT L | THRM ALERT L - @m38 lib.M38 | 10D3 |
| THRM THM | THRM THM - @m38 lib.M38 | 10C4 |
| TMDS CK TERM | TMDS CK TERM - @m38 lib.M38 | 97C8 |
| TMDS CLK N | TMDS CLK N - @m38 lib.M38 | 93C3 97C8 |
| TMDS CLK P | TMDS CLK P - @m38 lib.M38 | 93C3 97C8 |
| TMDS CONN CLKN | TMDS CONN CLKN - @m38 lib.M38 | 97C4 97C7 |
| TMDS CONN CLKP | TMDS CONN CLKP - @m38 lib.M38 | 97C4 97C7 |
| TMDS CONN DN<0> | TMDS CONN DN<0> - @m38 lib.M38 | 97C4 97D7 |
| TMDS CONN DN<1> | TMDS CONN DN<1> - @m38 lib.M38 | 97C4 97D7 |
| TMDS CONN DN<2> | TMDS CONN DN<2> - @m38 lib.M38 | 97C7 97D4 |
| TMDS CONN DP<0> | TMDS CONN DP<0> - @m38 lib.M38 | 97C4 97D7 |
| TMDS CONN DP<1> | TMDS CONN DP<1> - @m38 lib.M38 | 97C4 97D7 |
| TMDS CONN DP<2> | TMDS CONN DP<2> - @m38 lib.M38 | 97C7 97D4 |
| TMDS DATA N<0> | TMDS DATA N<0> - @m38 lib.M38 | 93C3 97D8 |
| TMDS DATA N<1> | TMDS DATA N<1> - @m38 lib.M38 | 93C3 97D8 |
| TMDS DATA N<2> | TMDS DATA N<2> - @m38 lib.M38 | 93C3 97C8 |
| TMDS DATA N<3> | TMDS DATA N<3> - @m38 lib.M38 | 93C3 95D6 |
| TMDS DATA N<4> | TMDS DATA N<4> - @m38 lib.M38 | 93C3 95D6 |
| TMDS DATA N<5> | TMDS DATA N<5> - @m38 lib.M38 | 93C3 95D6 |
| TMDS DATA P<0> | TMDS DATA P<0> - @m38 lib.M38 | 93C3 97D8 |
| TMDS DATA P<1> | TMDS DATA P<1> - @m38 lib.M38 | 93C3 97C8 |
| TMDS DATA P<2> | TMDS DATA P<2> - @m38 lib.M38 | 93C3 97C8 |
| TMDS DATA P<3> | TMDS DATA P<3> - @m38 lib.M38 | 93C3 95D6 |
| TMDS DATA P<4> | TMDS DATA P<4> - @m38 lib.M38 | 93C3 95D6 |
| TMDS DATA P<5> | TMDS DATA P<5> - @m38 lib.M38 | 93C3 95D6 |
| TPM BADD | TPM BADD - @m38 lib.M38 | 67C4 |
| TPM GPIO1 | TPM GPIO1 - @m38 lib.M38 | 59B5 67C6 |
| TPM GPIO2 | TPM GPIO2 - @m38 lib.M38 | 59B5 67C6 |
| TPM LRESET L | TPM LRESET L - @m38 lib.M38 | 6B7 67B7 |
| TPM PP | TPM PP - @m38 lib.M38 | 59A5 67C6 |
| TPM RST L | TPM RST L - @m38 lib.M38 | 67B6 |
| TPM XTALI | TPM XTALI - @m38 lib.M38 | 59B7 67C6 |
| TPM XTALO | TPM XTALO - @m38 lib.M38 | 59B7 67C6 |
| TP ATI ROMCS L | TP ATI ROMCS L - @m38 lib.M38 | 91A3 |
| TP AZ DOCK EN L | TP AZ DOCK EN L - @m38 lib.M38 | 23C5 |
| TP AZ DOCK RST L | TP AZ DOCK RST L - @m38 lib.M38 | 23C5 |
| TP CLK14P3M SPARE | TP CLK14P3M SPARE - @m38 lib.M38 | 34C4 |
| TP CPU A32 L | TP CPU A32 L - @m38 lib.M38 | 7C7 |
| TP CPU A33 L | TP CPU A33 L - @m38 lib.M38 | 7B7 |
| TP CPU A34 L | TP CPU A34 L - @m38 lib.M38 | 7B7 |
| TP CPU A35 L | TP CPU A35 L - @m38 lib.M38 | 7B7 |
| TP CPU A36 L | TP CPU A36 L - @m38 lib.M38 | 7B7 |
| TP CPU A37 L | TP CPU A37 L - @m38 lib.M38 | 7B7 |
| TP CPU A38 L | TP CPU A38 L - @m38 lib.M38 | 7B7 |
| TP CPU A39 L | TP CPU A39 L - @m38 lib.M38 | 7B7 |
| TP CPU APM0 L | TP CPU APM0 L - @m38 lib.M38 | 7B7 |
| TP CPU APM1 L | TP CPU APM1 L - @m38 lib.M38 | 7B7 |
| TP CPU CPUSLP L | TP CPU CPUSLP L - @m38 lib.M38 | 21C4 |
| TP CPU EXTBREF | TP CPU EXTBREF - @m38 lib.M38 | 7B6 |
| TP CPU HFPLL | TP CPU HFPLL - @m38 lib.M38 | 7B7 |
| TP CPU SPARE0 | TP CPU SPARE0 - @m38 lib.M38 | 7B6 |
| TP CPU SPARE1 | TP CPU SPARE1 - @m38 lib.M38 | 7B6 |
| TP CPU SPARE2 | TP CPU SPARE2 - @m38 lib.M38 | 7B6 |
| TP CPU SPARE3 | TP CPU SPARE3 - @m38 lib.M38 | 7B6 |
| TP CPU SPARE4 | TP CPU SPARE4 - @m38 lib.M38 | 7B6 |
| TP CPU SPARE5 | TP CPU SPARE5 - @m38 lib.M38 | 7B6 |
| TP CPU SPARE6 | TP CPU SPARE6 - @m38 lib.M38 | 7B6 |
| TP CPU SPARE7 | TP CPU SPARE7 - @m38 lib.M38 | 7B6 |
| TP FB A MA12 | TP FB A MA12 - @m38 lib.M38 | 87D5 |
| TP FB A ODT<0> | TP FB A ODT<0> - @m38 lib.M38 | 87B5 |
| TP FB A ODT<1> | TP FB A ODT<1> - @m38 lib.M38 | 87B5 |
| TP FB B MA12 | TP FB B MA12 - @m38 lib.M38 | 87D1 |
| TP FB B ODT<0> | TP FB B ODT<0> - @m38 lib.M38 | 87B1 |
| TP FB B ODT<1> | TP FB B ODT<1> - @m38 lib.M38 | 87B1 |
| TP FW CNA | TP FW CNA - @m38 lib.M38 | 44B3 |
| TP FW LKON | TP FW LKON - @m38 lib.M38 | 44B3 |
| TP FW LPS | TP FW LPS - @m38 lib.M38 | 44B3 |
| TP FW MPCIACT L | TP FW MPCIACT L - @m38 lib.M38 | 44B3 |
| TP FW NANDTREE | TP FW NANDTREE - @m38 lib.M38 | 44B3 |
| TP FW ROM AD | TP FW ROM AD - @m38 lib.M38 | 44B3 |
| TP FW VAUX PRES | TP FW VAUX PRES - @m38 lib.M38 | 44B3 |
| TP LVDS VBG | TP LVDS VBG - @m38 lib.M38 | 13D5 |
| TP MEM A A<14> | TP MEM A A<14> - @m38 lib.M38 | 28C3 |
| TP MEM A A<15> | TP MEM A A<15> - @m38 lib.M38 | 28C3 |
| TP MEM B A<14> | TP MEM B A<14> - @m38 lib.M38 | 5B4 29C3 |
| TP MEM B A<15> | TP MEM B A<15> - @m38 lib.M38 | 5B4 29C3 |
| TP NB TESTIN L | TP NB TESTIN L - @m38 lib.M38 | 14D6 |
| TP NB XOR FSB2 H7 | TP NB XOR FSB2 H7 - @m38 lib.M38 | 14D6 |
| TP NB XOR LVDS A34 | TP NB XOR LVDS A34 - @m38 lib.M38 | 14C6 |
| TP NB XOR LVDS A35 | TP NB XOR LVDS A35 - @m38 lib.M38 | 14C6 |
| TP NB XOR LVDS D27 | TP NB XOR LVDS D27 - @m38 lib.M38 | 14C6 |
| TP NB XOR LVDS D28 | TP NB XOR LVDS D28 - @m38 lib.M38 | 14C6 |
| TP PCI CLK SPARE | TP PCI CLK SPARE - @m38 lib.M38 | 5B4 34C4 |
| TP PCI GNT0 L | TP PCI GNT0 L - @m38 lib.M38 | 22B6 |
| TP PCI GNT1 L | TP PCI GNT1 L - @m38 lib.M38 | 22B6 |
| TP PCI GNT2 L | TP PCI GNT2 L - @m38 lib.M38 | 22B6 |
| TP PCI GNT4 L | TP PCI GNT4 L - @m38 lib.M38 | 22B6 |
| TP PCI PME L | TP PCI PME L - @m38 lib.M38 | 22A6 |
| TP SB ACZ SDIN1 | TP SB ACZ SDIN1 - @m38 lib.M38 | 21C6 |
| TP SB ACZ SDIN2 | TP SB ACZ SDIN2 - @m38 lib.M38 | 21C6 |
| TP SB DRQ0 L | TP SB DRQ0 L - @m38 lib.M38 | 21D4 |
| TP SB GPIO6 | TP SB GPIO6 - @m38 lib.M38 | 23C5 |
| TP SB GPIO23 | TP SB GPIO23 - @m38 lib.M38 | 21D5 |
| TP SB GPIO25 DO NOT USE | TP SB GPIO25 DO NOT USE - @m38 lib.M38 | 23C3 |
| TP SB GPIO38 | TP SB GPIO38 - @m38 lib.M38 | 23C3 |
| TP SB RSVD9 | TP SB RSVD9 - @m38 lib.M38 | 22A6 |
| TP SB SATALED L | TP SB SATALED L - @m38 lib.M38 | 21C6 |
| TP SB XOR AD5 | TP SB XOR AD5 - @m38 lib.M38 | 22A7 |
| TP SB XOR AD9 | TP SB XOR AD9 - @m38 lib.M38 | 22A7 |
| TP SB XOR AE5 | TP SB XOR AE5 - @m38 lib.M38 | 22A7 |
| TP SB XOR AE9 | TP SB XOR AE9 - @m38 lib.M38 | 22A6 |
| TP SB XOR AG4 | TP SB XOR AG4 - @m38 lib.M38 | 22A7 |
| TP SB XOR AG8 | TP SB XOR AG8 - @m38 lib.M38 | 22A6 |
| TP SB XOR AH4 | TP SB XOR AH4 - @m38 lib.M38 | 22A7 |
| TP SB XOR AH8 | TP SB XOR AH8 - @m38 lib.M38 | 22A6 |
| TP SB XOR T5 | TP SB XOR T5 - @m38 lib.M38 | 21C6 |
| TP SB XOR U3 | TP SB XOR U3 - @m38 lib.M38 | 21C6 |
| TP SB XOR U5 | TP SB XOR U5 - @m38 lib.M38 | 21C6 |
| TP SB XOR U7 | TP SB XOR U7 - @m38 lib.M38 | 21C6 |
| TP SB XOR V3 | TP SB XOR V3 - @m38 lib.M38 | 21C6 |
| TP SB XOR V4 | TP SB XOR V4 - @m38 lib.M38 | 21C6 |
| TP SB XOR V6 | TP SB XOR V6 - @m38 lib.M38 | 21C6 |
| TP SB XOR V7 | TP SB XOR V7 - @m38 lib.M38 | 21C6 |
| TP SB XOR W1 | TP SB XOR W1 - @m38 lib.M38 | 21C6 |
| TP SB XOR W3 | TP SB XOR W3 - @m38 lib.M38 | 21C6 |
| TP SB XOR Y1 | TP SB XOR Y1 - @m38 lib.M38 | 21C6 |
| TP SB XOR Y2 | TP SB XOR Y2 - @m38 lib.M38 | 21C6 |
| TP U5999 P1 | TP U5999 P1 - @m38 lib.M38 | 59A7 |
| TP U5999 P13 | TP U5999 P13 - @m38 lib.M38 | 59A5 |
| TP U5999 P14 | TP U5999 P14 - @m38 lib.M38 | 59A5 |
| TP U8400 AG14 | TP U8400 AG14 - @m38 lib.M38 | 91A5 |
| TP U8900 J2 | TP U8900 J2 - @m38 lib.M38 | 89A7 |
| TP U8900 J3 | TP U8900 J3 - @m38 lib.M38 | 89A7 |
| TP U8950 J2 | TP U8950 J2 - @m38 lib.M38 | 89A4 |
| TP U8950 J3 | TP U8950 J3 - @m38 lib.M38 | 89A4 |
| TP U9000 J2 | TP U9000 J2 - @m38 lib.M38 | 90A7 |
| TP U9000 J3 | TP U9000 J3 - @m38 lib.M38 | 90A7 |
| TP U9050 J2 | TP U9050 J2 - @m38 lib.M38 | 90A4 |
| TP U9050 J3 | TP U9050 J3 - @m38 lib.M38 | 90A4 |
| TSENSE GPU DXP | TSENSE GPU DXP - @m38 lib.M38 | 61C5 |
| TSENSE NB DXP | TSENSE NB DXP - @m38 lib.M38 | 61B5 |
| TSENSE NB GPU DXN | TSENSE NB GPU DXN - @m38 lib.M38 | 61B5 |
| TV DACA OUT | TV DACA OUT - @m38 lib.M38 | 13C5 19B1 |
| | TV DACB OUT - @m38 lib.M38 | 13C5 19B1 |
| | TV DACC OUT - @m38 lib.M38 | 13C5 19B1 |
| | TV IRTNA - @m38 lib.M38 | 13C5 19B1 |
| | TV IRTNB - @m38 lib.M38 | 13C5 19B1 |
| | TV IRTNC - @m38 lib.M38 | 13C5 19B1 |
| | TV IREF - @m38 lib.M38 | 13C5 19B1 |
| | PP3V3 S0 NB VCCA TVBG - @m38 lib.M38 | 17C6 19B1 |
| | PP3V3 S0 NB VCCA TVDACC - @m38 lib.M38 | 17C6 19B1 |
| | PP3V3 S0 NB VCCA TVDACB - @m38 lib.M38 | 17C6 19B1 |
| | PP3V3 S0 NB VCCA TVDACA - @m38 lib.M38 | 17C6 19B1 |
| | =PP1V5 S0 NB TVDAC - @m38 lib.M38 | 6C4 19B2 19D7 |
| | =PP1V5 S0 AIRPORT - @m38 lib.M38 | 6C4 53D3 |
| | PP1V5 S0 - @m38 lib.M38 | 6C6 80C2 |
| | =PP1V5 S0 SB - @m38 lib.M38 | 6C4 25A8 25C8 |
| | =PP1V5 S0 SB VCC1 5 A - @m38 lib.M38 | 6C4 24A3 25C1 |
| | =PP1V5 S0 SB VCC1 5 A USB CORE - @m38 lib.M38 | 6C4 24A3 25B1 |
| | =PP1V5 S0 SB VCCUSBPLL - @m38 lib.M38 | 6C4 24A5 25B6 |
| | =PP1V5 S0 SB VCC1 5 A ATX - @m38 lib.M38 | 6C4 24A5 25C6 |
| | =PP1V5 S0 SB VCCSATAPLL - @m38 lib.M38 | 6C4 24B5 25D6 |
| | =PP1V5 S0 SB VCC1 5 A ARX - @m38 lib.M38 | 6C4 24B5 25D6 |
| | =PP1V5 S0 NB 3GPLL - @m38 lib.M38 | 6C4 19A6 19A6 |
| | =PP1V5 S0 NB PLL - @m38 lib.M38 | 6C4 19C8 19D7 |
| | =PP1V5 S0 NB VCCAUX - @m38 lib.M38 | 6C4 6C4 16D1 17B6 19A7 19D7 |
| | =PP1V5 S0 NB VCCD HMPLL - @m38 lib.M38 | 6C4 17C6 19D7 |
| | =PP1V5 S0 NB PCIE - @m38 lib.M38 | 6C4 13D2 19D7 |
| | =PP1V5 S0 CPU - @m38 lib.M38 | 6C4 8B6 8C5 |
| | TV IRTNC - @m38 lib.M38 | 13C5 19B1 |
| | TV IRTNB - @m38 lib.M38 | 13C5 19B1 |
| | TV IRTNA - @m38 lib.M38 | 13C5 19B1 |
| | TV IREF - @m38 lib.M38 | 13C5 19B1 |
| | TV DACC OUT - @m38 lib.M38 | 13C5 19B1 |
| | TV DACB OUT - @m38 lib.M38 | 13C5 19B1 |
| | PP3V3 S0 NB VCCA TVDACC - @m38 lib.M38 | 17C6 19B1 |
| | PP3V3 S0 NB VCCA TVDACB - @m38 lib.M38 | 17C6 19B1 |
| | PP3V3 S0 NB VCCA TVDACA - @m38 lib.M38 | 17C6 19B1 |
| | PP3V3 S0 NB VCCA TVBG - @m38 lib.M38 | 17C6 19B1 |
| | PP1V5 S0 - @m38 lib.M38 | 6C6 80C2 |
| | =PP1V5 S0 SB VCCUSBPLL - @m38 lib.M38 | 6C4 24A5 25B6 |
| | =PP1V5 S0 SB VCCSATAPLL - @m38 lib.M38 | 6C4 24B5 25D6 |
| | =PP1V5 S0 SB VCC1 5 A USB CORE - @m38 lib.M38 | 6C4 24A3 25B1 |
| | =PP1V5 S0 SB VCC1 5 A ATX - @m38 lib.M38 | 6C4 24A5 25C6 |
| | =PP1V5 S0 SB VCC1 5 A ARX - @m38 lib.M38 | 6C4 24B5 25D6 |
| | =PP1V5 S0 SB VCC1 5 A - @m38 lib.M38 | 6C4 24A3 25C1 |
| | =PP1V5 S0 SB - @m38 lib.M38 | 6C4 25A8 25C8 |
| | =PP1V5 S0 NB VCCD HMPLL - @m38 lib.M38 | 6C4 17C6 19D7 |
| | =PP1V5 S0 NB VCCAUX - @m38 lib.M38 | 6C4 6C4 16D1 17B6 19A7 19D7 |
| | =PP1V5 S0 NB TVDAC - @m38 lib.M38 | 6C4 19B2 19D7 |
| | =PP1V5 S0 NB PLL - @m38 lib.M38 | 6C4 19C8 19D7 |
| | =PP1V5 S0 NB PCIE - @m38 lib.M38 | 6C4 13D2 19D7 |
| | =PP1V5 S0 NB 3GPLL - @m38 lib.M38 | 6C4 19A6 19A6 |
| | =PP1V5 S0 NB - @m38 lib.M38 | 6C4 19B2 19D7 |
| | =PP1V5 S0 CPU - @m38 lib.M38 | 6C4 8B6 8C5 |
| | =PP1V5 S0 AIRPORT - @m38 lib.M38 | 6C4 53D3 |
| U600 3 | U600 3 - @m38 lib.M38 | 6C7 |
| U600 6 | U600 6 - @m38 lib.M38 | 6B7 |
| U600 8 | U600 8 - @m38 lib.M38 | 6B7 |
| U600 11 | U600 11 - @m38 lib.M38 | 6A7 |
| U2698 4 | U2698 4 - @m38 lib.M38 | 26C4 |
| U3100 VDDQ | U3100 VDDQ - @m38 lib.M38 | 31C5 |
| U6100 VCC | U6100 VCC - @m38 lib.M38 | 61C4 |
| U8595 1 | U8595 1 - @m38 lib.M38 | 85D2 |
| USB AIRPORT N | USB AIRPORT N - @m38 lib.M38 | 53B4 |
| USB AIRPORT P | USB AIRPORT P - @m38 lib.M38 | 53B4 |
| USB A N | USB A N - @m38 lib.M38 | 22C2 47D7 |
| USB A OC L | USB A OC L - @m38 lib.M38 | 22C4 22D8 47B8 47B8 |
| USB A P | USB A P - @m38 lib.M38 | 22C2 47C7 |
| USB B N | USB B N - @m38 lib.M38 | 22C2 53B2 |
| USB B OC L | USB B OC L - @m38 lib.M38 | 22C4 22D8 47B8 |
| USB B P | USB B P - @m38 lib.M38 | 22C2 53B2 |
| USB CAMERA N | USB CAMERA N - @m38 lib.M38 | 47B2 |
| USB CAMERA P | USB CAMERA P - @m38 lib.M38 | 47B2 |
| USB C N | USB C N - @m38 lib.M38 | 22C2 47B7 |
| USB C OC L | USB C OC L - @m38 lib.M38 | 22C4 22D8 47B7 47B8 |
| USB C P | USB C P - @m38 lib.M38 | 22C2 47B7 |
| USB D N | USB D N - @m38 lib.M38 | 22C2 47B3 |
| USB D OC L | USB D OC L - @m38 lib.M38 | 22C4 22D8 47B7 |
| USB D P | USB D P - @m38 lib.M38 | 22C2 47B3 |
| USB E N | USB E N - @m38 lib.M38 | 22C2 47A7 |
| USB E OC L | USB E OC L - @m38 lib.M38 | 22C4 22D8 47B7 |
| USB E P | USB E P - @m38 lib.M38 | 22C2 47A7 |
| USB F N | USB F N - @m38 lib.M38 | 22C2 49C6 |
| | USB FLASH N - @m38 lib.M38 | 49C5 |
| USB F P | USB F P - @m38 lib.M38 | 22C2 49C6 |
| | USB FLASH P - @m38 lib.M38 | 49C5 |
| USB G N | USB G N - @m38 lib.M38 | 22C2 47A3 |
| | USB BT N - @m38 lib.M38 | 47A3 |
| USB G P | USB G P - @m38 lib.M38 | 22C2 47A3 |
| | USB BT P - @m38 lib.M38 | 47A3 |
| USB H N | USB H N - @m38 lib.M38 | 22C2 47C3 |
| USB H P | USB H P - @m38 lib.M38 | 22C2 47C3 |
| USB IR N | USB IR N - @m38 lib.M38 | 47C2 |
| USB IR P | USB IR P - @m38 lib.M38 | 47C2 |
| USB PORT0 N | USB PORT0 N - @m38 lib.M38 | 47D5 |
| USB PORT0 P | USB PORT0 P - @m38 lib.M38 | 47C5 |
| USB PORT1 N | USB PORT1 N - @m38 lib.M38 | 47B5 |
| USB PORT1 P | USB PORT1 P - @m38 lib.M38 | 47B5 |
| USB PORT2 N | USB PORT2 N - @m38 lib.M38 | 47A5 |
| USB PORT2 P | USB PORT2 P - @m38 lib.M38 | 47A5 |
| USB RBIAS PN | USB RBIAS PN - @m38 lib.M38 | 22C2 |
| VGA B | VGA B - @m38 lib.M38 | 97B6 97C5 |
| VGA G | VGA G - @m38 lib.M38 | 97A6 97C5 |
| VGA HSYNC | VGA HSYNC - @m38 lib.M38 | 97A3 97C5 |
| VGA R | VGA R - @m38 lib.M38 | 97A6 97C5 |
| VGA VSYNC | VGA VSYNC - @m38 lib.M38 | 97A3 97C5 |
| VMAIN AVLBL | VMAIN AVLBL - @m38 lib.M38 | 41C7 |
| VOL DOWN | VOL DOWN - @m38 lib.M38 | 68A6 68C7 |
| VOL UP | VOL UP - @m38 lib.M38 | 68A6 68C7 |
| VREG FB | VREG FB - @m38 lib.M38 | 68A3 |
| VR PWRGD CK410 | VR PWRGD CK410 - @m38 lib.M38 | 23C5 26A8 |
| VR PWRGOOD DELAY | VR PWRGOOD DELAY - @m38 lib.M38 | 5C7 14B6 26D5 75C6 |
| XDP BPM L<0> | XDP BPM L<0> - @m38 lib.M38 | 7C6 11B3 |
| XDP BPM L<1> | XDP BPM L<1> - @m38 lib.M38 | 7C6 11B3 |
| XDP BPM L<2> | XDP BPM L<2> - @m38 lib.M38 | 7C6 11B3 |
| XDP BPM L<3> | XDP BPM L<3> - @m38 lib.M38 | 7C6 11B3 |
| XDP BPM L<4> | XDP BPM L<4> - @m38 lib.M38 | 7C6 11B3 |
| XDP BPM L<5> | XDP BPM L<5> - @m38 lib.M38 | 7C6 11B3 |
| XDP DBRESET L | XDP DBRESET L - @m38 lib.M38 | 7C6 11B4 26B5 |
| XDP TCK | XDP TCK - @m38 lib.M38 | 5D1 7A8 7C6 11B3 11B3 59A1 59A6 |
| XDP TDI | XDP TDI - @m38 lib.M38 | 5D1 7B8 7C6 11B3 59B2 |
| XDP TDO | XDP TDO - @m38 lib.M38 | 5D1 7C6 11B5 59A6 |
| XDP TMS | XDP TMS - @m38 lib.M38 | 5D1 7B8 7C6 11B3 59B1 |
| XDP TRST L | XDP TRST L - @m38 lib.M38 | 5D1 7C6 11B3 59A2 |
| ZH701P1 | ZH701P1 - @m38 lib.M38 | 6A3 |
| ZH702P1 | ZH702P1 - @m38 lib.M38 | 6A3 |
| ZH703P1 | ZH703P1 - @m38 lib.M38 | 6A3 |
| ZH704P1 | ZH704P1 - @m38 lib.M38 | 6B3 |

Title: Cref Part Report
Design: m38
Date: Nov 15 17:44:33 2005

| Ref | Part | Location |
|---|---|---|
| C85A0 | CAP_402 | m38[85D1] |
| C600 | CAP_402 | m38[6C7] |
| C601 | CAP_402 | m38[6A3] |
| C602 | CAP_402 | m38[6A3] |
| C603 | CAP_402 | m38[6A3] |
| C604 | CAP_402 | m38[6A4] |
| C610 | CAP_402 | m38[6C7] |
| C699 | CAP_P_CASE-C1 | m38[6D7] |
| C0800 | CAP_402 | m38[8B5] |
| C0801 | CAP_603 | m38[8B5] |
| C900 | CAP_805 | m38[9B6] |
| C901 | CAP_805 | m38[9B6] |
| C902 | CAP_805 | m38[9A6] |
| C903 | CAP_805 | m38[9A6] |
| C904 | CAP_805 | m38[9A6] |
| C905 | CAP_805 | m38[9A6] |
| C906 | CAP_805 | m38[9A6] |
| C907 | CAP_805 | m38[9B5] |
| C908 | CAP_805 | m38[9B7] |
| C909 | CAP_805 | m38[9B5] |
| C910 | CAP_805 | m38[9B7] |
| C911 | CAP_805 | m38[9B7] |
| C912 | CAP_805 | m38[9A7] |
| C913 | CAP_805 | m38[9A7] |
| C914 | CAP_805 | m38[9A7] |
| C915 | CAP_805 | m38[9A7] |
| C916 | CAP_805 | m38[9A7] |
| C917 | CAP_805 | m38[9A7] |
| C918 | CAP_805 | m38[9A7] |
| C919 | CAP_805 | m38[9A7] |
| C920 | CAP_805 | m38[9A5] |
| C921 | CAP_805 | m38[9A7] |
| C922 | CAP_805 | m38[9A7] |
| C923 | CAP_805 | m38[9B7] |
| C924 | CAP_805 | m38[9A7] |
| C925 | CAP_805 | m38[9A7] |
| C926 | CAP_402 | m38[9B7] |
| C928 | CAP_805 | m38[9B6] |
| C929 | CAP_805 | m38[9B5] |
| C930 | CAP_805 | m38[9A6] |
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| C946 | CAP_P_3P_D2T | m38[9A6] |
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| C953 | CAP_402 | m38[9D2] |
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| C1001 | CAP_402 | m38[10D4] |
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| C9347 | CAP 402 | m38[93A5] |
| C9400 | CAP 603-1 | m38[94C7] |
| C9401 | CAP 402 | m38[94C6] |
| C9410 | CAP 402 | m38[94C6] |
| C9420 | CAP 1210 | m38[94C5] |
| C9450 | CAP 805 | m38[94C2] |
| C9470 | CAP 402 | m38[94B2] |
| C9700 | CAP 402 | m38[97C8] |
| C9710 | CAP 603 | m38[97C3] |
| C9711 | CAP 402 | m38[97D3] |
| C9713 | CAP 402 | m38[97C2] |
| C9714 | CAP 402 | m38[97C2] |
| C9740 | CAP 402 | m38[97A7] |
| C9741 | CAP 402 | m38[97A6] |
| C9742 | CAP 402 | m38[97A6] |
| D2500 | DIODE SCHOT SOT23 | m38[25C8] |
| D2501 | DIODE SCHOT SOT23 | m38[25D8] |
| D2600 | DIODE SCHOT SOT23 | m38[26D8] |
| D2601 | DIODE SCHOT SOT23 | m38[26C8] |
| D4600 | DIODE SMC | m38[46D5] |
| D4690 | ZENER SOT23 | m38[46A6] |
| D4700 | DIODE SCHOT 3P A SC-75 | m38[47C5] |
| D4701 | DIODE SCHOT 3P A SC-75 | m38[47B5] |
| D4702 | DIODE SCHOT 3P A SC-75 | m38[47A5] |
| D4705 | DIODE SCHOT 3P A SC-75 | m38[47C6] |
| D4706 | DIODE SCHOT 3P A SC-75 | m38[47B6] |
| D4707 | DIODE SCHOT 3P A SC-75 | m38[47A6] |
| D4900 | DIODE SCHOT 3P A SC-75 | m38[49B4] |
| D6500 | DIODE SOT23 | m38[65C4] |
| D6501 | DIODE SOT23 | m38[65B4] |
| D6502 | DIODE SCHOT SMB | m38[65C4] |
| D6503 | DIODE SCHOT SMB | m38[65B4] |
| D6600 | DIODE SOT23 | m38[66C4] |
| D6601 | DIODE SCHOT SMB | m38[66C3] |
| D7500 | DIODE SCHOT SMB | m38[75C3] |
| D7501 | DIODE SCHOT SMB | m38[75B2] |
| D7599 | DIODE SOT23 | m38[76D6] |
| D8520 | DIODE SCHOT SMB | m38[85C3] |
| D9700 | ZENER CASE425 | m38[97C1] |
| DP4610 | DIODE DUAL 6P SOT-363 | m38[46D4 46D3] |
| DP4611 | DIODE DUAL 6P SOT-363 | m38[46C4 46C3] |
| DP4620 | DIODE DUAL 6P SOT-363 | m38[46B4 46B3] |
| DP4621 | DIODE DUAL 6P SOT-363 | m38[46A4 46A3] |
| DZ7300 | SUPPR TRANSIENT 4P1 0405 | m38[73C6] |
| DZ7301 | SUPPR TRANSIENT 4P1 0405 | m38[73A5] |
| DZ7302 | SUPPR TRANSIENT 4P1 0405 | m38[73A4] |
| DZ7303 | SUPPR TRANSIENT 4P1 0405 | m38[73C5] |
| DZ7304 | SUPPR TRANSIENT 4P1 0405 | m38[73C4] |
| F4600 | FUSE SM-LF | m38[46D2] |
| F4601 | FUSE SM-LF | m38[46D2] |
| F4602 | FUSE MINISMD-LF | m38[46D3] |
| F4701 | FUSE MINISMD-LF | m38[47D3] |
| F9710 | FUSE SM-LF | m38[97D5] |
| FL4610 | FILTER 4P 2012 | m38[46C3] |
| FL4620 | FILTER 4P 2012 | m38[46B3] |
| FL9740 | FILTER LC SM-220MHZ-LF | m38[97B7] |
| FL9741 | FILTER LC SM-220MHZ-LF | m38[97A7] |
| FL9742 | FILTER LC SM-220MHZ-LF | m38[97A7] |
| FLE011 | FILTER 4P 2012 | m38[46C3] |
| FLE021 | FILTER 4P 2012 | m38[46B3] |
| GV3801 | HOLE VIA | m38[38A8] |
| GV3802 | HOLE VIA | m38[38A7] |
| GV3803 | HOLE VIA | m38[38A8] |
| GV3804 | HOLE VIA | m38[38A7] |
| GV3805 | HOLE VIA | m38[38A8] |
| GV3806 | HOLE VIA | m38[38A7] |
| GV3807 | HOLE VIA | m38[38A8] |
| GV3808 | HOLE VIA | m38[38A7] |
| J2 | CON 2RTSM 125 SM-2MT-BLK-LF | m38[61B7] |
| J3 | CON 2RTSM 125 SM-2MT-BLK-LF | m38[61B6] |

| Ref | Part | Location |
|---|---|---|
| J600 | CON_M12RT_D_THA_M-RT-TH | m38[6D7] |
| J0700 | CPU_YONAH_SKT_BGA | m38[7C3 7D7] |
| J0700 | CPU_YONAH_SKT_BGA | m38[8D4 8D8] |
| J1000 | CON_2RTSM_125_SM-2MT-BLK-LF | m38[10B7] |
| J1101 | CON_F28RT_S2MT_SM_F-RT-SM | m38[11C2] |
| J2600 | BATTERY_2P_TH | m38[26C8] |
| J2800 | CON_F200RT_DDR2DIMM_5MT_SM_F-RT-SM1 | m38[28D5] |
| J2900 | CON_F200RT_DDR2DIMM_5MT_SM_F-RT-SM1 | m38[29D5] |
| J2901 | CON_F4ST_S2MT_SM_F-ST-SM | m38[59C7] |
| J2903 | CON_M2ST_S2MT_SM_M-ST-SM | m38[59C8] |
| J4700 | CON_F10ST_D_SMA_F-ST-SM | m38[47A2] |
| J4950 | CON_M4RT_S2MT_SM_M-RT-SM | m38[49C4] |
| J5300 | CON_F52RT_D2MT_SM_F-RT-SM | m38[53C5] |
| J6000 | CON_F30STSM_5047_SM1 | m38[60B5] |
| J6500 | CON_M4RT_S2MT_SM_M-RT-SM | m38[65D3] |
| J6501 | CON_M5RT_S2MT_SMA_M-RT-SM | m38[65B2] |
| J6600 | CON_F4ST_S2MT_SM_F-ST-SM | m38[66C2] |
| J6601 | CON_M4RT_S2MT_SM_M-RT-SM | m38[66B5] |
| J6602 | CON_M4RT_S2MT_SM_M-RT-SM | m38[66B3] |
| J7300 | CON_F4ST_S4MT_TH1_F-ST-TH | m38[73D8] |
| J7301 | CON_M7RT_S2MT_SM_M-RT-SM | m38[73D1] |
| J7303 | CON_F9ANG_S4MT_TH1_F-ANG-TH | m38[73B3] |
| J9401 | CON_M4ST_S_SM_M-ST-SM | m38[94C2] |
| J9402 | CON_F30ST_D_SM_F-ST-SM | m38[94B6] |
| J9410 | CON_M4ST_S_SM_M-ST-SM | m38[94A3] |
| J9710 | CON_DVI_F32ST_Q2MT_SM_F-ST-SM | m38[97D5] |
| JC900 | CON_M7ST_SATA_SM_M-ST-SM | m38[38B8] |
| JC901 | CON_F50ST_D2MT_SM_F-ST-SM | m38[38D2] |
| JD600 | CON_RJ45_10ANG_S3MT_TH1_F-ANG-TH | m38[43C6] |
| JE000 | CON_F6ST_S4MT_TH1_F-ST-TH | m38[46C2] |
| JE001 | CON_F6ST_S4MT_TH1_F-ST-TH | m38[46B2] |
| JE310 | CON_F4ST_USB_S3MT_TH_F-ST-TH | m38[47D4] |
| JE320 | CON_F4ST_USB_S3MT_TH_F-ST-TH | m38[47B4] |
| JE330 | CON_F4ST_USB_S3MT_TH_F-ST-TH | m38[47A4] |
| JE350 | CON_M14RT_S2MT_SM_M-RT-SM | m38[47C1] |
| L1934 | IND_0603 | m38[19C7] |
| L1936 | IND_0603 | m38[19C7] |
| L1970 | IND_1210 | m38[19A5] |
| L1975 | IND_0805 | m38[19A5] |
| L2500 | IND_SM-3 | m38[25B8] |
| L2507 | IND_1206 | m38[25A7] |
| L3301 | IND_0402 | m38[33D7] |
| L3302 | IND_0402 | m38[33D3] |
| L4200 | IND_SM | m38[42D7] |
| L4201 | IND_SM | m38[42B7] |
| L4300 | IND_SM | m38[43D7] |
| L4409 | IND_0402 | m38[44D6] |
| L4610 | IND_1206-LF | m38[46D2] |
| L4620 | IND_1206-LF | m38[46B2] |
| L4690 | IND_SM-1 | m38[46A6] |
| L4710 | IND_SM | m38[47D6] |
| L4712 | FILTER_4P_2012 | m38[47C6] |
| L4720 | IND_SM | m38[47C6] |
| L4722 | FILTER_4P_2012 | m38[47B6] |
| L4730 | IND_SM | m38[47B6] |
| L4732 | FILTER_4P_2012 | m38[47A6] |
| L4740 | IND_SM | m38[47D2] |
| L4742 | FILTER_4P_2012 | m38[47C2] |
| L4752 | FILTER_4P_2012 | m38[47B2] |
| L5300 | FILTER_4P_2012 | m38[53B3] |
| L6800 | IND_0402 | m38[68A5] |
| L6801 | IND_0402 | m38[68D6] |
| L7200 | IND_SM-1 | m38[72D6] |
| L7201 | IND_0603 | m38[72C2] |
| L7202 | IND_0603 | m38[72C2] |
| L7203 | IND_0603 | m38[72C3] |
| L7204 | IND_0603 | m38[72C3] |
| L7205 | IND_0603 | m38[72D6] |
| L7206 | IND_0603 | m38[72C6] |
| L7207 | IND_0603 | m38[72C6] |
| L7208 | IND_0603 | m38[72C6] |
| L7300 | IND_SM | m38[73D7] |
| L7301 | IND_SM | m38[73D7] |
| L7302 | IND_SM | m38[73D7] |
| L7303 | IND_SM | m38[73D7] |
| L7304 | IND_SM | m38[73D5] |
| L7305 | IND_SM | m38[73D5] |
| L7306 | IND_SM | m38[73D5] |
| L7307 | IND_SM | m38[73D5] |
| L7309 | IND_SM | m38[73C6] |
| L7310 | IND_SM | m38[73C6] |
| L7312 | IND_SM | m38[73C4] |
| L7313 | IND_SM | m38[73C4] |
| L7314 | IND_SM | m38[73B7] |
| L7315 | IND_SM | m38[73B7] |
| L7316 | IND_SM | m38[73B7] |
| L7317 | IND_SM | m38[73A7] |
| L7318 | IND_SM | m38[73B7] |
| L7319 | IND_SM | m38[73B7] |
| L7320 | IND_SM | m38[73A7] |
| L7322 | IND_SM | m38[73B5] |
| L7323 | IND_SM | m38[73B5] |
| L7324 | IND_SM | m38[73B5] |
| L7325 | IND_SM | m38[73A5] |
| L7326 | IND_SM | m38[73B5] |
| L7327 | IND_SM | m38[73B5] |
| L7328 | IND_SM | m38[73A5] |
| L7500 | IND_SM | m38[75D1] |
| L7501 | IND_SM | m38[75B2] |
| L7502 | IND_TH-VERT-LF | m38[76D8] |
| L7700 | IND_SM1-LF | m38[77C4] |
| L7750 | IND_SM-LF | m38[77B4] |
| L7800 | IND_3P_SM | m38[78B3] |
| L7900 | IND_3P_SM | m38[79C3] |
| L8000 | IND_3P_SM | m38[80C3] |
| L8100 | IND_IHLP | m38[81C3] |
| L8400 | IND_0402 | m38[84B7] |
| L8520 | IND_IHLP | m38[85C3] |
| L8585 | IND_4P_2COIL_SDQ12150-SM | m38[85A3] |
| L8715 | IND_0402 | m38[87A7] |
| L8725 | IND_0402 | m38[87A4] |
| L8910 | IND_0402 | m38[89D7] |
| L8915 | IND_0402 | m38[89D7] |
| L8960 | IND_0402 | m38[89D4] |
| L8965 | IND_0402 | m38[89D4] |
| L9010 | IND_0402 | m38[90D7] |
| L9015 | IND_0402 | m38[90D7] |
| L9060 | IND_0402 | m38[90D4] |
| L9065 | IND_0402 | m38[90D4] |
| L9120 | IND_0402 | m38[91B6] |
| L9125 | IND_0402 | m38[91B6] |
| L9130 | IND_0402 | m38[91B7] |
| L9135 | IND_0402 | m38[91A7] |
| L9140 | IND_0402 | m38[91A7] |
| L9300 | IND_0402 | m38[93C7] |
| L9305 | IND_0402 | m38[93C7] |
| L9310 | IND_0402 | m38[93C7] |
| L9315 | IND_0402 | m38[93C7] |
| L9320 | IND_0402 | m38[93B7] |
| L9325 | IND_0402 | m38[93B7] |
| L9330 | IND_0402 | m38[93B7] |
| L9345 | IND_0402 | m38[93B7] |
| L9400 | IND_SM | m38[94C6] |
| L9700 | FILTER_4P_SM | m38[97D7] |
| L9701 | FILTER_4P_SM | m38[97D7] |
| L9702 | FILTER_4P_SM | m38[97C7] |
| L9703 | FILTER_4P_SM | m38[97C7] |
| L9710 | IND_SM-1 | m38[97D5] |
| LED600 | LED_2.0X1.25MM-SM | m38[6A7] |
| LED601 | LED_2.0X1.25MM-SM | m38[6A8] |
| LED602 | LED_2.0X1.25MM-SM | m38[6A7] |
| LED2901 | LED_3X2MM-SM | m38[59D6] |
| LED3800 | LED_2.0X1.25MM-SM | m38[38B3] |
| LED4300 | LED_2.0X1.25MM-SM | m38[43D2] |
| LED4301 | LED_2.0X1.25MM-SM | m38[43D2] |
| LED4302 | LED_2.0X1.25MM-SM | m38[43D1] |
| LED4303 | LED_2.0X1.25MM-SM | m38[43D1] |
| LED7900 | LED_2.0X1.25MM-SM | m38[79A4] |
| LED8000 | LED_2.0X1.25MM-SM | m38[80A4] |
| LED8100 | LED_2.0X1.25MM-SM | m38[81A4] |
| PP5E1 | PROBEPOINT_SM | m38[5B8] |
| PP5E2 | PROBEPOINT_SM | m38[5B8] |
| PP6A0 | PROBEPOINT_SM | m38[5A6] |
| PP6A1 | PROBEPOINT_SM | m38[5A6] |
| PP6A2 | PROBEPOINT_SM | m38[5A6] |
| PP6A3 | PROBEPOINT_SM | m38[5A6] |
| PP6A4 | PROBEPOINT_SM | m38[5A6] |
| PP6A5 | PROBEPOINT_SM | m38[5A6] |
| PP6A6 | PROBEPOINT_SM | m38[5A6] |
| PP6A7 | PROBEPOINT_SM | m38[5A6] |
| PP6A8 | PROBEPOINT_SM | m38[5A6] |
| PP6A9 | PROBEPOINT_SM | m38[5A6] |
| PP6B0 | PROBEPOINT_SM | m38[5A6] |
| PP6B1 | PROBEPOINT_SM | m38[5A6] |
| PP6B2 | PROBEPOINT_SM | m38[5A6] |
| PP6B3 | PROBEPOINT_SM | m38[5A6] |
| PP6B4 | PROBEPOINT_SM | m38[5A6] |
| PP6B5 | PROBEPOINT_SM | m38[5A6] |
| PP6B6 | PROBEPOINT_SM | m38[5A6] |
| PP6B7 | PROBEPOINT_SM | m38[5A6] |
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| PP6C3 | PROBEPOINT_SM | m38[5A6] |
| PP6C4 | PROBEPOINT_SM | m38[5C8] |
| PP6C5 | PROBEPOINT_SM | m38[5C8] |
| PP6C6 | PROBEPOINT_SM | m38[5C8] |
| PP6C7 | PROBEPOINT_SM | m38[5C8] |
| PP6C8 | PROBEPOINT_SM | m38[5C8] |
| PP6D0 | PROBEPOINT_SM | m38[5C8] |
| PP6D1 | PROBEPOINT_SM | m38[5C8] |
| PP6D2 | PROBEPOINT_SM | m38[5B8] |
| PP6D3 | PROBEPOINT_SM | m38[5B8] |
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| PP6D7 | PROBEPOINT_SM | m38[5B8] |
| PP6D8 | PROBEPOINT_SM | m38[5B8] |
| PP6D9 | PROBEPOINT_SM | m38[5B8] |
| PP6E0 | PROBEPOINT_SM | m38[5B8] |
| PP6E1 | PROBEPOINT_SM | m38[5B6] |
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| PP700 | TP_SM-TP50-TOP | m38[5D3] |
| PP701 | TP_SM-TP50-TOP | m38[5D3] |
| PP702 | TP_SM-TP50-TOP | m38[5D3] |
| PP1200 | TP_SM-TP50-TOP | m38[5D3] |
| PP1201 | TP_SM-TP50-TOP | m38[5D3] |
| PP1202 | TP_SM-TP50-TOP | m38[5D3] |
| PP2800 | TP_SM-TP50-TOP | m38[5C3] |
| PP2801 | TP_SM-TP50-TOP | m38[5C3] |
| PP2802 | TP_SM-TP50-TOP | m38[5C3] |
| PP4100 | PROBEPOINT_SM | m38[5D4] |
| PP4101 | PROBEPOINT_SM | m38[5D4] |
| PP8400 | PROBEPOINT_SM | m38[5C5] |
| PP8401 | PROBEPOINT_SM | m38[5C5] |
| PP8700 | PROBEPOINT_SM | m38[5D5] |
| PP8701 | PROBEPOINT_SM | m38[5D5] |
| PP8702 | PROBEPOINT_SM | m38[5D5] |
| PP8703 | PROBEPOINT_SM | m38[5D5] |
| PP8704 | PROBEPOINT_SM | m38[5D5] |
| PP8705 | PROBEPOINT_SM | m38[5D5] |
| PP8706 | PROBEPOINT_SM | m38[5D5] |
| PP8707 | PROBEPOINT_SM | m38[5D5] |
| PP8708 | PROBEPOINT_SM | m38[5D5] |
| PP8709 | PROBEPOINT_SM | m38[5D5] |
| PP8710 | PROBEPOINT_SM | m38[5D5] |
| PP8711 | PROBEPOINT_SM | m38[5D5] |
| PP8712 | PROBEPOINT_SM | m38[5D5] |
| PP8713 | PROBEPOINT_SM | m38[5D5] |
| PP8714 | PROBEPOINT_SM | m38[5D5] |
| PP8715 | PROBEPOINT_SM | m38[5D5] |
| PP8716 | PROBEPOINT_SM | m38[5D5] |
| PP8720 | PROBEPOINT_SM | m38[5D5] |
| PP8721 | PROBEPOINT_SM | m38[5D5] |
| PP8722 | PROBEPOINT_SM | m38[5D5] |
| PP8723 | PROBEPOINT_SM | m38[5C5] |
| PP8724 | PROBEPOINT_SM | m38[5C5] |
| PP8725 | PROBEPOINT_SM | m38[5C5] |
| PP8726 | PROBEPOINT_SM | m38[5C5] |
| PP8727 | PROBEPOINT_SM | m38[5C5] |
| PP8728 | PROBEPOINT_SM | m38[5C5] |
| PP8729 | PROBEPOINT_SM | m38[5C5] |
| PP8730 | PROBEPOINT_SM | m38[5C5] |
| PP8731 | PROBEPOINT_SM | m38[5C5] |
| PP8732 | PROBEPOINT_SM | m38[5C5] |
| PP8733 | PROBEPOINT_SM | m38[5C5] |
| PP8734 | PROBEPOINT_SM | m38[5C5] |
| PP8735 | PROBEPOINT_SM | m38[5C5] |
| PP8736 | PROBEPOINT_SM | m38[5C5] |
| PP8900 | PROBEPOINT_SM | m38[5B5] |
| PP8901 | PROBEPOINT_SM | m38[5B5] |
| PP8902 | PROBEPOINT_SM | m38[5B5] |
| PP8903 | PROBEPOINT_SM | m38[5B5] |
| PP8904 | PROBEPOINT_SM | m38[5B5] |
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| PP8912 | PROBEPOINT_SM | m38[5B5] |
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| PP8915 | PROBEPOINT_SM | m38[5A5] |
| PP8916 | PROBEPOINT_SM | m38[5A5] |
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| PP8925 | PROBEPOINT_SM | m38[5A5] |
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| PP8931 | PROBEPOINT_SM | m38[5A5] |
| PP8932 | PROBEPOINT_SM | m38[5A5] |
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| PP8935 | PROBEPOINT_SM | m38[5A5] |
| PP8936 | PROBEPOINT_SM | m38[5A5] |
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| PP9001 | PROBEPOINT_SM | m38[5B4] |
| PP9002 | PROBEPOINT_SM | m38[5B4] |
| PP9003 | PROBEPOINT_SM | m38[5B4] |
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| PP9007 | PROBEPOINT_SM | m38[5B4] |
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| PP9014 | PROBEPOINT_SM | m38[5A4] |
| PP9015 | PROBEPOINT_SM | m38[5A4] |
| PP9016 | PROBEPOINT_SM | m38[5A4] |
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| PP9022 | PROBEPOINT_SM | m38[5A4] |
| PP9023 | PROBEPOINT_SM | m38[5A4] |
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| PP9025 | PROBEPOINT_SM | m38[5A4] |
| PP9026 | PROBEPOINT_SM | m38[5A4] |
| PP9027 | PROBEPOINT_SM | m38[5A4] |
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| PP9031 | PROBEPOINT_SM | m38[5A4] |
| PP9032 | PROBEPOINT_SM | m38[5A4] |
| PP9033 | PROBEPOINT_SM | m38[5A4] |
| PP9034 | PROBEPOINT_SM | m38[5A4] |
| PP9035 | PROBEPOINT_SM | m38[5A4] |
| PP9036 | PROBEPOINT_SM | m38[5A4] |
| Q4200 | TRA_2N7002_SOT23-LF | m38[42D2] |
| Q4201 | TRA_PBSS5540Z_SOT223 | m38[42C6] |
| Q4400 | TRA_SINGLE_MOSFET_NCHN_SOT23 | m38[44A7] |
| Q5900 | TRA_FDV301N_SOT23-LF | m38[59D7] |
| Q5901 | TRA_2N7002DW_SOT-363 | m38[59C7 59C7] |
| Q5902 | TRA_2N7002DW_SOT-363 | m38[59B1 59B2] |
| Q5903 | TRA_DUAL_MMDT3904_SOT-363-LF | m38[59A2 59A1] |
| Q5910 | TRA_SINGLE_MOSFET_PCHN_SOT-23 | m38[59A4] |
| Q5911 | TRA_2N7002_SOT23-LF | m38[59A4] |
| Q6500 | TRA_NTHS5443T1_1206A-03-LF | m38[65D4] |
| Q6502 | TRA_2N7002DW_SOT-363 | m38[65D8 65D6] |
| Q6503 | TRA_NTHS5443T1_1206A-03-LF | m38[65B4] |
| Q6505 | TRA_2N7002DW_SOT-363 | m38[65C8 65B6] |
| Q6600 | TRA_NTHS5443T1_1206A-03-LF | m38[66D4] |
| Q6602 | TRA_2N7002DW_SOT-363 | m38[66D8 66C5] |
| Q7200 | TRA_2N7002DW_SOT-363 | m38[72B6 72B7] |
| Q7400 | TRA_2N7002DW_SOT-363 | m38[74B3 74B3] |
| Q7401 | TRA_2N7002_SOT23-LF | m38[74C3] |
| Q7402 | TRA_2N7002DW_SOT-363 | m38[74B2 74B2] |
| Q7500 | TRA_HAT2168H_LFPAK | m38[75D3] |
| Q7501 | TRA_HAT2165H_LFPAK | m38[75D3] |
| Q7502 | TRA_HAT2168H_LFPAK | m38[75C3] |
| Q7503 | TRA_HAT2165H_LFPAK | m38[75B3] |
| Q7504 | TRA_HAT2165H_LFPAK | m38[75D3] |
| Q7505 | TRA_HAT2165H_LFPAK | m38[75B3] |
| Q7570 | TRA_HAT2168H_LFPAK | m38[75D2] |
| Q7572 | TRA_HAT2168H_LFPAK | m38[75C3] |
| Q7639 | TRA_2N7002_SOT23-LF | m38[76A7] |
| Q7640 | TRA_SINGLE_MOSFET_PCHN_SOT-23 | m38[76A6] |
| Q7641 | TRA_FDC796N_SUPERSOT-6 | m38[76A5] |
| Q7642 | TRA_FDC796N_SUPERSOT-6 | m38[76A4] |
| Q7701 | TRA_SI3446DV_TSOP-LF | m38[77A3] |
| Q7703 | TRA_2N7002DW_SOT-363 | m38[77C7 77D7] |
| Q7799 | TRA_2N7002_SOT23-LF | m38[77A6] |
| Q7800 | TRA_NTD60N02R_CASE369-LF | m38[78C4] |
| Q7801 | TRA_NTD60N02R_CASE369-LF | m38[78B4] |
| Q7802 | TRA_2N7002_SOT23-LF | m38[78B7] |
| Q7900 | TRA_NTD60N02R_CASE369-LF | m38[79D4] |
| Q7901 | TRA_NTD60N02R_CASE369-LF | m38[79C4] |
| Q7902 | TRA_2N7002_SOT23-LF | m38[79C7] |
| Q8000 | TRA_NTD60N02R_CASE369-LF | m38[80D4] |
| Q8001 | TRA_NTD60N02R_CASE369-LF | m38[80C4] |
| Q8003 | TRA_2N7002_SOT23-LF | m38[80C7] |
| Q8102 | TRA_NTD60N02R_CASE369-LF | m38[81D4] |

| Ref | Part | Location |
|---|---|---|
| Q8103 | TRA_NTD60N02R_CASE369-LF | m38[81C4] |
| Q8104 | TRA_2N7002_SOT23-LF | m38[81C7] |
| Q8300 | TRA_IRF7413_SO-8 | m38[83C4] |
| Q8301 | TRA_IRF7413_SO-8 | m38[83B4] |
| Q8302 | TRA_2N7002DW_SOT-363 | m38[83B5] |
| Q8303 | TRA_2N7002DW_SOT-363 | m38[83C5] |
| Q8520 | TRA_HAT2168H_LFPAK | m38[85D4] |
| Q8521 | TRA_HAT2165H_LFPAK | m38[85C4] |
| Q8522 | TRA_HAT2165H_LFPAK | m38[85C5] |
| Q8523 | TRA_2N7002DW_SOT-363 | m38[85B3 85B2] |
| Q8554 | TRA_2N7002_SOT23-LF | m38[85A8] |
| Q8570 | TRA_2N7002_SOT23-LF | m38[85A5] |
| Q8575 | TRA_FDC796N_SUPERSOT-6 | m38[85B6] |
| Q8576 | TRA_FDC796N_SUPERSOT-6 | m38[85A2] |
| Q9400 | TRA_SI3443DV_TSOP-LF | m38[94C7] |
| Q9401 | TRA_2N7002_SOT23-LF | m38[94C7] |
| Q9711 | TRA_2N7002DW_SOT-363 | m38[97D2 97C2] |
| R75A0 | RES_402 | m38[75C7] |
| R85A0 | RES_402 | m38[85D1] |
| R600 | RES_603 | m38[6A7] |
| R601 | RES_402 | m38[6D8] |
| R602 | RES_603 | m38[6A8] |
| R603 | RES_402 | m38[6B1] |
| R605 | RES_603 | m38[6A7] |
| R611 | RES_402 | m38[6B7] |
| R612 | RES_402 | m38[6B7] |
| R614 | RES_402 | m38[6B7] |
| R615 | RES_402 | m38[6B7] |
| R616 | RES_402 | m38[6A7] |
| R617 | RES_402 | m38[6A7] |
| R618 | RES_402 | m38[6C7] |
| R619 | RES_402 | m38[6B7] |
| R0702 | RES_402 | m38[7D6] |
| R0703 | RES_402 | m38[7C6] |
| R0704 | RES_402 | m38[7C5] |
| R0705 | RES_402 | m38[7B4] |
| R0706 | RES_402 | m38[7B5] |
| R0707 | RES_402 | m38[7A4] |
| R0712 | RES_402 | m38[7A3] |
| R0716 | RES_402 | m38[7B1] |
| R0717 | RES_402 | m38[7B1] |
| R0718 | RES_402 | m38[7B1] |
| R0719 | RES_402 | m38[7B1] |
| R0720 | RES_402 | m38[7B7] |
| R0721 | RES_402 | m38[7B7] |
| R0722 | RES_402 | m38[7A7] |
| R0730 | RES_402 | m38[7A4] |
| R0802 | RES_402 | m38[8B7] |
| R0803 | RES_402 | m38[8A7] |
| R1000 | RES_402 | m38[10D3] |
| R1001 | RES_402 | m38[10D3] |
| R1002 | RES_402 | m38[10C6] |
| R1005 | RES_402 | m38[10D3] |
| R1017 | RES_402 | m38[10C6] |
| R1018 | RES_402 | m38[10B6] |
| R1019 | RES_402 | m38[10B6] |
| R1100 | RES_402 | m38[11B5] |
| R1101 | RES_402 | m38[11C5] |
| R1102 | RES_402 | m38[11B4] |
| R1103 | RES_402 | m38[11C5] |
| R1104 | RES_402 | m38[11B5] |
| R1106 | RES_402 | m38[11A3] |
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| R1211 | RES_402 | m38[12C3] |
| R1220 | RES_402 | m38[12B7] |
| R1221 | RES_402 | m38[12B7] |
| R1225 | RES_402 | m38[12B7] |
| R1226 | RES_402 | m38[12B7] |
| R1230 | RES_402 | m38[12A7] |
| R1231 | RES_402 | m38[12A7] |
| R1235 | RES_402 | m38[12A7] |
| R1236 | RES_402 | m38[12A7] |
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| R1410 | RES_402 | m38[14C3] |
| R1411 | RES_402 | m38[14C3] |
| R1420 | RES_402 | m38[14B6] |
| R1430 | RES_402 | m38[14B6] |
| R1440 | RES_402 | m38[14D6] |
| R1441 | RES_402 | m38[14D6] |
| R1975 | RES_402 | m38[19A4] |
| R1980 | RES_402 | m38[19B7] |
| R1981 | RES_402 | m38[19B7] |
| R1982 | RES_402 | m38[19B8] |
| R1983 | RES_402 | m38[19B8] |
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| R2059 | RES_402 | m38[20B4] |
| R2060 | RES_402 | m38[20A4] |
| R2075 | RES_402 | m38[20C7] |
| R2077 | RES_402 | m38[20B7] |
| R2079 | RES_402 | m38[20B7] |
| R2085 | RES_402 | m38[20C4] |
| R2100 | RES_402 | m38[21C3] |
| R2101 | RES_402 | m38[21C4] |
| R2105 | RES_402 | m38[21D6] |
| R2107 | RES_402 | m38[21C2] |
| R2108 | RES_402 | m38[21C2] |
| R2110 | RES_402 | m38[21C2] |
| R2194 | RES_402 | m38[21D4] |
| R2195 | RES_402 | m38[21C6] |
| R2196 | RES_402 | m38[21C6] |
| R2197 | RES_402 | m38[21C6] |
| R2198 | RES_402 | m38[21C6] |
| R2199 | RES_402 | m38[21C3] |
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| R2203 | RES_402 | m38[22C2] |
| R2204 | RES_402 | m38[22C2] |
| R2205 | RES_402 | m38[22C6] |
| R2206 | RES_402 | m38[22C5] |
| R2207 | RES_402 | m38[22C5] |
| R2211 | RES_402 | m38[22B3] |
| R2222 | RES_402 | m38[22D6] |
| R2223 | RES_402 | m38[22D6] |
| R2225 | RES_402 | m38[22D7] |
| R2226 | RES_402 | m38[22D5] |
| R2250 | RES_402 | m38[22D7] |
| R2251 | RES_402 | m38[22D6] |
| R2252 | RES_402 | m38[47B8] |
| R2253 | RES_402 | m38[47B8] |
| R2254 | RES_402 | m38[47B8] |
| R2255 | RES_402 | m38[22D7] |
| R2298 | RES_402 | m38[22B5] |
| R2299 | RES_402 | m38[22B5] |
| R2302 | RES_402 | m38[23D3] |
| R2303 | RES_402 | m38[23D3] |
| R2305 | RES_402 | m38[23D3] |
| R2306 | RES_402 | m38[23B7] |
| R2307 | RES_402 | m38[23A7] |
| R2308 | RES_402 | m38[23B7] |
| R2309 | RES_402 | m38[23A7] |
| R2310 | RES_402 | m38[23A7] |
| R2311 | RES_402 | m38[23A7] |
| R2313 | RES_402 | m38[23A7] |
| R2314 | RES_402 | m38[23A7] |
| R2316 | RES_402 | m38[23D7] |
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| R2320 | RES_402 | m38[23D7] |
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| R2327 | RES_402 | m38[23D6] |
| R2343 | RES_402 | m38[23D1] |
| R2388 | RES_402 | m38[23A3] |
| R2389 | RES_402 | m38[38D5] |
| R2390 | RES_402 | m38[23B3] |
| R2395 | RES_402 | m38[23D7] |
| R2396 | RES_402 | m38[23D6] |
| R2397 | RES_402 | m38[23D6] |
| R2398 | RES_402 | m38[23D8] |
| R2399 | RES_402 | m38[23C1] |
| R2500 | RES_603 | m38[25A8] |
| R2501 | RES_402 | m38[25C8] |
| R2502 | RES_402 | m38[25D8] |
| R2600 | RES_402 | m38[26C7] |
| R2606 | RES_402 | m38[26C7] |
| R2607 | RES_402 | m38[26C8] |
| R2609 | RES_402 | m38[26D7] |
| R2611 | RES_402 | m38[26D5] |
| R2612 | RES_402 | m38[26D5] |
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| R2625 | RES_402 | m38[26D2] |
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| R2632 | RES_402 | m38[26D2] |
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| R2697 | RES_402 | m38[26C3] |
| R2698 | RES_402 | m38[26C5] |
| R2699 | RES_402 | m38[26C5] |
| R2718 | RES_402 | m38[27B7] |
| R2719 | RES_402 | m38[27B7] |
| R2800 | RES_402 | m38[28C7] |
| R2801 | RES_402 | m38[28C7] |
| R2900 | RES_402 | m38[29A3] |
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| R3009 | RES_402 | m38[30D4] |
| R3011 | RES_402 | m38[30C4] |
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| R3035 | RES_402 | m38[30B4] |
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| R3101 | RES_402 | m38[31C5] |
| R3300 | RES_402 | m38[33B6] |
| R3301 | RES_402 | m38[33B7] |
| R3302 | RES_402 | m38[33D4] |
| R3303 | RES_402 | m38[33C4] |
| R3304 | RES_402 | m38[33C7] |
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| R3401 | RES_402 | m38[34B5] |
| R3402 | RES_402 | m38[34B5] |
| R3403 | RES_402 | m38[34C5] |
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| R3430 | RES_402 | m38[34C1] |
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| R8198 | RES_402 | m38[81A5] |
| R8199 | RES_402 | m38[81A5] |
| R8300 | RES_402 | m38[83B4] |
| R8301 | RES_402 | m38[83C5] |
| R8302 | RES_402 | m38[83B5] |
| R8303 | RES_402 | m38[83C4] |
| R8495 | RES_402 | m38[84A2] |
| R8496 | RES_402 | m38[84A2] |
| R8497 | RES_402 | m38[84A2] |
| R8502 | RES_402 | m38[85D6] |
| R8503 | RES_402 | m38[85D7] |
| R8504 | RES_402 | m38[85D7] |
| R8505 | RES_402 | m38[85C7] |
| R8506 | RES_402 | m38[85C8] |
| R8507 | RES_402 | m38[85D7] |
| R8508 | RES_402 | m38[85C7] |
| R8510 | RES_402 | m38[85C5] |
| R8521 | RES_402 | m38[85C3] |
| R8522 | RES_402 | m38[85C3] |
| R8523 | RES_402 | m38[85C3] |
| R8524 | RES_402 | m38[85B3] |
| R8525 | RES_402 | m38[85B3] |
| R8526 | RES_402 | m38[85B3] |
| R8540 | RES_402 | m38[85C2] |
| R8554 | RES_402 | m38[85B7] |
| R8555 | RES_402 | m38[85B6] |
| R8556 | RES_402 | m38[85B6] |
| R8560 | RES_402 | m38[85A6] |
| R8561 | RES_402 | m38[85A7] |
| R8570 | RES_402 | m38[85A5] |
| R8585 | RES_603 | m38[85A3] |
| R8586 | RES_402 | m38[85A3] |
| R8588 | RES_402 | m38[85C5] |
| R8590 | RES_402 | m38[85C3] |
| R8591 | RES_402 | m38[85D3] |
| R8592 | RES_402 | m38[85D2] |
| R8593 | RES_402 | m38[85D3] |
| R8594 | RES_402 | m38[85D3] |
| R8596 | RES_402 | m38[85D3] |
| R8597 | THERMISTER_0603-LF | m38[85D3] |
| R8598 | RES_402 | m38[85D2] |
| R8599 | RES_1206 | m38[85C4] |
| R8630 | RES_603 | m38[86C7] |
| R8710 | RES_402 | m38[87B8] |
| R8711 | RES_402 | m38[87A8] |
| R8712 | RES_402 | m38[87B7] |
| R8713 | RES_402 | m38[87A7] |
| R8720 | RES_402 | m38[87B4] |
| R8721 | RES_402 | m38[87A4] |
| R8722 | RES_402 | m38[87B4] |
| R8723 | RES_402 | m38[87A4] |
| R8730 | RES_402 | m38[87A3] |
| R8731 | RES_402 | m38[87A3] |
| R8732 | RES_402 | m38[87A3] |
| R8733 | RES_402 | m38[87A1] |
| R8800 | RES_603 | m38[88D7] |
| R8801 | RES_402 | m38[88A7] |
| R8802 | RES_402 | m38[88D4] |
| R8803 | RES_402 | m38[88D4] |
| R8804 | RES_402 | m38[88D4] |
| R8805 | RES_402 | m38[88C4] |
| R8806 | RES_402 | m38[88C4] |
| R8807 | RES_402 | m38[88C4] |
| R8808 | RES_402 | m38[88C4] |
| R8809 | RES_402 | m38[88C4] |
| R8810 | RES_402 | m38[88C4] |
| R8811 | RES_402 | m38[88B4] |
| R8812 | RES_402 | m38[88C4] |
| R8813 | RES_402 | m38[88D4] |
| R8830 | RES_402 | m38[88B4] |
| R8831 | RES_402 | m38[88B4] |
| R8832 | RES_402 | m38[88B4] |
| R8833 | RES_402 | m38[88B4] |
| R8850 | RES_402 | m38[88B4] |
| R8930 | RES_402 | m38[89C7] |
| R8931 | RES_402 | m38[89C7] |
| R8932 | RES_402 | m38[89C7] |
| R8933 | RES_402 | m38[89C7] |
| R8940 | RES_402 | m38[89B8] |
| R8941 | RES_402 | m38[89B8] |
| R8942 | RES_402 | m38[89B7] |
| R8943 | RES_402 | m38[89B7] |
| R8944 | RES_402 | m38[89B7] |
| R8945 | RES_402 | m38[89B7] |
| R8946 | RES_402 | m38[89B7] |
| R8947 | RES_402 | m38[89B7] |
| R8948 | RES_402 | m38[89A7] |
| R8949 | RES_402 | m38[89A7] |
| R8980 | RES_402 | m38[89C4] |
| R8981 | RES_402 | m38[89C4] |
| R8982 | RES_402 | m38[89C4] |
| R8983 | RES_402 | m38[89C4] |
| R8990 | RES_402 | m38[89B5] |
| R8991 | RES_402 | m38[89B4] |
| R8992 | RES_402 | m38[89B4] |
| R8993 | RES_402 | m38[89B4] |
| R8994 | RES_402 | m38[89B4] |
| R8995 | RES_402 | m38[89B4] |
| R8996 | RES_402 | m38[89B4] |
| R8997 | RES_402 | m38[89B4] |
| R8998 | RES_402 | m38[89A4] |
| R8999 | RES_402 | m38[89A4] |
| R9030 | RES_402 | m38[90C7] |
| R9031 | RES_402 | m38[90C7] |
| R9032 | RES_402 | m38[90C7] |
| R9033 | RES_402 | m38[90C7] |
| R9040 | RES_402 | m38[90B8] |
| R9041 | RES_402 | m38[90B8] |
| R9042 | RES_402 | m38[90B7] |
| R9043 | RES_402 | m38[90B7] |
| R9044 | RES_402 | m38[90B7] |
| R9045 | RES_402 | m38[90B7] |
| R9046 | RES_402 | m38[90B7] |
| R9047 | RES_402 | m38[90B7] |
| R9048 | RES_402 | m38[90A7] |
| R9049 | RES_402 | m38[90A7] |
| R9080 | RES_402 | m38[90C4] |
| R9081 | RES_402 | m38[90C4] |
| R9082 | RES_402 | m38[90C4] |
| R9083 | RES_402 | m38[90C4] |
| R9090 | RES_402 | m38[90B5] |
| R9091 | RES_402 | m38[90B4] |
| R9092 | RES_402 | m38[90B4] |
| R9093 | RES_402 | m38[90B4] |
| R9094 | RES_402 | m38[90B4] |
| R9095 | RES_402 | m38[90B4] |
| R9096 | RES_402 | m38[90B4] |
| R9097 | RES_402 | m38[90B4] |
| R9098 | RES_402 | m38[90A4] |
| R9099 | RES_402 | m38[90A4] |
| R9190 | RES_402 | m38[91D2] |
| R9191 | RES_402 | m38[91D2] |
| R9195 | RES_402 | m38[91A3] |
| R9202 | RES_402 | m38[92C6] |
| R9250 | RES_402 | m38[92C6] |
| R9350 | RES_402 | m38[93A8] |
| R9351 | RES_402 | m38[93A8] |
| R9370 | RES_402 | m38[93D1] |
| R9371 | RES_402 | m38[93D1] |
| R9372 | RES_402 | m38[93C1] |
| R9373 | RES_402 | m38[93C1] |
| R9390 | RES_402 | m38[93A1] |
| R9391 | RES_402 | m38[93A1] |
| R9400 | RES_402 | m38[94C8] |
| R9401 | RES_402 | m38[94C7] |
| R9410 | RES_402 | m38[94C6] |
| R9411 | RES_402 | m38[94C6] |
| R9450 | RES_402 | m38[94C2] |
| R9470 | RES_402 | m38[94C7] |
| R9472 | RES_402 | m38[94B3] |
| R9473 | RES_402 | m38[94B2] |
| R9474 | RES_402 | m38[94B2] |
| R9475 | RES_402 | m38[94B1] |
| R9490 | RES_805 | m38[94C6] |
| R9491 | RES_805 | m38[94D6] |
| R9700 | RES_402 | m38[97D7] |
| R9701 | RES_402 | m38[97D8] |
| R9702 | RES_402 | m38[97D7] |
| R9703 | RES_402 | m38[97D7] |
| R9704 | RES_402 | m38[97C7] |
| R9705 | RES_402 | m38[97C7] |
| R9706 | RES_402 | m38[97D8] |
| R9707 | RES_402 | m38[97C8] |
| R9708 | RES_402 | m38[97C7] |
| R9709 | RES_402 | m38[97C7] |
| R9710 | RES_402 | m38[97D2] |
| R9711 | RES_402 | m38[97D2] |
| R9712 | RES_402 | m38[97D2] |
| R9713 | RES_402 | m38[97C2] |
| R9714 | RES_402 | m38[97C2] |
| R9715 | RES_402 | m38[97C7] |
| R9716 | RES_402 | m38[97C8] |
| R9717 | RES_402 | m38[97C8] |
| R9720 | RES_402 | m38[97D1] |
| R9721 | RES_402 | m38[97D1] |
| R9722 | RES_402 | m38[97C2] |
| R9740 | RES_402 | m38[97A8] |
| R9741 | RES_402 | m38[97A8] |
| R9742 | RES_402 | m38[97A7] |
| R9750 | RES_402 | m38[97A3] |
| R9751 | RES_402 | m38[97A3] |
| RP2300 | RPAK4P_SM-LF | m38[23D5] |
| RP3000 | RPAK4P_SM-LF | m38[30B4 30C4 30D4 30D4] |
| RP3001 | RPAK4P_SM-LF | m38[30C4 30A4 30A4 30D4] |
| RP3002 | RPAK4P_SM-LF | m38[30A4 30A4 30A4 30D4] |
| RP3003 | RPAK4P_SM-LF | m38[30C4 30C4 30C4 30D4] |
| RP3004 | RPAK4P_SM-LF | m38[30C4 30C4 30D4] |
| RP3005 | RPAK4P_SM-LF | m38[30B4 30A4 30A4 30D4] |
| RP3006 | RPAK4P_SM-LF | m38[30B4 30B4 30A4 30D4] |
| RP3007 | RPAK4P_SM-LF | m38[30C4 30C4 30C4 30C4] |
| RP3008 | RPAK4P_SM-LF | m38[30C4 30C4 30C4 30C4] |
| RP3009 | RPAK4P_SM-LF | m38[30B4 30B4 30C4 30C4] |
| RP3010 | RPAK4P_SM-LF | m38[30B4 30B4 30B4 30B4] |
| RP3011 | RPAK4P_SM-LF | m38[30B4 30A4 30B4 30B4] |
| RP7200 | RPAK4P_SM-LF | m38[72A4] |
| SDF4700 | PCB_STANDOFF | m38[47A2] |
| SDF4701 | PCB_STANDOFF | m38[47A2] |
| SDF5300 | PCB_STANDOFF | m38[53A5] |
| SDF5301 | PCB_STANDOFF | m38[53A5] |
| SDF9400 | PCB_STANDOFF | m38[94B6] |
| SDF9401 | PCB_STANDOFF | m38[94A6] |
| SDF9402 | PCB_STANDOFF | m38[94A6] |
| SW2600 | SWI_TACT_4SM_EVQPH_SM-LF | m38[26C6] |
| SW5900 | SWI_TACT_4SM_EVQPH_SM-LF | m38[59D8] |
| SW5901 | SWI_TACT_4SM_EVQPH_SM-LF | m38[59B8] |
| U600 | 74LC125_TSSOP | m38[6A7 6B7 6B7 6C7] |
| U601 | SN74LVC1G04_SOT23-5 | m38[6C7] |
| U1000 | ADT7461_MSOP | m38[10D5] |
| U1200 | NB_945GM_BGA | m38[12D5] |
| U1200 | NB_945GM_BGA | m38[13D4] |
| U1200 | NB_945GM_BGA | m38[14D5] |
| U1200 | NB_945GM_BGA | m38[15D3 15D7] |
| U1200 | NB_945GM_BGA | m38[16D2 16C8] |
| U1200 | NB_945GM_BGA | m38[17D5] |
| U1200 | NB_945GM_BGA | m38[18D4 18D7] |
| U2100 | SB_ICH7M_BGA | m38[21D6] |
| U2100 | SB_ICH7M_BGA | m38[22B7 22D3] |
| U2100 | SB_ICH7M_BGA | m38[23D4] |
| U2100 | SB_ICH7M_BGA | m38[24D4 24D7] |
| U2601 | MC74VHC1G08_SOT23-5-LF | m38[26D5] |
| U2603 | SN74LVC1G04_SOT23-5 | m38[26A7] |
| U2698 | MC74VHC1G08_SOT23-5-LF | m38[26C4] |
| U2699 | MAX6816_SOT143 | m38[26C5] |
| U3100 | LREG_BD3533FVM_MSOP-8 | m38[31C5] |
| U3301 | CLK_GEN_CY284455_QFN | m38[33C5] |
| U4101 | 88E8053_QFN | m38[41D5] |
| U4102 | EEPROM_M24C08_SO8 | m38[41A3] |
| U4400 | FW32306_TQFP | m38[44D5] |
| U4700 | SWI_TPS2024_SOI | m38[47C8] |
| U5800 | SMC_H8S2116_BGA | m38[58A8 58C3 58C6 58D6] |
| U5900 | VDET_RN5VD_SOT23-5 | m38[59D8] |
| U5940 | VREF_REF3133_SOT23-3 | m38[59A4] |
| U5999 | COMPARATOR_LMV339_TSSOP | m38[59A6 59A6 59A8 59A8] |
| U6100 | MAX6695_UMAX | m38[61C4] |
| U6301 | FLASH_SST25VF016B_SOI_SOI | m38[63D3] |
| U6700 | TPM_TSSOP | m38[67C5] |
| U6800 | AUDIO_STAC9220_LQFP | m38[68D5] |
| U7200 | MAX9714_QFN-LF | m38[72C5] |
| U7500 | ISL6262_QFN | m38[75C6] |
| U7501 | INA138_SOT23-5-LF | m38[76D7] |
| U7600 | OPAMP_LMV2011_SOT23-5 | m38[76D3] |
| U7700 | LTC3411_MSOP-LF | m38[77D5] |
| U7710 | MC74VHC1G08_SOT23-5-LF | m38[77C7] |
| U7711 | MC74VHC1G08_SOT23-5-LF | m38[77B7] |
| U7712 | MC74VHC1G08_SOT23-5-LF | m38[77B7] |
| U7750 | LTC3412_TSSOP-LF | m38[77B5] |
| U7800 | ISL6549_QFN | m38[78C6] |
| U7900 | ISL6549_QFN | m38[79D6] |
| U7901 | COMPARATOR_LM339A_SOI-LF | m38[79A5] |
| U7901 | COMPARATOR_LM339A_SOI-LF | m38[80A4] |
| U7901 | COMPARATOR_LM339A_SOI-LF | m38[81A5] |
| U7910 | COMPARATOR_LM339A_SOI-LF | m38[79A3 79B3] |
| U7910 | COMPARATOR_LM339A_SOI-LF | m38[80B2] |
| U7910 | COMPARATOR_LM339A_SOI-LF | m38[81B3] |
| U8000 | ISL6549_QFN | m38[80D6] |
| U8100 | ISL6549_QFN | m38[81D6] |
| U8400 | ATI_M56P_BGA | m38[84C8 84D4] |
| U8400 | ATI_M56P_BGA | m38[86D4] |
| U8400 | ATI_M56P_BGA | m38[87D2 87D6] |
| U8400 | ATI_M56P_BGA | m38[91D4] |
| U8400 | ATI_M56P_BGA | m38[93C4] |
| U8500 | ISL6269_QFN | m38[85D6] |
| U8550 | FAN2558_SOT23-6-LF | m38[85B7] |
| U8560 | 74LVC1G125LF_SOT23-5 | m38[85A6] |
| U8580 | LT3483_TSOT23-6 | m38[85A4] |
| U8595 | OPAMP_LMV2011_SOT23-5 | m38[85D2] |
| U8900 | SGRAM_16MX32_GDDR3_136H_FBGA | m38[89D6 89B6] |
| U8950 | SGRAM_16MX32_GDDR3_136H_FBGA | m38[89D3 89B3] |
| U9000 | SGRAM_16MX32_GDDR3_136H_FBGA | m38[90D6 90B6] |
| U9050 | SGRAM_16MX32_GDDR3_136H_FBGA | m38[90D3 90B3] |
| U9470 | MC74VHC1G08_SOT23-5-LF | m38[94B2] |
| U9750 | 74AHC1G32_SM-LF | m38[97A4] |
| U9751 | 74AHC1G32_SM-LF | m38[97A4] |
| VR6800 | LREG_TPS79501_SOT223-6 | m38[68A4] |
| XC7200 | MTGHOLE | m38[72B3] |
| XW601 | SHORT_SM | m38[6C2] |
| XW602 | SHORT_SM | m38[6C2] |
| XW604 | SHORT_SM | m38[6A5] |
| XW605 | SHORT_SM | m38[6A5] |
| XW5800 | SHORT_SM | m38[58B3] |
| XW5900 | SHORT_SM | m38[59B1] |
| XW6800 | SHORT_SM | m38[74A4] |
| XW7201 | SHORT_SM | m38[72B2] |
| XW7300 | SHORT_SM | m38[73C5] |
| XW7400 | SHORT_SM | m38[74D2] |
| XW7500 | SHORT_SM | m38[75A6] |
| XW7501 | SHORT_SM | m38[75B2] |
| XW7502 | SHORT_SM | m38[75B1] |
| XW7503 | SHORT_SM | m38[75D2] |
| XW7504 | SHORT_SM | m38[75D1] |
| XW7598 | SHORT_SM | m38[76D7] |
| XW7700 | SHORT_SM | m38[77C4] |
| XW7750 | SHORT_SM | m38[77A5] |
| XW7800 | SHORT_SM | m38[78B6] |
| XW7900 | SHORT_SM | m38[79C6] |
| XW8000 | SHORT_SM | m38[80C6] |
| XW8100 | SHORT_SM | m38[81C6] |
| XW8500 | SHORT_SM | m38[85C6] |
| XW8715 | SHORT_SM | m38[87A7] |
| XW8725 | SHORT_SM | m38[87A4] |
| XW9135 | SHORT_SM | m38[91A4] |
| XW9140 | SHORT_SM | m38[91A4] |
| XW9300 | SHORT_SM | m38[93C7] |
| XW9305 | SHORT_SM | m38[93C7] |
| XW9310 | SHORT_SM | m38[93C7] |

| | | |
|---|---|---|
| XW9314 | SHORT_SM | m38[93C7] |
| XW9320 | SHORT_SM | m38[93B7] |
| XW9324 | SHORT_SM | m38[93B7] |
| XW9330 | SHORT_SM | m38[93B7] |
| XW9345 | SHORT_SM | m38[93A7] |
| Y2600 | CRYSTAL_4PIN_SM-LF | m38[26D8] |
| Y3301 | CRYSTAL_5X3.2-SM | m38[33C7] |
| Y4101 | CRYSTAL_4PIN_SM-3 | m38[41B5] |
| Y4400 | CRYSTAL_HC49-USMD | m38[44D2] |
| Y5800 | CRYSTAL_SM-3 | m38[59B8] |
| Y6700 | CRYSTAL_4PIN_SM-LF | m38[59B7] |
| ZH500 | HOLE_VIA | m38[5C1] |
| ZH501 | HOLE_VIA | m38[5C1] |
| ZH502 | HOLE_VIA | m38[5C1] |
| ZH503 | HOLE_VIA | m38[5C1] |
| ZH504 | HOLE_VIA | m38[5B1] |
| ZH505 | HOLE_VIA | m38[5B1] |
| ZH506 | HOLE_VIA | m38[5B1] |
| ZH507 | HOLE_VIA | m38[5B1] |
| ZH508 | HOLE_VIA | m38[5B1] |
| ZH509 | HOLE_VIA | m38[5B1] |
| ZH510 | HOLE_VIA | m38[5C1] |
| ZH511 | HOLE_VIA | m38[5C1] |
| ZH512 | HOLE_VIA | m38[5C1] |
| ZH513 | HOLE_VIA | m38[5C1] |
| ZH514 | HOLE_VIA | m38[5B1] |
| ZH515 | HOLE_VIA | m38[5B1] |
| ZH516 | HOLE_VIA | m38[5B1] |
| ZH517 | HOLE_VIA | m38[5B1] |
| ZH518 | HOLE_VIA | m38[5B1] |
| ZH519 | HOLE_VIA | m38[5B1] |
| ZH520 | HOLE_VIA | m38[5C1] |
| ZH521 | HOLE_VIA | m38[5C1] |
| ZH522 | HOLE_VIA | m38[5C1] |
| ZH523 | HOLE_VIA | m38[5C1] |
| ZH524 | HOLE_VIA | m38[5B1] |
| ZH525 | HOLE_VIA | m38[5B1] |
| ZH526 | HOLE_VIA | m38[5B1] |
| ZH527 | HOLE_VIA | m38[5B1] |
| ZH528 | HOLE_VIA | m38[5B1] |
| ZH529 | HOLE_VIA | m38[5B1] |
| ZH601 | MTGHOLE | m38[6A3] |
| ZH602 | MTGHOLE | m38[6A3] |
| ZH603 | MTGHOLE | m38[6A3] |
| ZH604 | MTGHOLE | m38[6B3] |
| ZH606 | MTGHOLE | m38[6A1] |
| ZH607 | MTGHOLE | m38[9D4] |
| ZH608 | MTGHOLE | m38[9D3] |
| ZH609 | MTGHOLE | m38[9D2] |
| ZH610 | MTGHOLE | m38[9D2] |